

Systematic Application of UCIS to Improve the Automation on Verification Closure

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Motivation – Verification, a language feat.?

- Despite SystemC language is one of the main drivers for virtual prototype development its verification features are rather limited:

Language	IEEE-1800 SystemVerilog	IEEE-1647 <i>e</i>	IEEE-1666 SystemC
Feature			
Coverage Facility	+++	+++	x
Assertions	+++	+++	x
RTPG	+++	+++	+ (SCV)
Verification Methodology	+++	+++	x ¹
TLM	++	+	+++
AOP	x	++	x
C-Software Simulation	simulator dependent	simulator dependent	+++

¹ not with OSCI simulator

- Previous work:
 - Establish a functional coverage and verification methodology for SC

- With this foundation,
lets improve the automation on verification closure in SC TBs

- Enhance the flow from coverage plan capture to functional coverage implementation within the testbench

- Basis:

- └ recently released

Unified Coverage Interoperability Standard (UCIS)

- Target Language/Simulator: SystemC Reference Simulator

- Motivation
 - Verification Features of HLDVLS
 - Abstraction and Automation for Verification Closure Productivity
- **UCIS to Improve the Automation on Verification Closure in SC**
 - UCIS and Verification Process
 - Steps in Detail
- Case Study
 - Tooling for UCIS with OSCI SystemC
- Lessons Learned
- Final Remarks

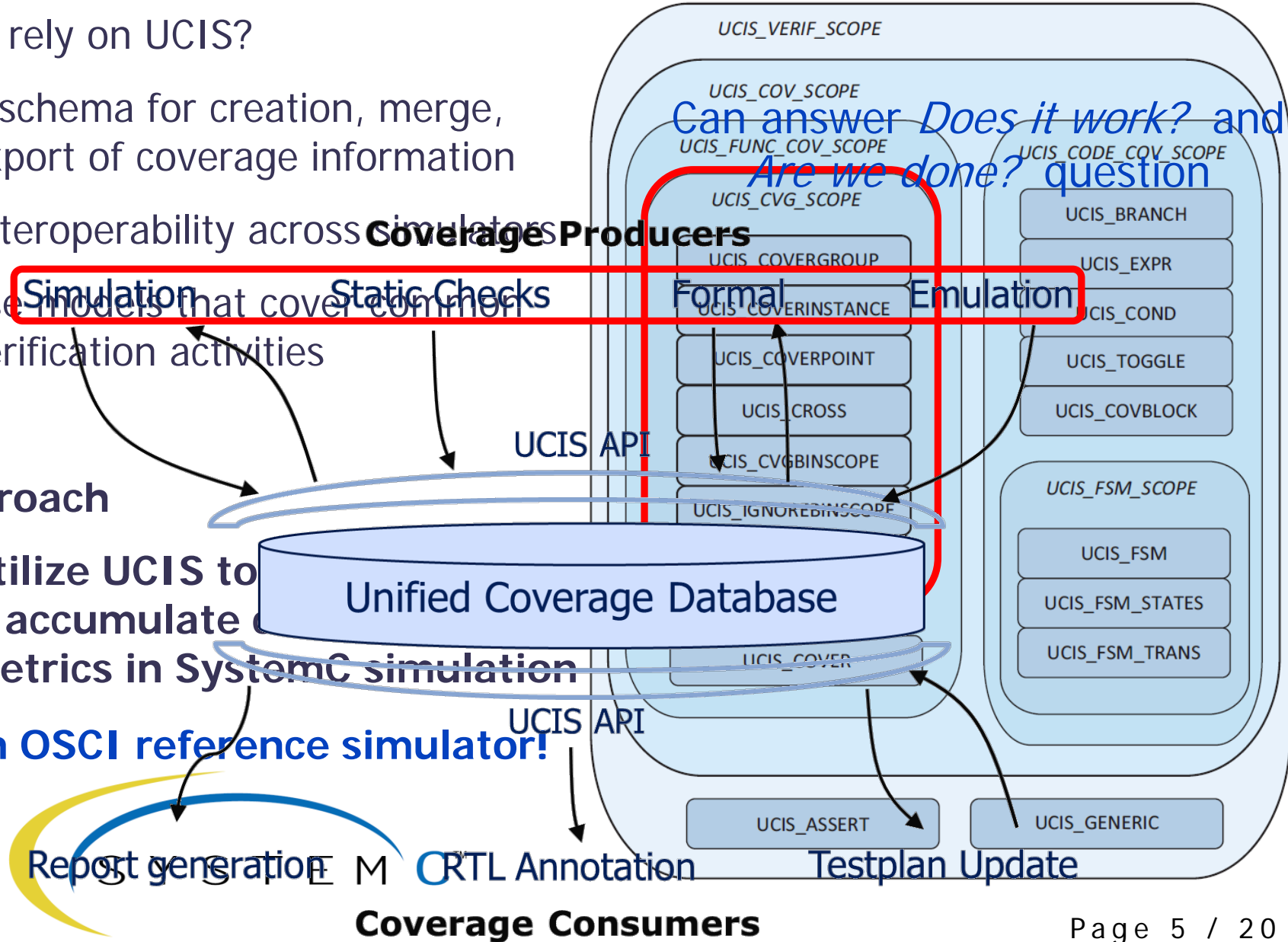
Unified Coverage Interoperability Standard

Why rely on UCIS?

- A schema for creation, merge, export of coverage information
- Interoperability across simulators
- use models that cover common verification activities

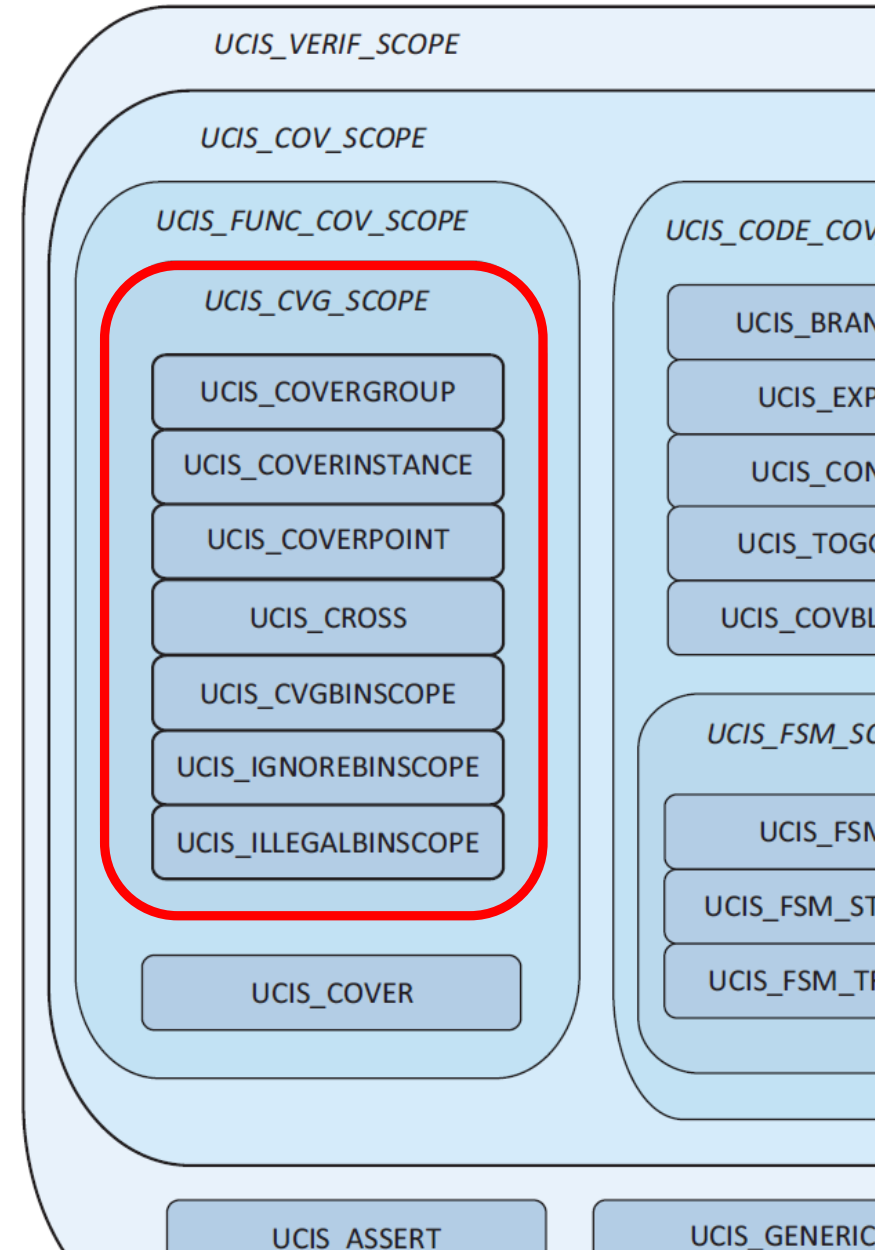
Approach

- Utilize UCIS to & accumulate metrics in SystemC simulation with OSCI reference simulator!



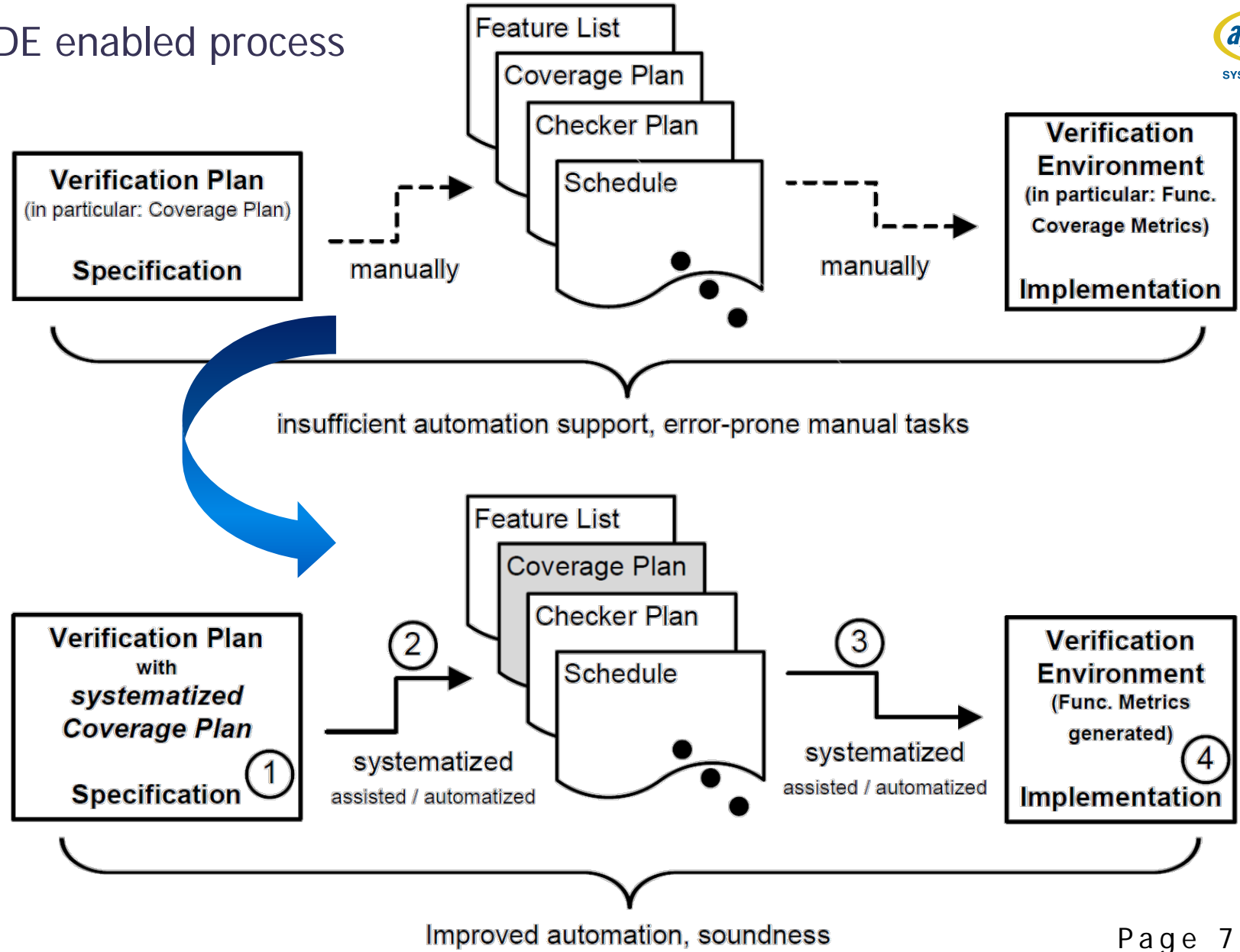
Challenges

- Accelera UCIS v1.0 Std.
 - API functions description
 - API header file
 - XML schema
- Consequently,
own API implementation needed!
- API = Setter / Getter / Advanced
- In this work, we focus on the functional coverage scope
UCIS_CVG_SCOPE



UCIS and Verification Process

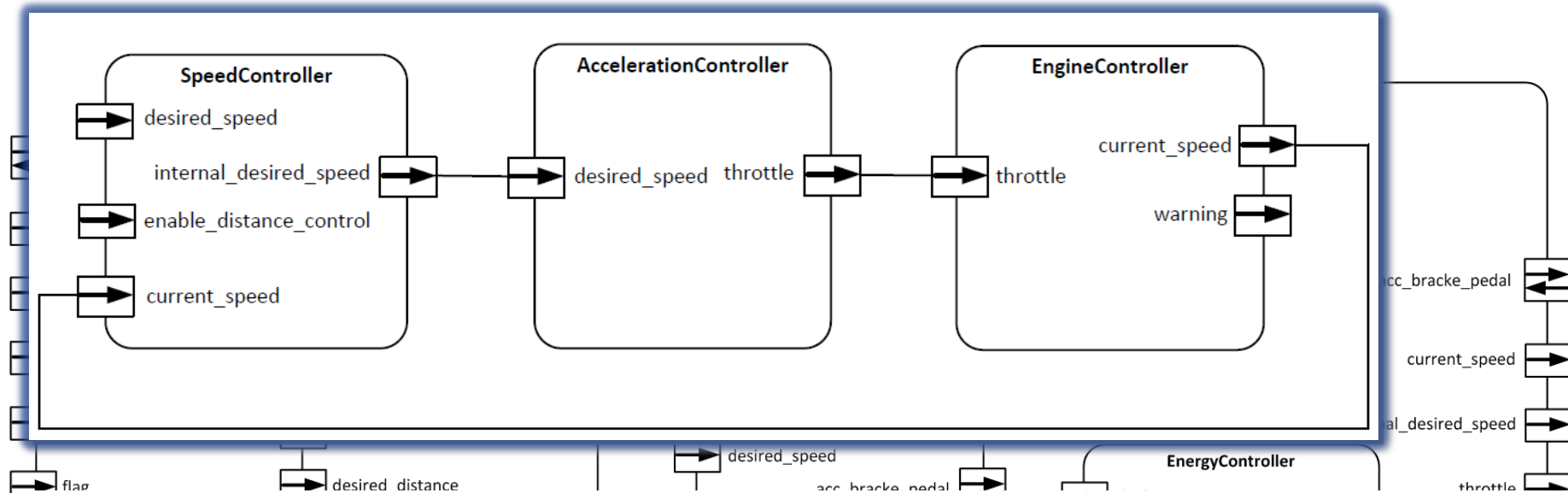
MDE enabled process



- 1) Capture cover plan data in spreadsheet style data model (language independent)
- 2) Transform cover plan data to UCIS metric model (in fact a template of the metric to generate)
- 3) Generate func. coverage metric skeletons from the UCIS metric model
- 4) Assisted completion of the verification environment skeletons



Architecture of ACC model



■ DUV has various I/O, functional coverage is a valuable metric to determine end of testing

■ Proceed the individual methodology steps

□ Build coverage plan

□ Use UCIS to automatize metric skeleton generation

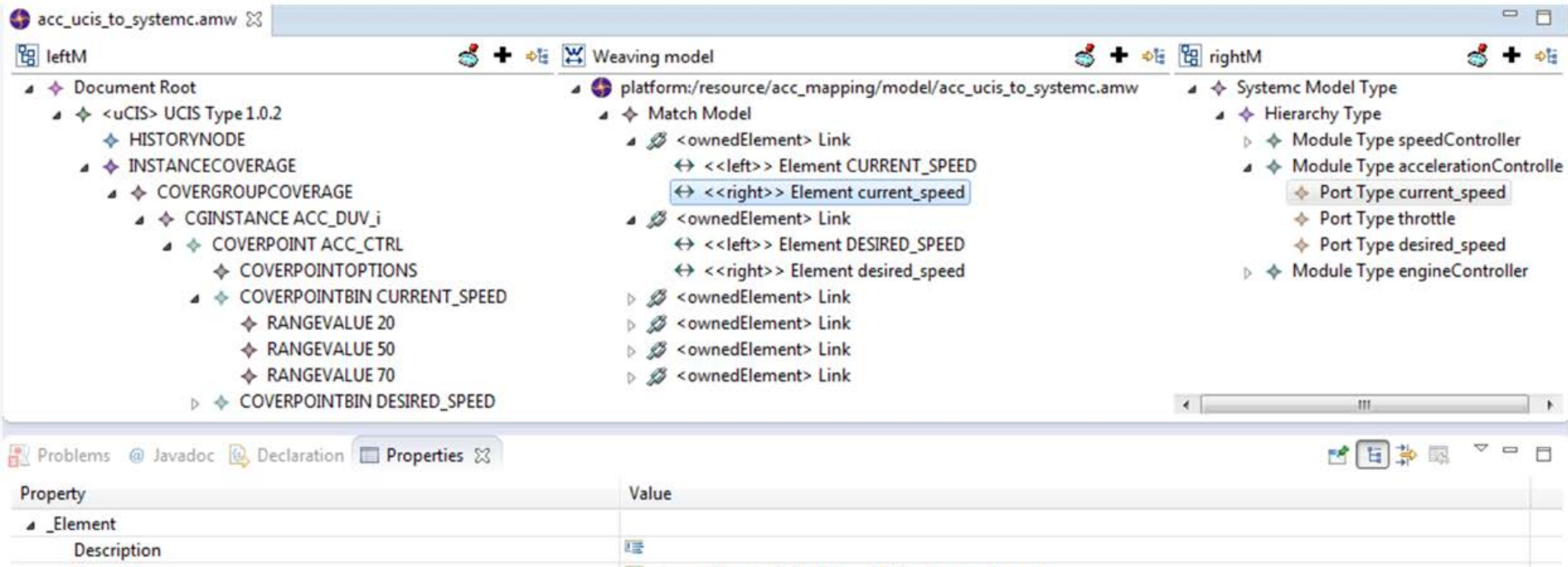
- Coverage Plan Spreadsheet structure
 - ~~Excel table~~ vs. custom coverage plan format
 - Usage of OMG Requirement Interchange Format (ReqIF)
 - Allows to define a custom „coverage plan spreadsheet“
 - MDE/Eclipse tooling compatible

■ Case Study Example

Name	Range	Type	Weight	Goal
desired_speed	[10:100]	BIN	1	100
current_speed	[0:100]	BIN	1	100
desired_distance	[10:30]	BIN	1	100
current_distance	[0:150]	BIN	1	100
enable_acc	[0:1]	BIN	1	100
enable_dist	[0:1]	BIN	1	100

Multi Pane Editor View

- Mapped model contain references to both UCIS metric template and design model



- A model of UCIS can be generated using MDE tooling, moreover API Setter/Getter functions can be generated

Case Study: Generated Metrics

```
1 // Init the factory
2 svm_pFac = svm_Factory::init();
3
4 // Set UCIS data model format
5 svm_pFac->setCoverageDb("acc_sim_cov.xml");
6
7 // Specify metric, covergroups
8 cv_pCG = svm_pFac->newCovergroups(cov_pCP, "ACC_DUV",
9     "ACC_DUV");
10 cv_pCP = svm_pFac->newCoverpoint("CURRENT_SPEED",
11     "CURRENT_SPEED");
12 cv_pCP->set_val("CURRENT_SPEED", 100);
13 cv_pCP->set_cov("CURRENT_SPEED", 100);
14 cv_pCP->set_cg("CURRENT_SPEED", cv_pCG);
15 (...)
16
17 // Specify metric, bin types
18 cv_pBa = svm_pFac->newBins(cov_pCP, "CURRENT_SPEED",
19     AUTOBINS);
20 cv_pBa << range(20, 49) << range(50, 69) << range(70,
21     100);
22 cv_pBa->connect(current_speed);
23
24 cv_pBb = svm_pFac->newBins(cov_pCP, "DESIRED_SPEED",
25     AUTOBINS);
26 cv_pBb << range(10, 49) << range(50, 69) << range(70,
27     100);
28 cv_pBb->connect(desired_speed);
29
30 (...)
```

- Stimuli = Computation Tree Method + Random
 - Tree-oriented decomposition of test scenarios into individual variable ranges
 - RTPG for the selected ranges

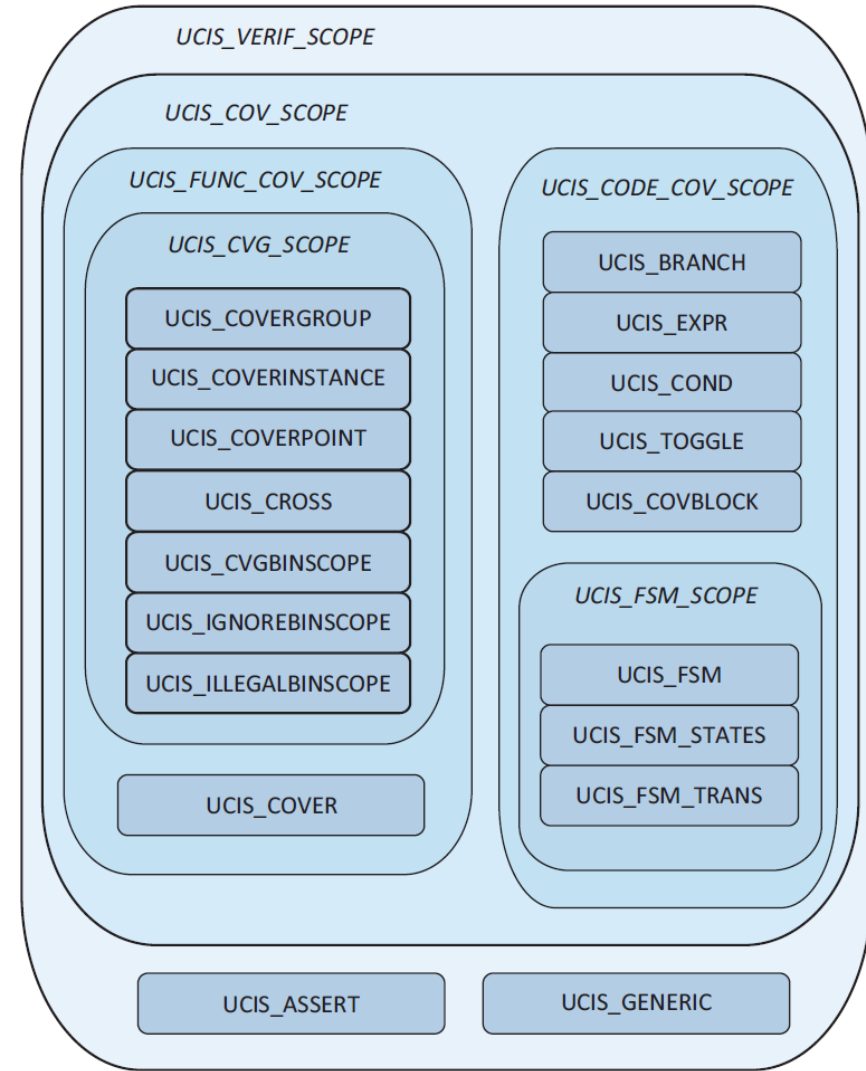
- Coverage
 - Usage of our functional coverage prototype for SystemC
 - Accumulation of hits w.r.t. metric

```
1  BIN: ACC_SPEED_CTRL:desired_speed::: 2014 Hits
2  BIN: ACC_SPEED_CTRL:current_speed::: 2189 Hits
3  BIN: ACC_SPEED_CTRL:desired_distance::: 2077 Hits
4  BIN: ACC_SPEED_CTRL:current_distance::: 2338 Hits
5  BIN: ACC_SPEED_CTRL:enable_ac::: 41 Hits
6  ...
```

- Output to UCIS format
 - Usage of generated Setter/Getter functions (w.r.t. schema)

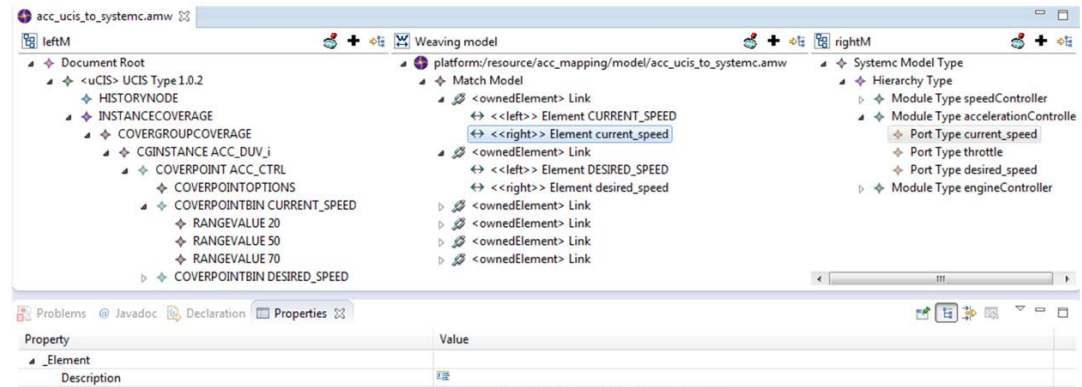
Example will be inserted asap

- „Missing“ reference API Writer
 - Effort to build Setter/Getter API for C/C#/... w.r.t. XML schema is low
 - Advanced API commands implementation requires in-depth expertise in UCIS and it's use models
- Slight inconsistencies of API and XML
 - Donation and glue residue
- A common standard format for *Does it work?* and *Are we done?*
 - A great thing ;-)



■ Eclipse Tooling Construction

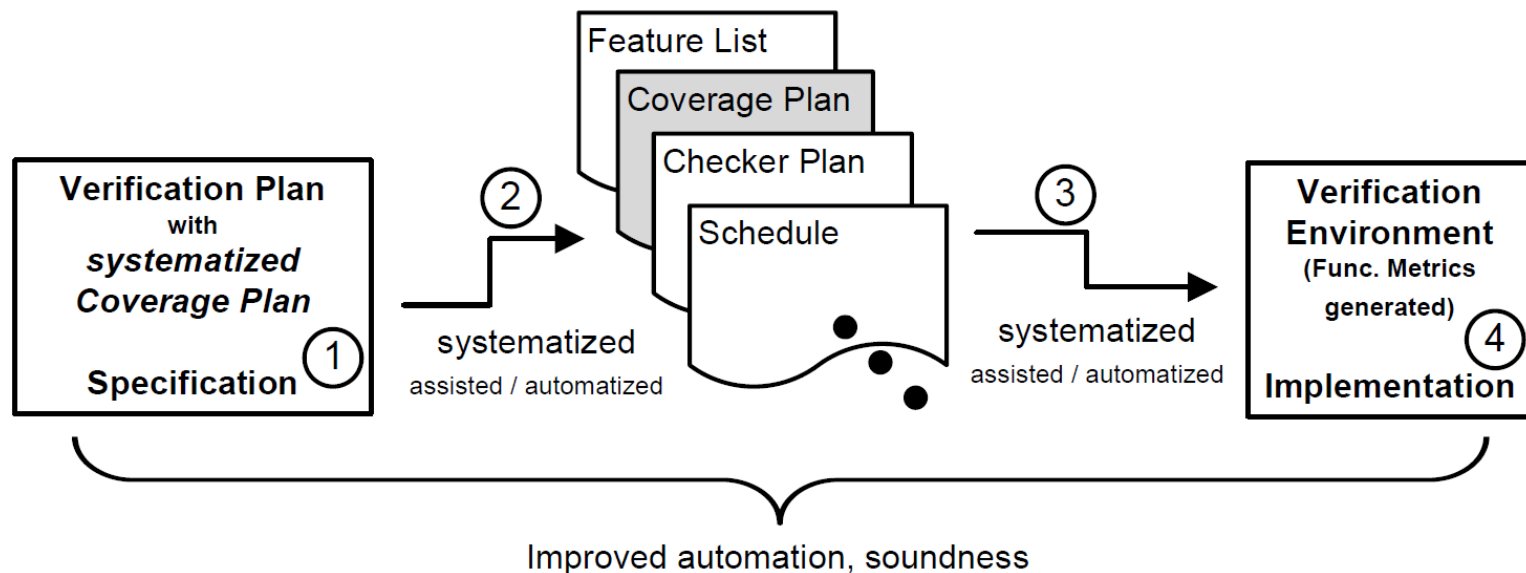
- the devil is in the details...
- ReqIF, EMF, EMF4CPP, specific Eclipse version requirements and dependencies
- MDE expertise definitely necessary



■ Eclipse Tools Usage

- Editors for coverage plan, UCIS, model weaving work
- BUT: domain-specific (EDA) view preferable

- We see potential for usage of UCIS in earlier phases of verification, in particular verification/coverage plan creation
- Building Setter/Getter API for UCIS is easy
- General: FC metric generation can avoid error-prone manual coding



- Accellera Organization Inc.:
Unified Coverage Interoperability Standard (UCIS)
Link: <http://www.accellera.org/downloads/standards/ucis>

- Marcio Oliviera, Christoph Kuznik, Wolfgang Mueller, Finn Haedicke, Hoang Le, Daniel Grosse, Rolf Drechsler, Wolfgang Ecker, Volkan Esen:
The System Verification Methodology for Advanced TLM Verification,
International Conference on Hardware/Software Codesign and System Synthesis (ISSS+CODES) 2012, Tampere, Finland, October 2012,
Link: <http://dl.acm.org/citation.cfm?doid=2380445.2380497>

- Marcio F. S. Oliveira, Christoph Kuznik, Wolfgang Mueller, Volkan Esen, Wolfgang Ecker:
Towards an Enhanced UVM for SystemC,
DVCON 2012, San Jose, February 2012,
Link: <http://adt.cs.upb.de/wolfgang/dvcon2012.pdf>

- Christoph Kuznik, Wolfgang Mueller: *A SystemC Based Library for Functional Coverage*,
DVCON 2011, San Jose, February 2011,
Link: <http://adt.cs.upb.de/wolfgang/dvcon2011.pdf>

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Thank you for your attention.

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