Innovative Technological Narratives Leveraging the Idea of Authenticity in a Human Being

Anna M. Ravitzki
Uri Feigin
Hagai Arbel









A New Way to Debug

- Vtool is a diverse team of experts Philosophers, sociologists, engineers, and designers.
- This is how we solve the diversified problem called: Bugs
- Just like the living that fight the dead.
- We made a revolutionary machine, with the purpose of creating an existential revolution.
- We call it Cogita. And it is the new way to debug it works!





What Debug Really Is

- Verification is a technological tool, for verifying technological innovations using a practical system.
- Without verification, there is no technology.
- Debug is the process of "identifying and removing errors".
- Debug consists of reading results, analyzing them, and concluding the root cause.
- Specifically, in most cases, debug uses simulation log files.





```
# UVM_INFO [monitor_axi_write_interface] @ 39530.0ns: => SKIP WRITE - curr_addr = 0xf383333f, wdata[0] = 0x0 (beat_counter: 71)
26657 # UVM_INFO [monitor_axi_write_interface] @ 39530.0ns: => SKIP WRITE - curr_addr = 0xf3833340, wdata[1] = 0x0 (beat_counter: 71)
26658 # UVM_INFO [monitor_axi_write_interface] @ 39530.0ns: => SKIP WRITE - curr_addr = 0xf3833341, wdata[2] = 0x0 (beat_counter: 71)
      # UVM INFO [monitor axi write interface] @ 39530.0ns: => SKIP WRITE - curr addr = 0xf3833342, wdata[3] = 0x0 (beat counter: 71)
      # UVM_INFO [monitor_axi_write_interface] @ 39530.0ns: => WRITE - curr_addr = 0xf3833343, wdata[4] = 0x3b (beat_counter: 71)
      # UVM_INFO [monitor_axi_write_interface] @ 39530.0ns: => *** curr_write_addr **** f3833194
26662 # UVM_INFO [monitor_axi_write_interface] @ 39530.0ns: => WRITE - curr_addr = 0xf3833344, wdata[5] = 0x75 (beat_counter: 71)
      # UVM_INFO [monitor_axi_write_interface] @ 39530.0ns: => *** curr_write_addr **** f3833195
26664 # UVM_INFO [monitor_axi_write_interface] @ 39530.0ns: => SKIP WRITE - curr_addr = 0xf3833345, wdata[6] = 0x0 (beat_counter: 71)
26665 # UVM_INFO [monitor_axi_write_interface] @ 39530.0ns: => SKIP WRITE - curr_addr = 0xf3833346, wdata[7] = 0x0 (beat_counter: 71)
26666 # UVM_INFO [monitor_axi_write_interface] @ 39580.0ns: => Received WDATA: 0x657f000000000000 (WSTRB: 0xc0)
      # UVM INFO [monitor axi write interface] @ 39580.0ns: => SKIP WRITE - curr addr = 0xf3833347, wdata[0] = 0x0 (beat counter: 72)
26668 # UVM_INFO [monitor_axi_write_interface] @ 39580.0ns: => SKIP WRITE - curr_addr = 0xf3833348, wdata[1] = 0x0 (beat_counter: 72)
      # UVM_INFO [monitor_axi_write_interface] @ 39580.0ns: => SKIP WRITE - curr_addr = 0xf3833349, wdata[2] = 0x0 (beat_counter: 72)
26670 # UVM_INFO [monitor_axi_write_interface] @ 39580.0ns: => SKIP WRITE – curr_addr = 0xf383334a, wdata[3] = 0x0 (beat_counter: 72)
26671 # UVM_INFO [monitor_axi_write_interface] @ 39580.0ns: => SKIP WRITE - curr_addr = 0xf383334b, wdata[4] = 0x0 (beat_counter: 72)
26672 # UVM INFO [monitor axi write interface] @ 39580.0ns: => SKIP WRITE - curr addr = 0xf383334c, wdata[5] = 0x0 (beat counter: 72)
      # UVM_INFO [monitor_axi_write_interface] @ 39580.0ns: => WRITE - curr_addr = 0xf383334d, wdata[6] = 0x7f (beat_counter: 72)
26674 # UVM_INFO [monitor_axi_write_interface] @ 39580.0ns: => *** curr_write_addr **** f3833196
      # UVM_INFO [monitor_axi_write_interface] @ 39580.0ns: => WRITE - curr_addr = 0xf383334e, wdata[7] = 0x65 (beat_counter: 72)
26676 # UVM_INFO [monitor_axi_write_interface] @ 39580.0ns: => *** curr_write_addr **** f3833197
      # UVM_INFO [monitor_axi_write_interface] @ 39620.0ns: => Received WDATA: 0xd4c0 (WSTRB: 0x3)
26678 # UVM_INFO [monitor_axi_write_interface] @ 39620.0ns: => WRITE - curr_addr = 0xf383334f, wdata[0] = 0xc0 (beat_counter: 73)
      # UVM_INFO [monitor_axi_write_interface] @ 39620.0ns: => *** curr_write_addr **** f3833198
      # UVM_INFO [monitor_axi_write_interface] @ 39620.0ns: => WRITE - curr_addr = 0xf3833350, wdata[1] = 0xd4 (beat_counter: 73)
      # UVM_INFO [monitor_axi_write_interface] @ 39620.0ns: => *** curr_write_addr **** f3833199
26682 # UVM_INFO [monitor_axi_write_interface] @ 39620.0ns: => SKIP WRITE - curr_addr = 0xf3833351, wdata[2] = 0x0 (beat_counter: 73)
      # UVM_INFO [monitor_axi_write_interface] @ 39620.0ns: => SKIP WRITE - curr_addr = 0xf3833352, wdata[3] = 0x0 (beat_counter: 73)
      # UVM INFO [monitor axi write interface] @ 39620.0ns: => SKIP WRITE - curr addr = 0xf3833353, wdata[4] = 0x0 (beat counter: 73)
      # UVM_INFO [monitor_axi_write_interface] @ 39620.0ns: => SKIP WRITE - curr_addr = 0xf3833354, wdata[5] = 0x0 (beat_counter: 73)
      # UVM_INFO [monitor_axi_write_interface] @ 39620.0ns: => SKIP WRITE - curr_addr = 0xf3833355, wdata[6] = 0x0 (beat_counter: 73)
       # UVM_INFO [monitor_axi_write_interface] @ 39620.0ns: => SKIP WRITE - curr_addr = 0xf3833356, wdata[7] = 0x0 (beat_counter: 73)
       # UVM_INFO [write_sys_axi_R_ap] @ 39640.0ns: => Entered in write_sys_axi_R_ap
       # UVM INFO [DEBUG] @ 39640.0ns: => SYS-AXI-READ-RESP RCVD:
       Name
                                    Type
                                                           Size Value
       rd_trans
                                    axi_txn
                                                                 @39302
         id
                                    integral
                                                           15
                                                                 'h0
         direction
                                    direction t
                                                           32
                                                                 READ
```





Log Files

- A log file may contain all the information that is required for understanding what happened in the test.
- But,
 - 1. It includes too much information.
 - 2. It is hard to search through.
 - 3. It is hard to remember.
- Log files are not ideal for humans to comprehend.





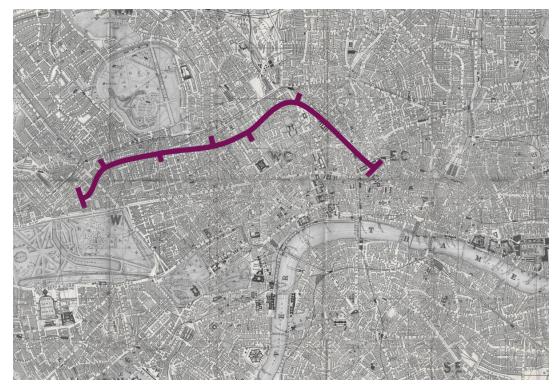
The London Tube - 19th Century







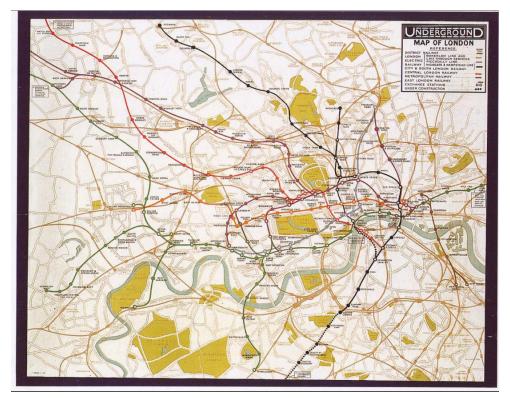
The London Tube Map - 1863







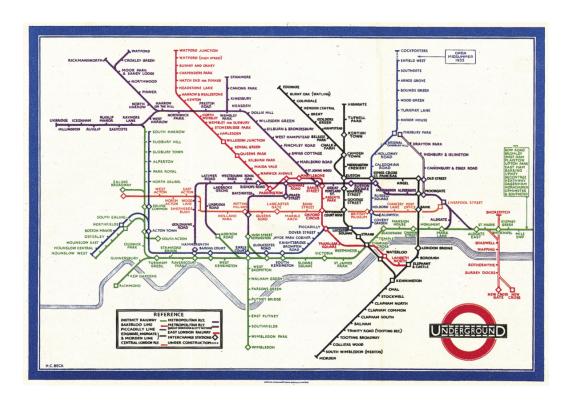
The London Tube Map - 1926







The London Tube Map - 1931





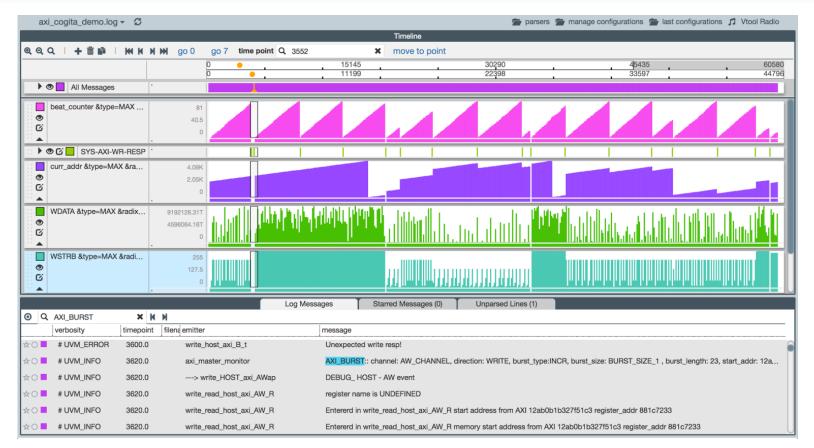


Revolutionizing How We Perceive a Log File

- We need to find a way to visualize test result, without the clutter of "geography".
- We must maintain the details around a bug scenario, but without "compromising the suburbs".
- Only one data path, one class, should be highlighted.
- Fisheye lens.











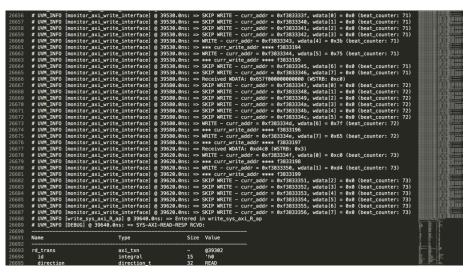
Cogita

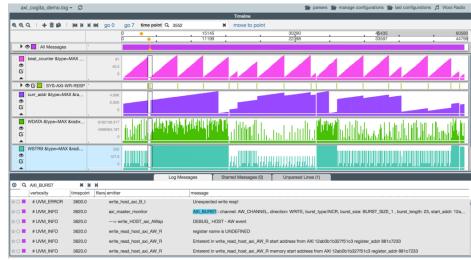
- Cogita automatically parses huge simulation log files.
- Cogita visualizes them in one clear navigational image.
- Cogita offers innovative tools that drastically reduce debug time.
- Cogita is content-agnostic, and able to read any log file in any verification environment, not limited to UVM methodology, SystemVerilog, or others.
- Not limited to verification at all.





Before and After









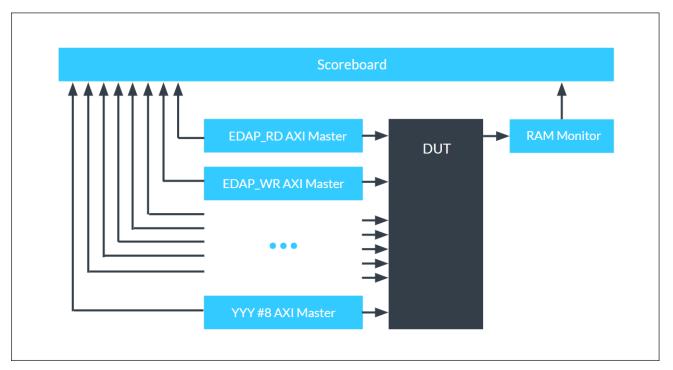
How Cogita Works

- Smart parser reads any log format.
- Messages from the log are presented on a timeline (much like a wave viewer).
- The query builder helps to search, and presents only the relevant info at any given moment.
- Visual comparisons enable to spot scenarios in an instance.
- We appeal to the natural human capacity for processing visual images.





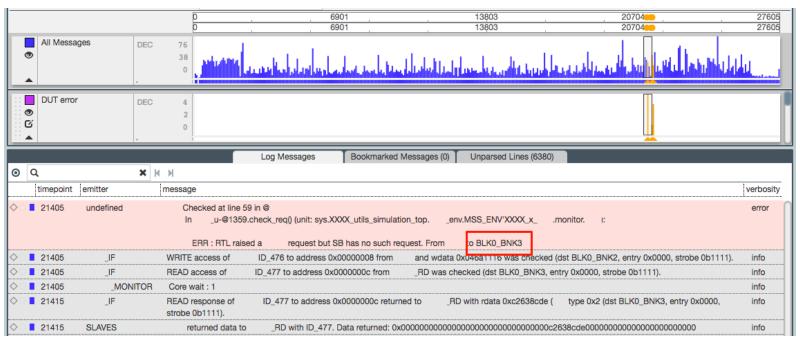
Example - Memory Controller with Multiple AXI Ports







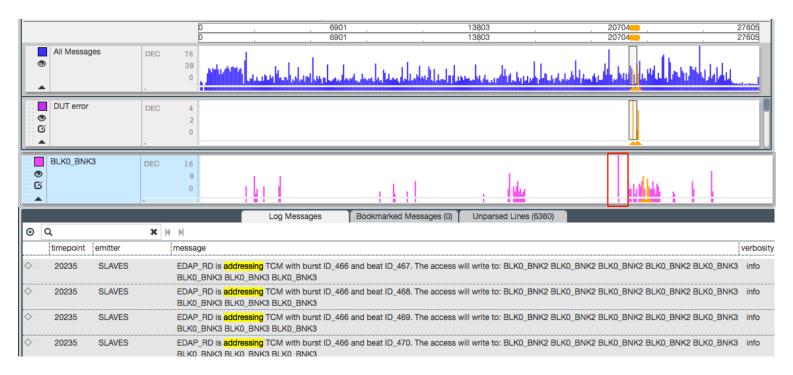
1. Entire Log Scenario with Error Highlighted







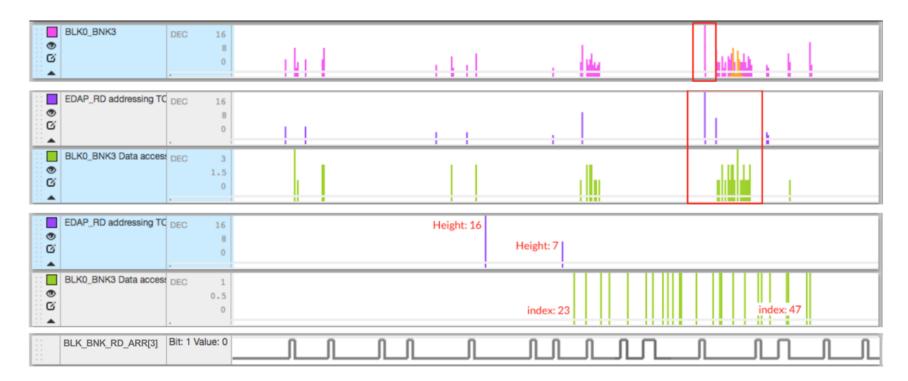
2. Looking for "BLKO_BNK3" Events







Test Case







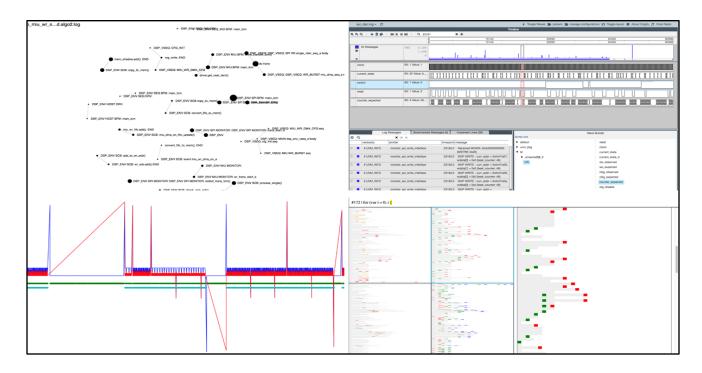
Endless Analysis Potential

- Cogita combines a multidimensional database, into one clear image.
- Log messages, errors, waves, simulation results.
- Cogita serves as a platform for root-cause-analysis algorithms, presented in a way that humans can quickly comprehend.





Other Analysis Capabilities







Cogita Advantages

- Drastically reduces detection time of complex bug scenarios.
- Tweaking test scenarios, to reach an expected outcome.
- Better understanding of teamwork scenarios.





Summary

- We appeal to the natural human capacity for processing images, not text.
- The log exists as a dia-log, instead of the addiction to logic rhythm (a-log-arithm).
- Cogita is the new way to debug.





Thank You





