High-Performance Mixed-Signal ESL Design of a Magneto Resistive Sensor Application

Martin Barnasconi, NXP Semiconductors Sumit Adhikari, NXP Semiconductors







Outline

- Introduction
- SystemC AMS extensions
- ESL design refinement methodology for mixed-signal systems
- Application example: Magneto Resistive (MR) Sensor
- SystemC AMS model of a programmable gain amplifier
- Simulation results
- Conclusions







Introduction

- Most virtual prototypes only focus on digital HW/SW system components – AMS functionality is neglected
- However, today's embedded systems contain AMS and RF components which tightly interact with the HW/SW system
- Advanced modeling approaches needed to include AMS behavior in the architecture design phase
- Application of a model-based ESL design refinement flow based on SystemC and SystemC AMS





V-model and ESL refinement flow



SystemC AMS extensions

- SystemC AMS extensions defined in IEEE Std 1666.1
- SystemC AMS defines new models of computation for efficient modeling of analog and RF functionality
 - Timed Data Flow to describe non-conservative signal processing functionality, including multi-rate systems
 - Linear Signal Flow and Electrical Linear Networks to describe conservative continuous-time descriptions
- Proof-of-concept implementation of SystemC AMS is available under Apache 2.0 license
- SystemC AMS models and simulator can be integrated in commercial EDA tools and flows





SystemC AMS model abstractions and modeling formalisms





2016

DESIGN AND VERIEICA

Modeling in SystemC/-AMS

SystemC

State machines Digital protocols (e.g. I2C, SPI, ...) Digital filters DSP algorithms/functions Processor instruction set model Register interfaces Calibration and control algorithms Transaction-level (TLM) communication FIFO's

. . .

SystemC AMS

Signal sensing, amplification and mixing A/D and D/A conversion Digital filters DSP algorithms/functions Power supply ripple Continuous-time filtering Noise (white noise, 1/f noise, ...) Non-linearities DC offsets Saturation effects Impedance mismatches Small-signal (AC) characteristics Charge/discharge effects





ESL design refinement methodology



- Implement the architecture in SystemC AMS and SystemC
- Design refinement using transfer functions, switches, accurate regulation and control loops
- Modeling of passive components, thermal noise, 1/f noise and non-idealities
- Include characterized behavior in the system-level model, incl. PVT variations and dependencies
- Monte Carlo (MC) simulations using statistical library
- Apply extensive failure injection and analysis
- If MC or failure analysis fails, re-optimize system architecture





Application example: Magneto Resistive (MR) Sensor





DESIGN AND VERIFICATION

CONFERENCE AND EXHIBITION

Programmable Gain Amplifier (PGA) in SystemC AMS

```
class pga: public sca_tdf::sca_module
{
public:
 sca_tdf::sca_in<double>
                                in;
 sca_tdf::sca_out<double>
                                out;
 sca tdf::sca de::sca in<bool> gain select;
 pga( sc_core::sc_module_name nm, double f3dB1 ,
      double dcgain1 , double f3dB2 , double dcgain2 )
  : in("in"), out("out"), gain select("gain select"),
   f3dB1(f3dB1_), dcgain1(dcgain1_), f3dB2(f3dB2_),
   dcgain2(dcgain2 ) {}
 void initialize() // initialize numerator and denominator
 {
   num1(0) = dcgain1; den1(0) = 1.0;
   den1(1) = 1.0 / ( 2.0 * M PI * f3dB1 );
   num2(0) = dcgain2; den2(0) = 1.0;
   den2(1) = 1.0 / ( 2.0 * M PI * f3dB2 );
 }
 void set_attributes()
 {
   request_next_activation( gain_select.default_event() );
   does_attribute_changes();
                                      Enable dynamic
   accept_attribute_changes();
 }
                                         time steps
```









Programmable Gain Amplifier (PGA) in SystemC AMS – incl noise

11

```
class pga noise: public sca_tdf::sca_module
 {
 public:
  sca_tdf::sca_in<double>
                                 in;
  sca_tdf::sca_out<double>
                                 out;
  sca tdf::sca de::sca in<bool> gain select;
  pga noise( sc_core::sc_module_name nm, double f3dB1 ,
       double dcgain1 , double f3dB2 , double dcgain2 )
   : in("in"), out("out"), gain select("gain select"),
    f3dB1(f3dB1_), dcgain1(dcgain1_), f3dB2(f3dB2_),
    dcgain2(dcgain2 ) {}
  void initialize() // initialize numerator and denominator
  {
    num1(0) = dcgain1; den1(0) = 1.0;
    den1(1) = 1.0 / ( 2.0 * M_PI * f3dB1 );
    num2(0) = dcgain2; den2(0) = 1.0;
    den2(1) = 1.0 / ( 2.0 * M PI * f3dB2 );
                                                           Noise
    Rth = 1e-6; Temp = 290.0; k = 1.38064852e-23;
                                                          (4kTBR)
    fs = 8e6; mu = std::sqrt(4*k*Temp*Rth*fs/2.0);
  }
  void set attributes()
     request_next_activation( gain select.default_event() );
    does_attribute_changes();
    accept attribute changes();
accellei
```

SYSTEMS INITIATIVE

```
void processing() // time-domain implementation
  {
    double pga out;
    if( gain select.read() )
      pga out = ltf nd1( num1, den1, state, in.read() +
                mu*r.get value(), , 1.0 );
    else
      pga out = ltf nd2( num2, den2, state, in.read() +
                mu*r.get_value(), , 1.0 );
    out.write(pga out);
  }
  void ch
          // file: gaussian rnd.h
  ł
          // Helper class to calculate a
    reque // Gaussian random number using
                                                lt_event() );
          // the GNU Scientific Library
  }
          #include <gsl/gsl rng.h>
 private:
          #include <gsl/gsl randist.h>
  sca_tdf
  sca_uti template < typename P >
                                                L, den2;
  sca uti class gaussian_rnd
  double
           public:
  double
            gaussian rnd(...);
};
            P get value();
           private:
            const gsl rng type* T;
            gsl rng*
                                 r;
          };
                                                    DESIGN AND VE
```

Typical temperature error of the MR Sensor





DESIGN AND VE

Mixed-signal transient simulation





DESIGN AND VERIFICATION

Typical simulation speed for each design refinement step

Design refinement step	Simulation speed	
	Simulation time (sec)	Wall clock time (sec)
A. Architecture-level design topology exploration	1	~60
B. Architecture design optimization and design centering	1	~500
C. Architecture design for reliability and robustness (using Monte Carlo)	1ms	~5 (per MC run)



Conclusions

- An mixed-signal ESL design refinement methodology has been developed using SystemC and SystemC AMS
- SystemC AMS offers unique capabilities to accurately describe analog/mixed-signal behavior, while keeping a high simulation speed
- The ESL design refinement flow has been successfully applied in the concept design phase of a Magneto Resistive (MR) Sensor
- The use of libraries such as Boost and the GNU Scientific Library (GSL) facilitated analog performance and Monte-Carlo analysis of the mixed-signal sensor application





Acknowledgment

- Karsten Einwich COSEDA Technologies GmbH
- Vincent Motel Cadence Design Systems







Questions



