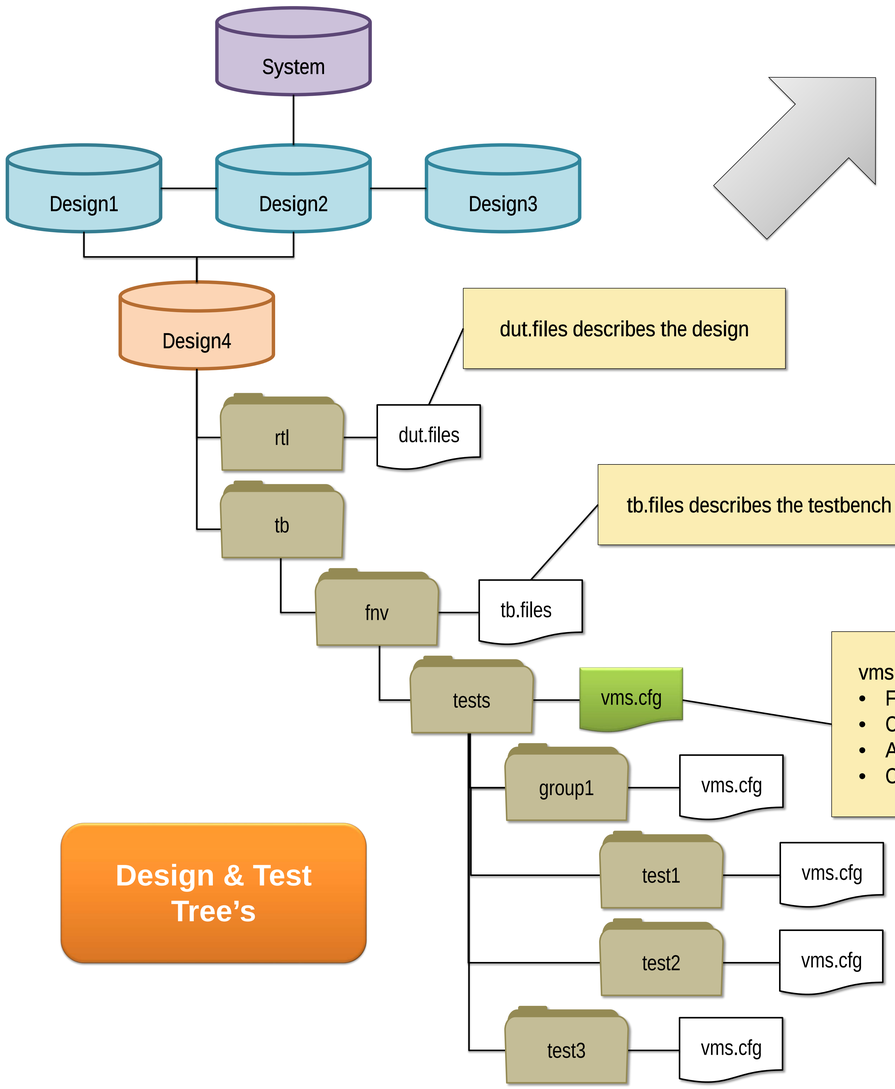
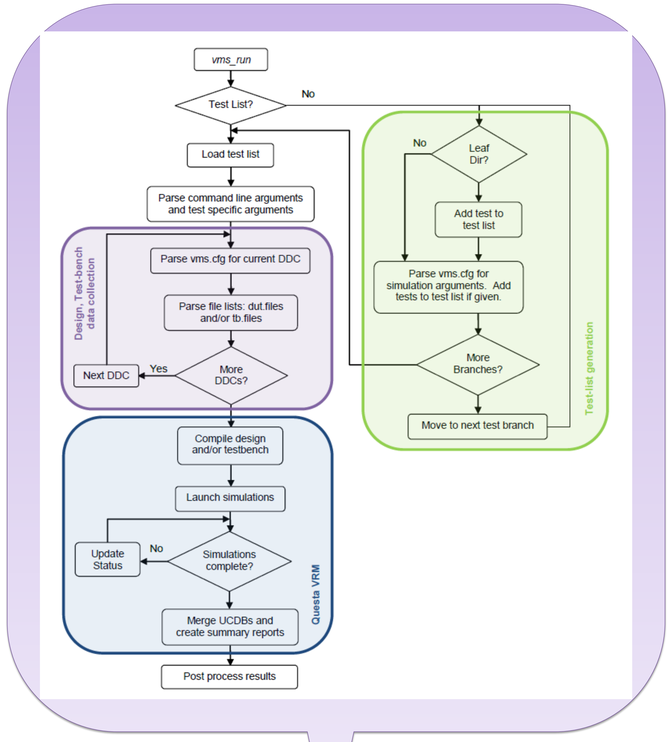
Bringing Regression Systems into the 21st Century

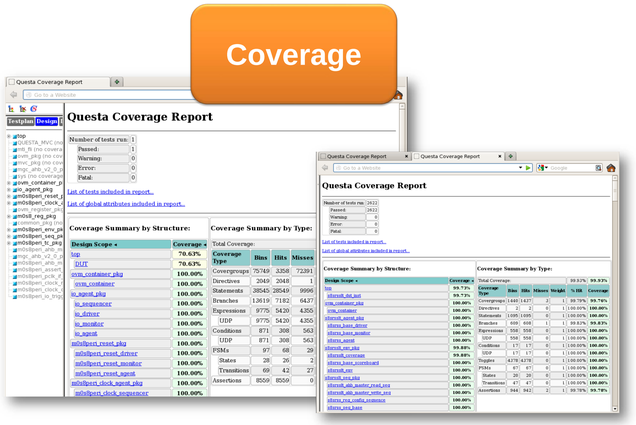
David Crutchfield
Cypress Semiconductor
daac@cypress.com

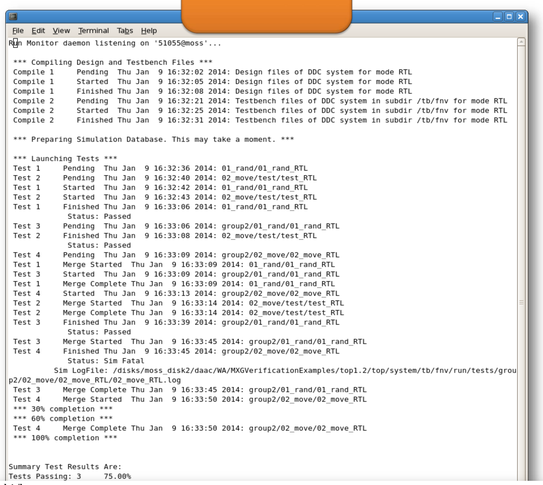
Thom Ellis
Mentor Graphics Corporation
thomas_ellis@mentor.com

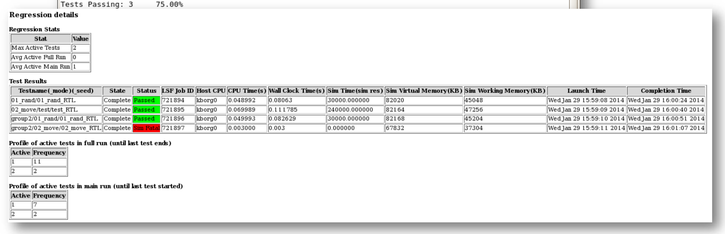
INPUT
- Configure the verification suite through *vms.cfg*, *dut.files*, and *tb.files*.
- Configure simulation modes as well as other options through *vms.cfg*, test-list, and command line options.
- Use *pre/post scripts* before or after any task to further configure the environment.











Ranked Reusults

	Original Results			Ranked Results			CPU Perf Savings
	No. Tests	cvg	CPU(T)	No. Tests	cvg	CPU(T)	
Des1	25	99.7%	4 hr	16	99.7%	2 hr	50%
Des2	2622	99.9%	22.4 hr	306	99.9%	2.2 hr	90%
Des3	146	97.4%	16.4 hr	41	97.4%	8.94 hr	46%
Des4	45	99.9%	3.6 hr	28	99.9%	2 hr	46%