# Automatic Generation of Infineon Microcontroller Product Configurations

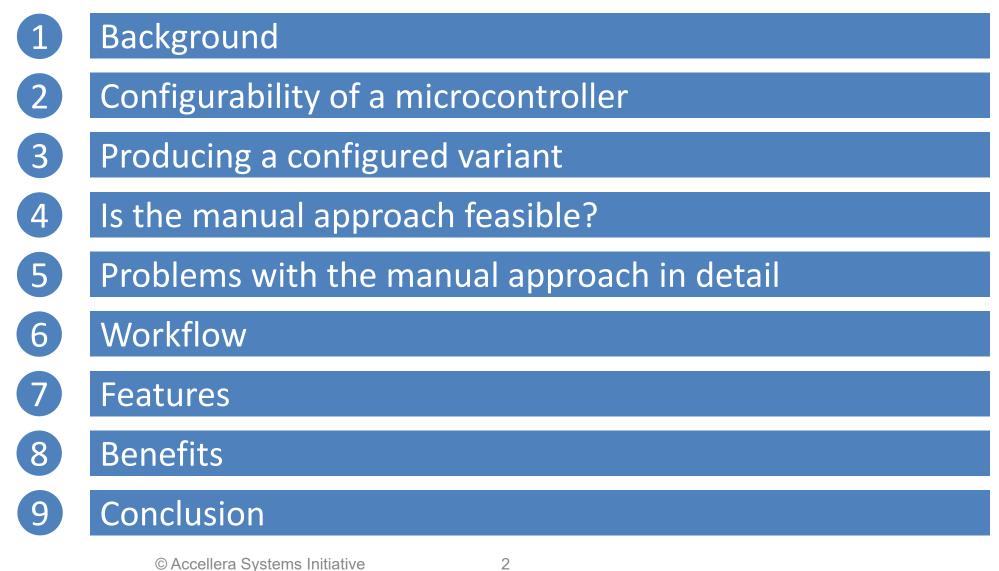
## Prateek Chandra, Leily Zafari, Boyko Traykov Infineon Technologies







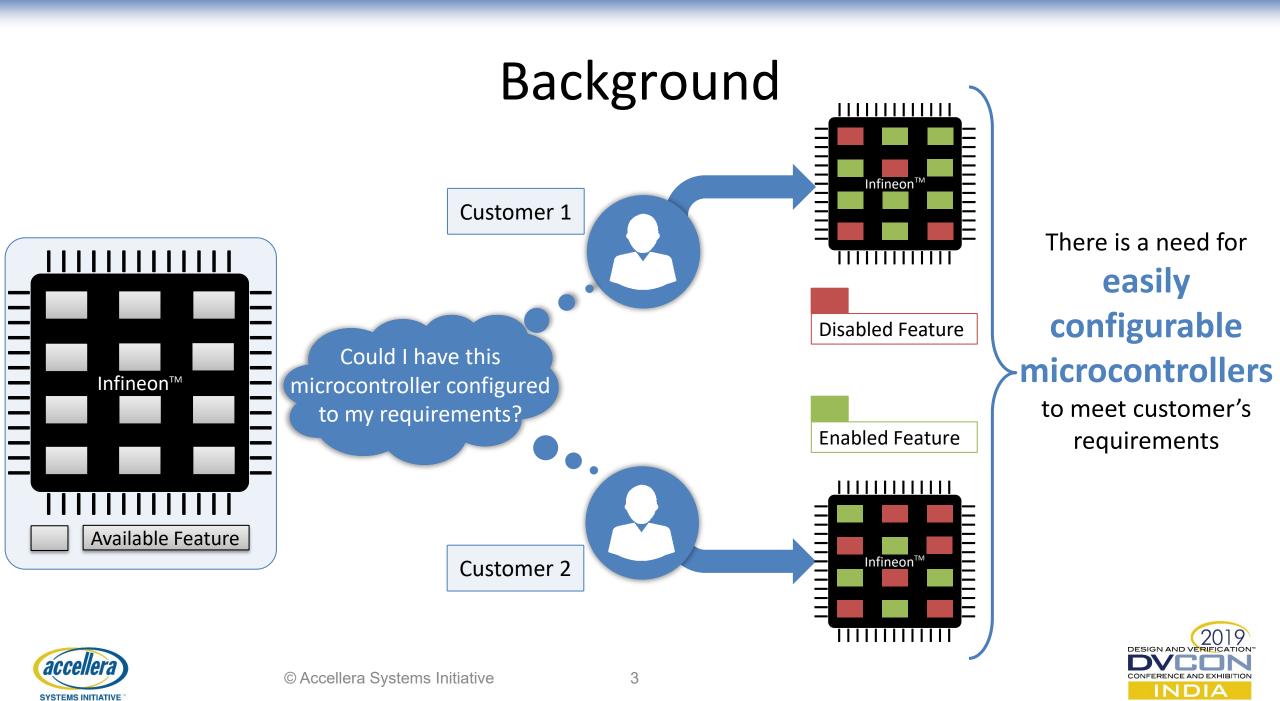
# Agenda



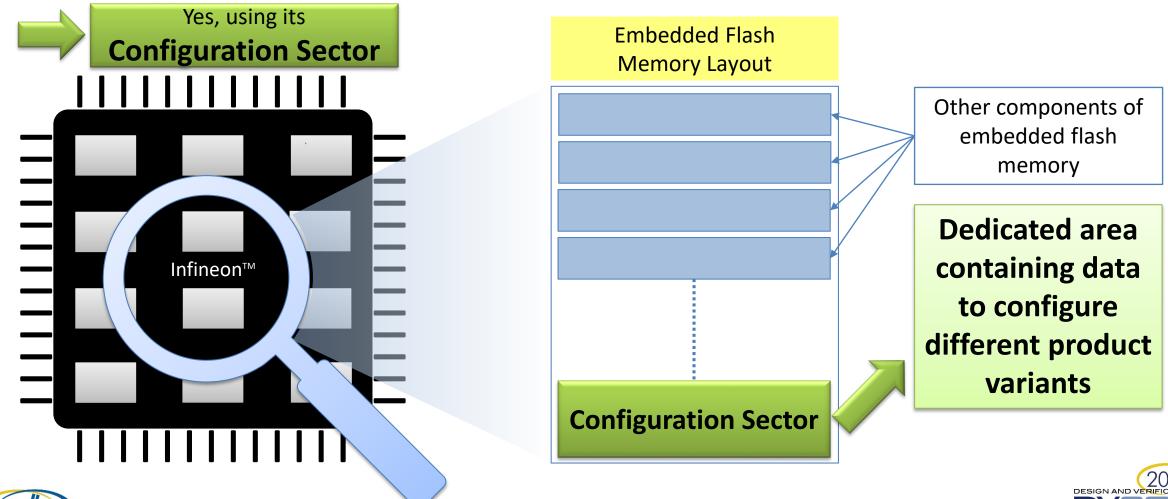
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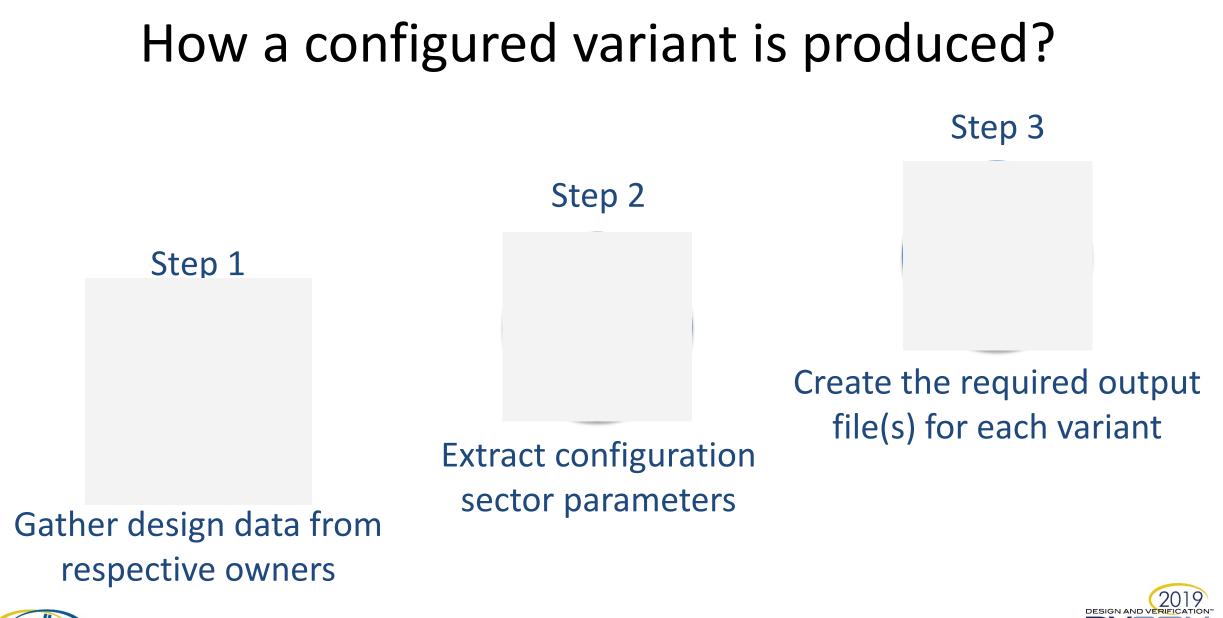




# Configurability of a microcontroller



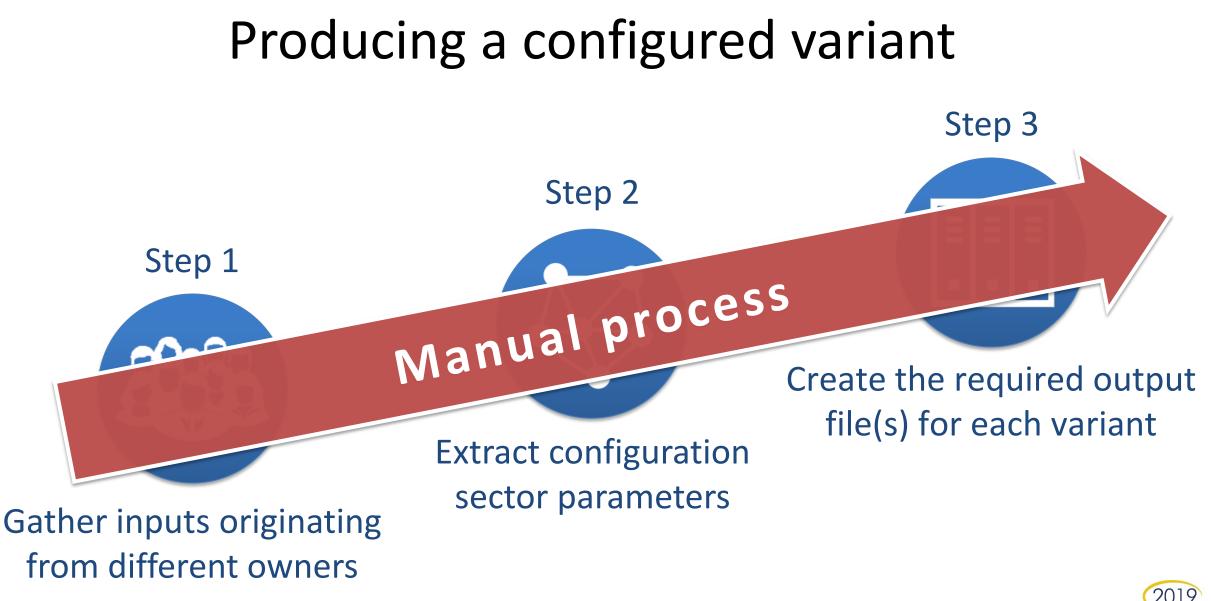




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2019 Design and verification DVCDN CONFERENCE AND EXHIBITION



DESIGN AND VE



# Is the manual approach feasible?

#### Gathering Input Data

Different owners responsible for contributing to the input

#### **Verification Gaps**

Unreliable/incomplete verification and test results

#### **Data Traceability**

Difficult to trace the origin of data from the heterogeneous inputs

#### Consistency

Missing or erroneous configured features; defective devices delivered





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NO

# Problems with manual approach in detail

Step 1



# Gather design data from respective owners







Input owners based at multiple sites

Incoherent communication

Effort is huge for complex designs





# Problems with manual approach in detail

Step 2



Extract configuration sector parameters

Keeping track of the origin of parameters

No version control of the design files

May end up with undesired configuration





# Problems with manual approach in detail

Step 3



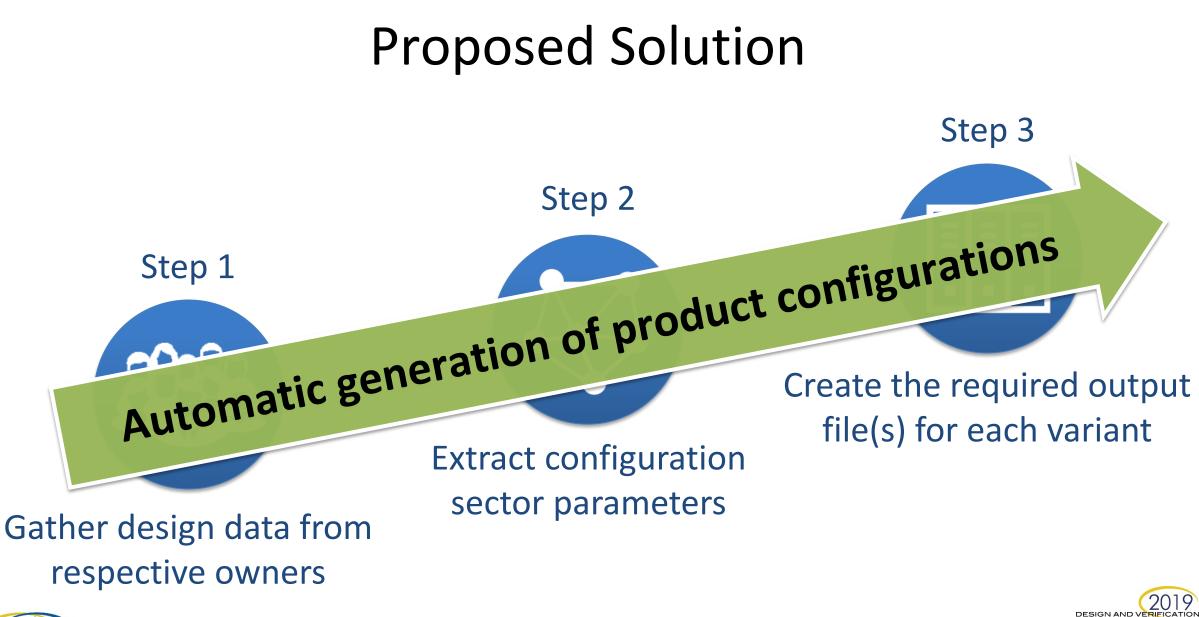
Create the required output file(s) for each variant Is zero-defect delivery ensured?

Is the input and output data consistent?

What is the Quality of Delivarables?

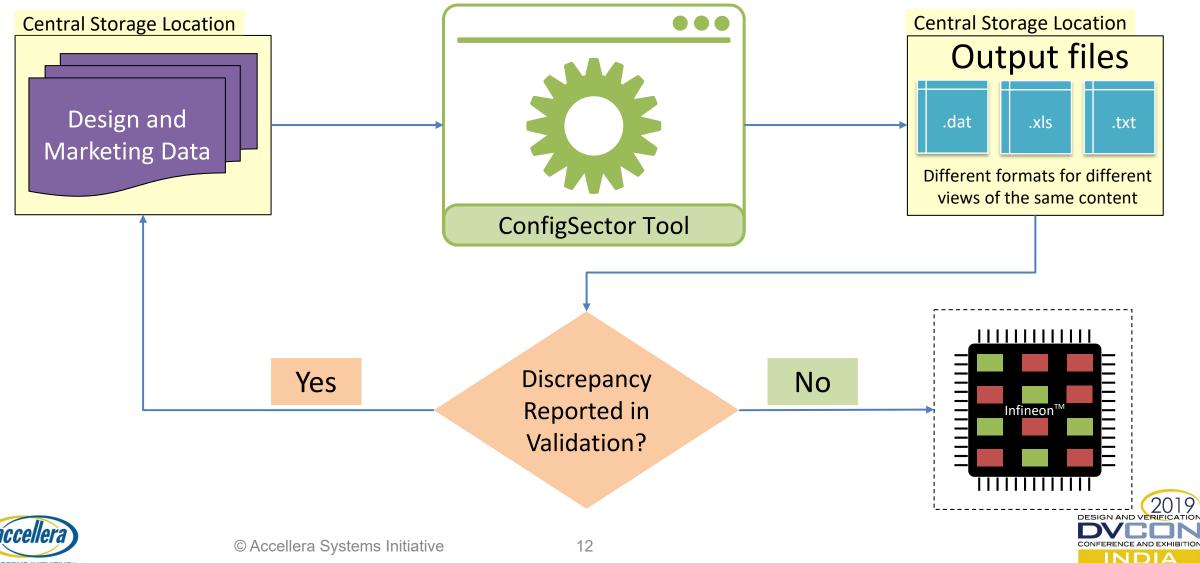








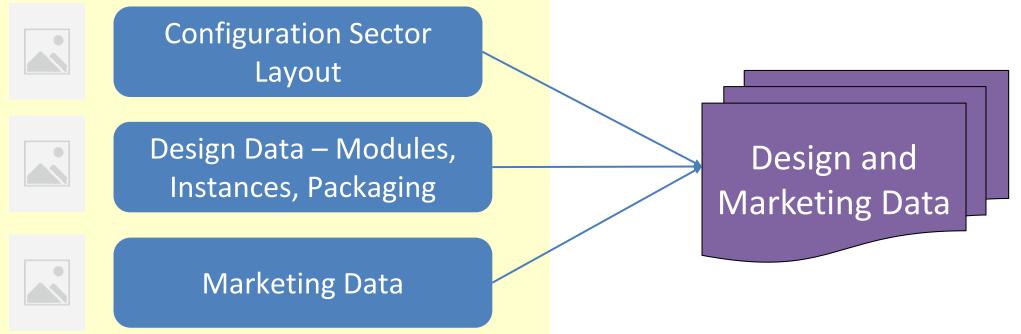
# Workflow



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# Input files present in central version controlled baseline system



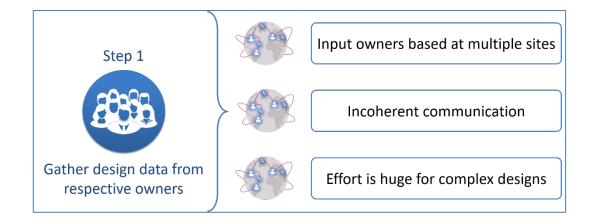






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#### Input files present in central version controlled baseline system





No dependency on data owners



Every input file is version controlled

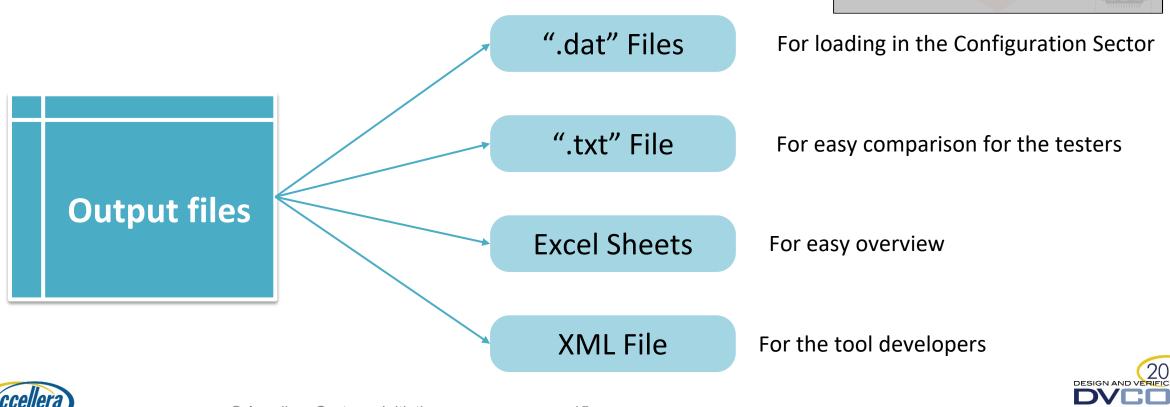


Tool automatically searches for relevant input files







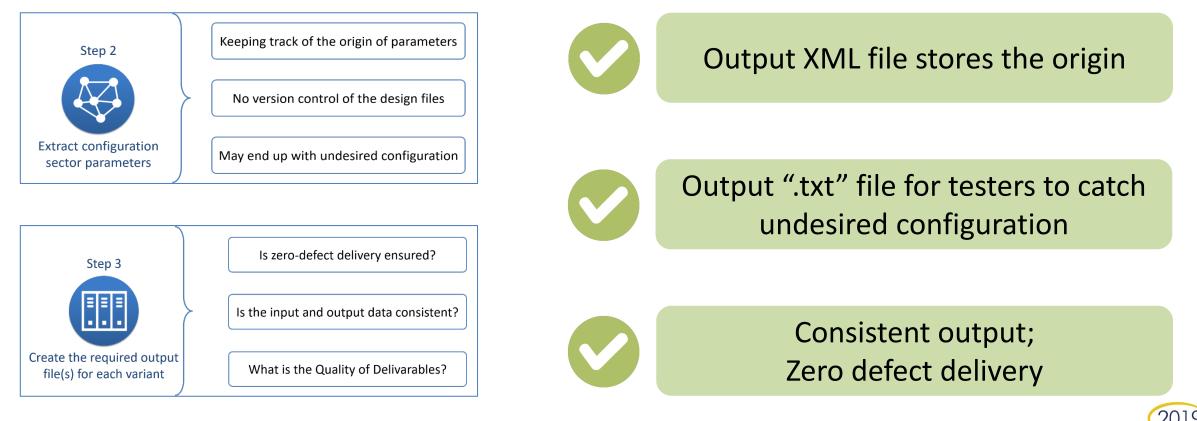


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Multiple views of the same output

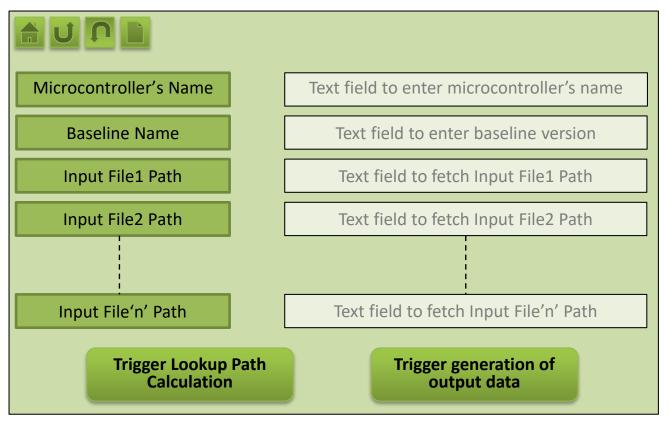
#### Output files released in central version controlled baseline system





DESIGN AND VERIFICA

#### **Platform independent GUI**







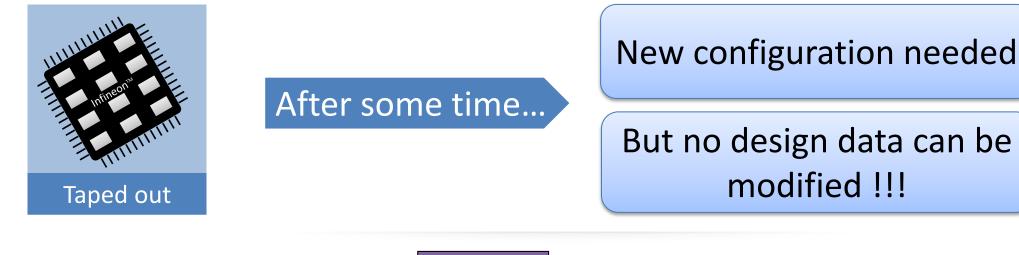
Single click generation



Hard stop if any input is missing

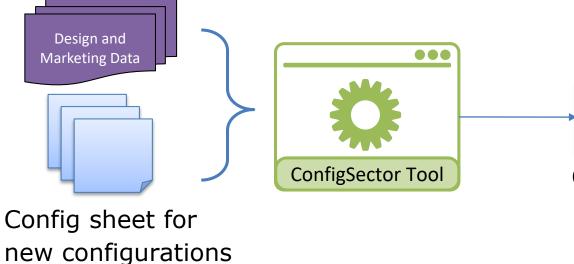


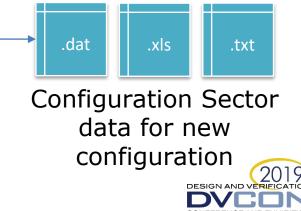




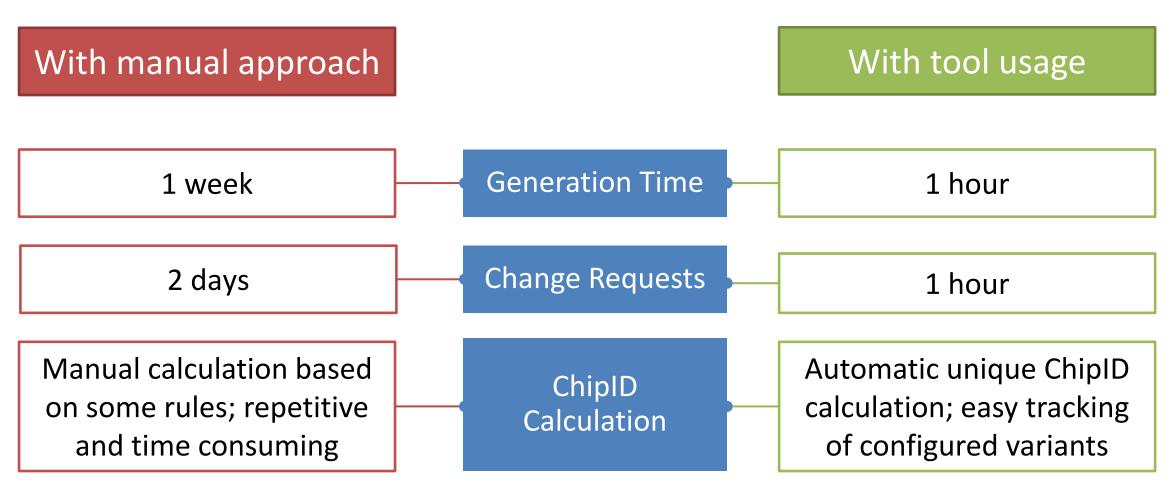
Config Sheet overwrites CFS values from design sources





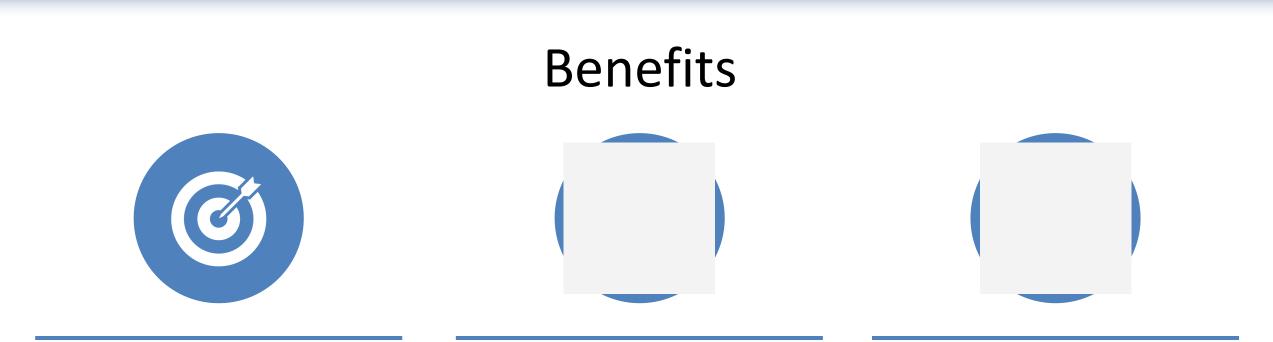


# Results









Configuration Sector Data for all the variants for a microcontroller in a single run Possible to have product variants even after design tapeout Reduced time to market; thus reduction in costs incurred; ease of maintenance





# Conclusion



"Correct-by-construction" methodology makes the flow robust

CFS flow boosts the productivity by cutting down on manual labor

Zero defect deliveries instils more trust in the customers





## Questions



