Automated Toggle Coverage Framework for AURIX™ TC4xx Virtual Prototype

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Problem Statement/Introduction
To verify toggling of each SystemC model interfaces in AURIX™ TC4xx Virtual Prototype (VP)
To identify the interfaces/ports that are not exercised in test case regression
Manually reviewing the toggle of each interface is cumbersome
No tool available to capture the interfaces which are toggled during test case regression run on VP
No reusable solution available across VP tools

Proposed Methodology/Advantages
Infineon developed an in-house utility called Automated Toggle Coverage Framework (ATCF) - Developed in combination with python interface/utilities supported by VP tool on which AURIX™ TC4xx VP is developed

Automated Toggle Coverage Framework is a consists of:
- **Interface Extractor**: Extracts the interfaces/ports list and its data type of each IP
- **Ports Monitor Script Generator**: Generates SimProbes Python Scripts per module with call back function for each extracted ports, which are sensitive to any change in port value
- **Consolidated Report Generator**: Generates consolidated report which consists of interface name and toggle count of interface during the simulation for each IP

Implementation Details/Diagram

Implementation Details/Flow Chart

Results Table

<table>
<thead>
<tr>
<th>Modules</th>
<th>Tested/Toggled Interface</th>
<th>Untested/Untoggled Interface</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>1st Iteration</td>
<td>2nd Iteration</td>
</tr>
<tr>
<td>PPU</td>
<td>18(81%)</td>
<td>20(90%)</td>
</tr>
<tr>
<td>CSCU</td>
<td>12(85%)</td>
<td>13(92%)</td>
</tr>
<tr>
<td>LMU</td>
<td>5(83%)</td>
<td>6(100%)</td>
</tr>
<tr>
<td>Clocking</td>
<td>25(100%)</td>
<td>25(100%)</td>
</tr>
</tbody>
</table>

Conclusion
Verification gaps in interface level is identified and improved by developing test cases for untested ports
Any update/new in IP Interface will be extracted by framework and added to interface list for toggle coverage without much manual effort
ATCF is expected to be deployed for all other IP's

REFERENCES