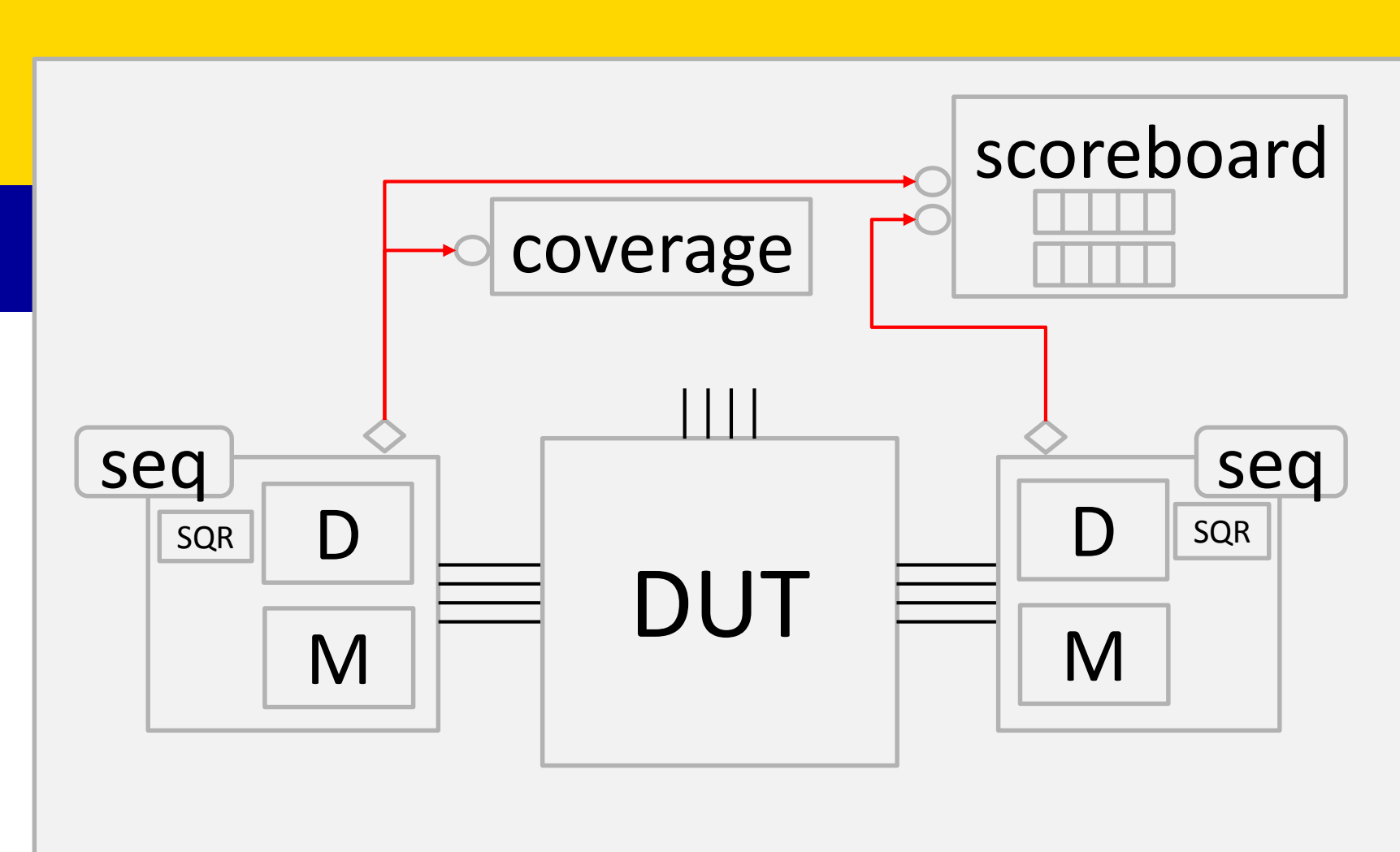


Are You Smarter Than Your Testbench? With a little work you could be



```

20 class abc_monitor extends uvm_monitor;
21   `uvm_component_utils(abc_monitor)
22
23   virtual abc_if bfm;
24   uvm_analysis_port #(abc_item) ap;
25
26   ...
27   abc_item t;
28   task run_phase(uvm_phase phase);
29
30   ...
31   t = abc_item::type_id::create(
32     $sformatf("t%0d", transaction_count++));
33   `W_NEW(t)
34   forever begin
35
36     bfm.monitor(transaction_ok,
37       t.rw, t.addr, t.data, t.burst);
38     if (transaction_ok) begin
39
40       `W_MARK(t)
41       ap.write(t);
42     end
43   end
44 endtask
45 endclass

```

abc_monitor

```

20 class abc_driver extends uvm_driver #(abc_item);
21   `uvm_component_utils(abc_driver)
22
23   virtual abc_if bfm;
24   uvm_analysis_port #(abc_item) ap;
25
26   ...
27   task run_phase(uvm_phase phase);
28     abc_item item;
29     abc_reset_item reset_item;
30
31     ...
32     forever begin
33       seq_item_port.get_next_item(item);
34       `W_MARK(item)
35
36       `W_MARK(item)
37       ap.write(item);
38       seq_item_port.item_done();
39     end
40   endtask
41 endclass

```

abc_driver

```

20 class abc_sequence extends uvm_sequence #(abc_item);
21   `uvm_object_utils(abc_sequence)
22
23   ...
24   abc_item item;
25   task body();
26     abc_reset_item reset_item;
27
28     ...
29     `S_CREATE_OBJECT(reset_item, abc_reset_item, "reset_item")
30     `W_NEW(reset_item)
31     `S_START_ITEM(reset_item) // start_item(reset_item)
32     `W_MARK(reset_item)
33     `S_FINISH_ITEM(reset_item) // finish_item(reset_item)
34     `W_MARK(reset_item)
35
36     ...
37     while(1) begin
38
39       // item = abc_item::type_id::create(
40       //   $sformatf("item%0d", transaction_count++));
41       `S_CREATE_OBJECT(item, abc_item,
42         $sformatf("item%0d", transaction_count++))
43       `W_NEW(item)
44
45       fork
46         automatic abc_item item_f = item;
47         begin
48           `W_MARK(item_f)
49           `S_START_ITEM(item_f) // start_item(item_f)
50           if (!item_f.randomize() with { ... })
51
52             `W_MARK(item_f)
53           `S_FINISH_ITEM(item_f) // finish_item(item_f)
54           `W_MARK(item_f)
55         end
56       join_none
57
58     end
59   endtask
60 endclass

```

abc_sequence

```

// Run regression with +UVM_VERBOSITY=UVM_LOW
// Regular `uvm_info: 20x seconds
// Fast `uvm_info: x seconds

import uvm_pkg::*;
`include "uvm_macros.svh"

`ifdef uvm_info
`undef uvm_info
`endif

`define uvm_info(ID,MSG,VERBOSITY) \
begin \
  if ( VERBOSITY <= UVM_LOW ) \
    if (uvm_report_enabled(VERBOSITY,UVM_INFO,ID)) \
      uvm_report_info (ID, MSG, VERBOSITY, \
        `__FILE__, `__LINE__); \
end

module top();
initial
  for (int i = 0; i < 100_000_000; i++) begin
    `uvm_info("INIT", msg, UVM_MEDIUM)
    `uvm_info("INIT", msg, UVM_HIGH)
  end
endmodule

```

uvm_info

```

2 class abc_checkerboard extends
  uvm_subscriber #(abc_item);
3   `uvm_component_utils(abc_checkerboard)
4
5   uvm_tlm_fifo #(abc_item) fifo;
6   uvm_analysis_port #(abc_item) ap;
7
8   abc_item t;
9   bit _end;
10
11   ...
12   function void write(abc_item t);
13     `W_MARK(t)
14     fork
15       #10 fifo.try_put(t);
16     join_none
17   endfunction
18
19   task calc(abc_item t);
20     `W_MARK(t)
21
22     ...
23     `W_PRINT(t)
24     `W_DESTROY(t)
25   endtask
26
27   task run_phase (uvm_phase phase);
28     forever begin
29
30       fifo.get(t);
31       `W_MARK(t)
32
33       ...
34       if (_end)
35         calc(t);
36       else
37         ap.write(t);
38     end
39   endtask
40 endclass

```

abc_checkerboard

```

Counted Report -----
Counted by file_line
  1 : abc_pkg/abc_seq_lib.svh:141
  2 : abc_pkg/abc_seq_lib.svh:58
 1983 : abc_pkg/abc_seq_lib.svh:79
  4 : xyz_pkg/xyz_seq_lib.svh:56
 3954 : xyz_pkg/xyz_seq_lib.svh:74
Counted by seq_full_name
  1 : uvm_test_top.env.agent1.sequencer.seq
 1985 : uvm_test_top.env.agent1.sequencer.seq1
 1973 : uvm_test_top.env.agent2.sequencer.seq2
 1985 : uvm_test_top.env.agent3.sequencer.seq3
Counted by seq_item_type_name
 1984 : abc_item
  2 : abc_reset_item
 3954 : xyz_item
  4 : xyz_reset_item
Counted by seq_type_name
  1 : abc_dump
 1985 : abc_even_sequence
 3958 : xyz_sequence

// Used for Sequence Items, from within a Sequence.
// lhs = type_t:::type_id::create(typename)
`define S_CREATE_OBJECT(lhs, type_t, typename)

```

Allocation

Where has my transaction been?

```

Report (0x00D01AD) -----
>> @2570: <item22> (abc_pkg/abc_seq_lib.svh:80) [in body / NEW ]
>> @2570: <item22> (abc_pkg/abc_seq_lib.svh:85) [in body / uvm_test_top.env.agent1.sequencer.seq1]
>> @2570: <item22> (abc_pkg/abc_seq_lib.svh:92) [in body / uvm_test_top.env.agent1.sequencer.seq1]
>> @2570: <item22> (abc_pkg/abc_driver.svh:46) [in run_phase / uvm_test_top.env.agent1.driver]
>> @2730: <item22> (abc_pkg/abc_driver.svh:65) [in run_phase / uvm_test_top.env.agent1.driver]
>> @2730: <item22> (abc_pkg/abc_checkerboard.svh:24) [in write / uvm_test_top.env.agent1.checkerboard2_0]
>> @2730: <item22> (abc_pkg/abc_seq_lib.svh:94) [in body / uvm_test_top.env.agent1.sequencer.seq1]
>> @2740: <item22> (abc_pkg/abc_checkerboard.svh:46) [in run_phase / uvm_test_top.env.agent1.checkerboard2_0]
>> @2746: <item22> (abc_pkg/abc_checkerboard.svh:24) [in write / uvm_test_top.env.agent1.checkerboard2_1]
>> @2756: <item22> (abc_pkg/abc_checkerboard.svh:46) [in run_phase / uvm_test_top.env.agent1.checkerboard2_1]
...
>> @2874: <item22> (abc_pkg/abc_checkerboard.svh:24) [in write / uvm_test_top.env.agent1.checkerboard2_9]
>> @2884: <item22> (abc_pkg/abc_checkerboard.svh:46) [in run_phase / uvm_test_top.env.agent1.checkerboard2_9]
>> @2894: <item22> (abc_pkg/abc_checkerboard.svh:32) [in calc / uvm_test_top.env.agent1.checkerboard2_9]

```

```

20 class abc_coverage extends
  uvm_subscriber #(abc_item);
21   `uvm_component_utils(abc_coverage)
22
23   abc_item t;
24
25   ...
26   virtual function void sample(abc_item t);
27     `W_MARK(t)
28     this.t = t;
29     cg.sample();
30   endfunction
31
32   function void write(abc_item t);
33     `W_MARK(t)
34     sample(t);
35
36   ...
37   endfunction
38 endclass

```

abc_coverage

```

36 class axx_basic_sequence extends axx_sequence_base;
37   `uvm_object_utils(axx_basic_sequence)
38
39   ...
40   task body();
41     abc_even_sequence seq1;
42     xyz_sequence seq2, seq3;
43
44     ...
45     seq1 = abc_even_sequence::type_id::create("seq1");
46     seq2 = xyz_sequence::type_id::create("seq2");
47     seq3 = xyz_sequence::type_id::create("seq3");
48
49     ...
50     fork
51       `S_START_SEQUENCE(seq1, env.agent1.sequencer) // seq1.start(env.agent1.sequencer)
52       `S_START_SEQUENCE(seq2, env.agent2.sequencer) // seq2.start(env.agent2.sequencer)
53       `S_START_SEQUENCE(seq3, env.agent3.sequencer) // seq3.start(env.agent3.sequencer)
54     join
55
56     ...
57   endtask
58 endclass

```

axx_sequence

```

Sequence Types Running Report -----
Sequence 4 : 'xyz_sequence' (last started at 0)
Sequence 2 : 'abc_even_sequence' (last started at 0)
Sequence 2 : 'abc_dump' (last started at 79790)
Sequence 1 : 'b' (last started at 179810)
Sequence 1 : 'a' (last started at 0)

`define S_START_SEQUENCE(seq, sqr) // seq.start(sqr)

```

Sequences Used?

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```

proc go { n } {
  for {set i 0} { $i < $n } { incr i } {
    step -current;
    set msg [vsim_kernel tb 0]
    puts $msg
  }
}

bp xyz.sv
run -all
go 100

```

"auto-single-step"

```

# STATS: Load Average Report (loadav) -----
# STATS: uvm_test_top.env.agent1: loadav [ 6 2.8 1]
# STATS: uvm_test_top.env.agent2: loadav [ 5.5 2.7 0.99]
# STATS: uvm_test_top.env.agent3: loadav [ 5.9 2.7 0.99]

// Should be used from within a sequence as
// start_item(item) => `START_ITEM(item)
// finish_item(item) => `FINISH_ITEM(item)

```

Load Average