

# **An Automated Pre-silicon IP Trustworthiness**

**Assessment for Hardware Assurance** 

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## **CAN YOU TRUST THIRD-PARTY IPs?**

Using (third-party) IPs is crucial for cost-effective IC and SoC design

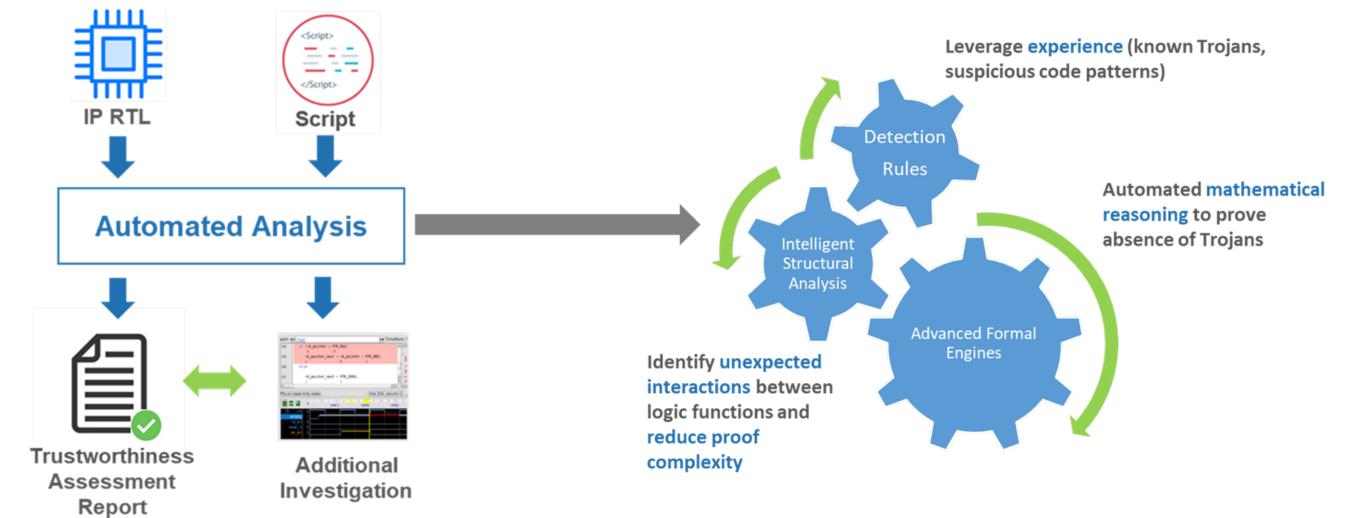
## Risks

- IPs could contain security vulnerabilities and undocumented, malicious logic
- Hardware Trojans can be inserted at various stages during the IP design cycle

# **AUTOMATED TRUSTWORTHINESS ASSESSMENT**

Highlights

- No trusted/independent IP model is required
- No formal verification or IP expertise required
- Automated, repeatable, objective assessment process
- Leverage unique technology and expertise under-the-hood



#### In security- and safety-critical chips these risks must be managed

## Challenges

- IPs are complex SoC Integrators don't know the details
- RTL code review is effort-intensive and likely to miss issues
- Limited engineering resources, expertise, tools

## RESULTS

#### Test suite

- 90 designs with and without Trojans inserted
- Size range: 100 to 100K FFs

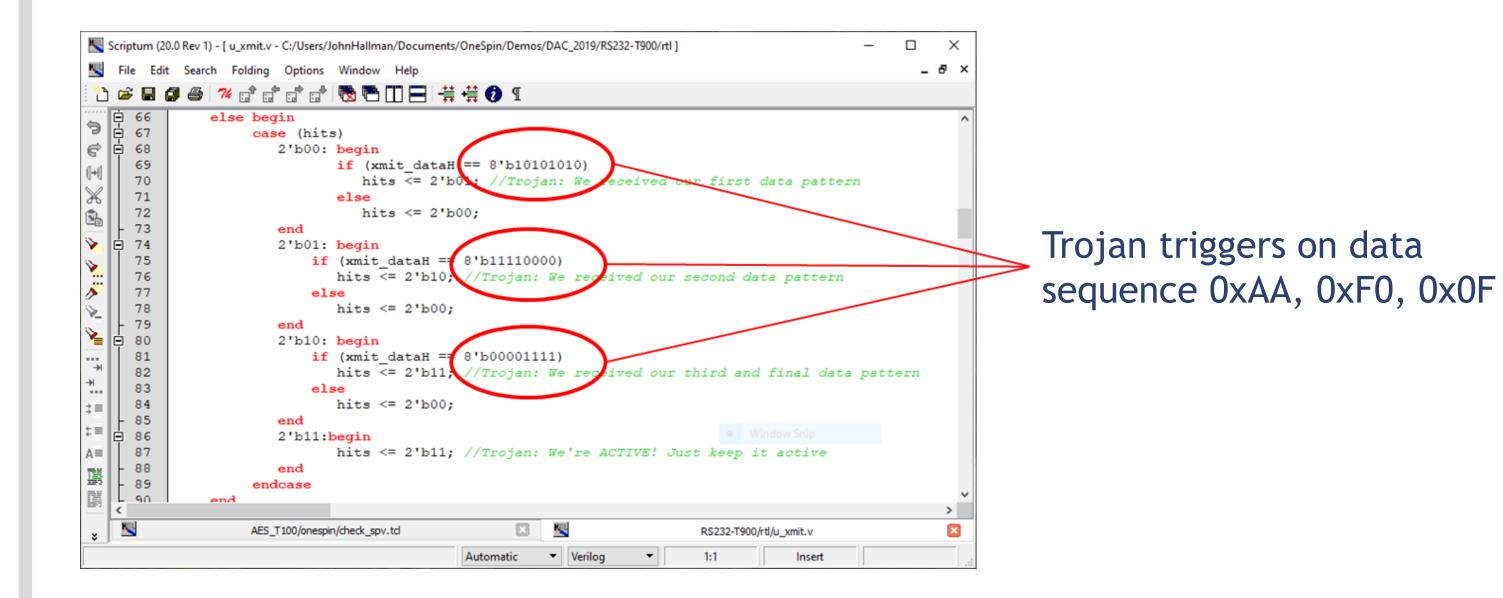
Source	Name	Runtime	lssues Reported	Trojan Inserted	Automatic Detection
TrustHub*	AES	11 hours	260	Yes	Yes
TrustHub	PIC16	<1 min	72	Yes	Yes
TrustHub	RS232	<1 min	3	Yes	Yes
TrustHub	BasicRSA	<1 min	17	Yes	Yes
GitHub	RISC-V Rocketcore	28 min	12	No	Yes
OneSpin	UART	<1 min	10	Yes	Yes
Aerospace**	SpaceWire	<1 min	3	No/Yes	No
Aerospace	RISC-V Taiga	13 min	46	No/Yes	No
Aerospace	Leon3	6 hours	423	No/Yes	Yes***

#### Results

- Representative selection of IP designs
- Few trigger-type issues reported
- Numerous reliability issues reported

# **EXAMPLE OF DETECTED TROJAN**

- Triggers based on deep counters
- Specific sequences of events triggering unusual control action
- FSM with malicious logic monitoring the occurrence of specific data sequence



- Very few false alarms
- Some Trojans have been missed
- Runtime is short

#### \*\* Aerospace designs contained 1 golden, 3 with Trojans

\* TrustHub designs averaged results over multiple articles

\*\*\* Leon3 articles consisted of 3 Trojan designs, 1/3 Trojans discovered

# SIGNOFF ASSESSMENT REPORT

#### Concise - Actionable - Customizable - Issues linked to IP model

<u>Session Setup F</u> ile <u>E</u> dit <u>C</u> C/MV EC <u>T</u> ools <u>W</u> indow <u>H</u> elp	
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🗞 Design Explorer 🛯 💲 Lint Browser 🗳 🗑 Auto Checks 🖾 😨 Dead-Code Checks 🖾 🗞 Assertion Checks 🖾 🗞 Trust 🖾	
Strust	Î
Issue Manager	
Image: Construction Image: Construction Reliability Triggers Deadlock	Search:
ID Location 14 Category 14 Object 14 Description	Severity 4
trigger_1_1 risc16f84.v:486 trigger inst_reg Parts of signal 'inst_reg' impact branch(es) with value '??????00	000000'! 1

## CONCLUSION

#### IP trustworthiness is a rising concern

• A vulnerability or hardware Trojan can compromise the security of the entire system

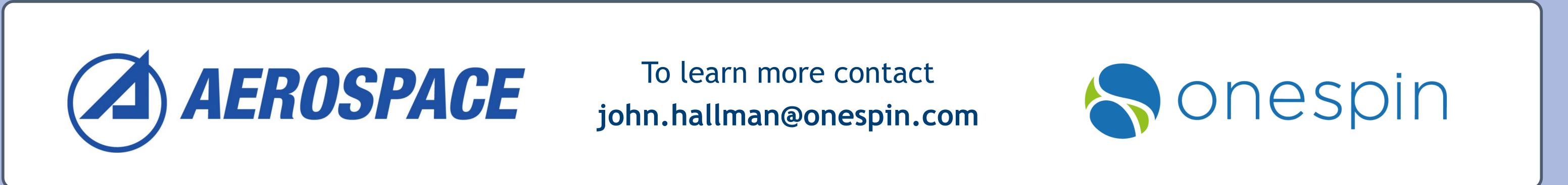
#### Automated trustworthiness assessment

- Provides a low-effort, objective approach to increase confidence that IP is trustworthy
- Does not require additional IP model or detailed IP knowledge
- Algorithms need continuous improvement
- Low noise level (false alarms) is key

	1	Parts of signal 'inst_reg' impact branch(es) with value '001100?00000??'!	inst_reg	trigger	risc16f84.v:822	trigger_1_2
	1	Parts of signal 'inst_reg' impact branch(es) with value '100?00?00000??'!	inst_reg	trigger	risc16f84.v:937	trigger_1_3
	1	Parts of signal 'inst_reg' impact branch(es) with value '0?1????00000?0'!	inst_reg	trigger	risc16f84.v:970	trigger_1_4
	1	Signal 'inst_reg' impacts branch(es) with value '0000000000001001'!	inst_reg	trigger	risc16f84.v:1013	trigger_1_5
-	1	Parts of signal 'inst reg' impact branch(es) with value '00000001100???'!	inst reg	trigger	risc16f84.v:1029	trigger 1 6
lelp	e Interrupt 🧳 H	Show Shell Shell Mode				

#### **Process limitations**

- No trustworthiness metric (open industry topic)
- Detects Trojans, but cannot prove absence of all functional Trojans



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