Accelerating Automotive Ethernet validation by leveraging Synopsys Virtualizer with TraceCompass

Ashish Gandhi

Praveen Kumar Kondugari

Sam Tennent

SYNOPSYS[®]





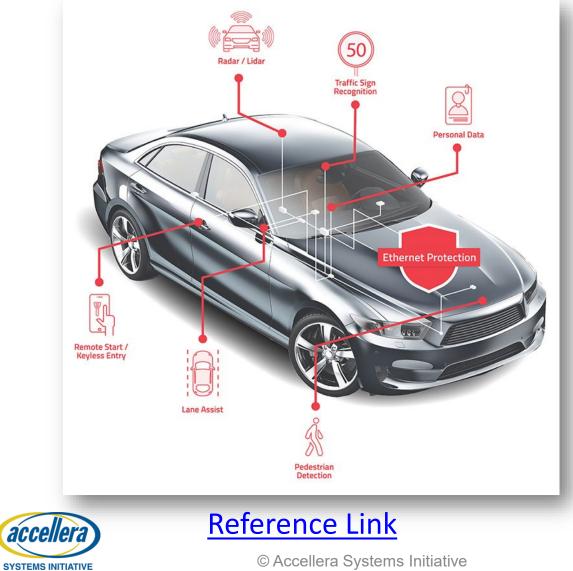
Overview

- What is Automotive Ethernet?
- Role of Virtual Prototyping
- Synopsys Virtualizer
- Challenges in debugging and current solutions
- TraceCompass
- Integrating TraceCompass with Virtualizer
- Experiments and Results
- Conclusions



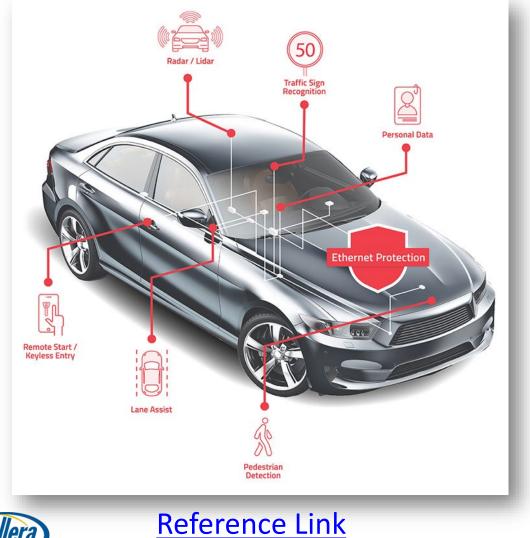


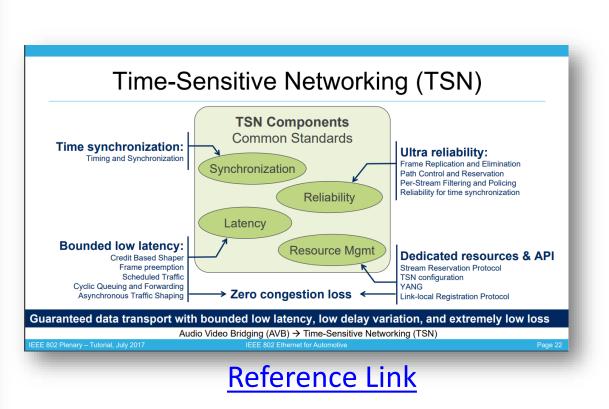
Automotive Ethernet





Automotive Ethernet









© Accellera Systems Initiative

Role of Virtual Prototyping

- Pre-silicon, Software Development
- Front-load Test Development

ECU Development		SW Development	
ECU Development			
Virtual Prototype Development	al Approa	ach	
SW Development			



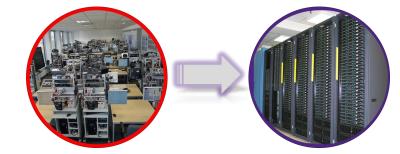


Role of Virtual Prototyping

- Pre-silicon, Software Development
- Front-load Test Development

ECU Development		SW Development	
ECU Development			
Virtual Prototype Development	ntal Approac	h	
SW Development			

- Accelerate test cycles in regression
- Anytime, anywhere availability







Role of Virtual Prototyping

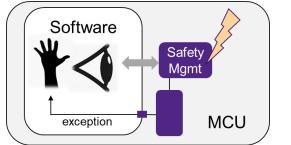
- Pre-silicon, Software Development
- Front-load Test Development

ECU Development	SW Development	
ECU Development		
Virtual Prototype Development	ntal Approach	
SW Development	Shift Left	Ľ

- Accelerate test cycles in regression
- Anytime, anywhere availability



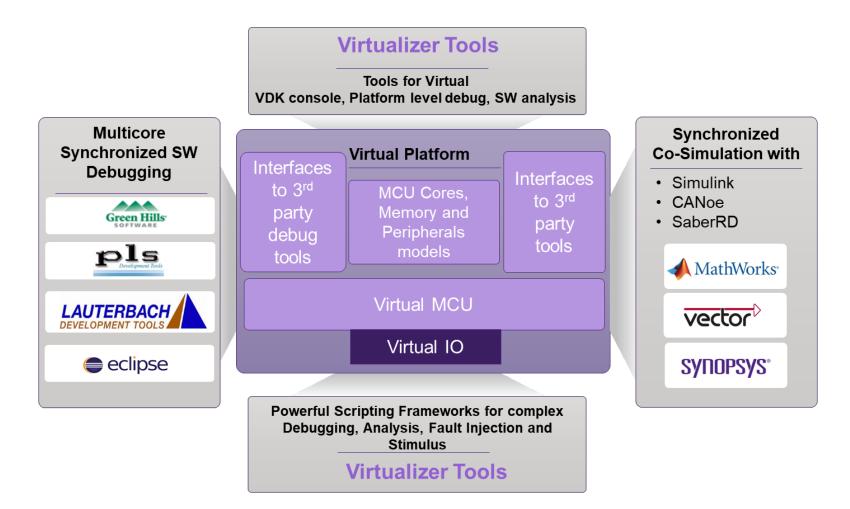
Increase fault & coverage testing







Synopsys Virtualizer[™]



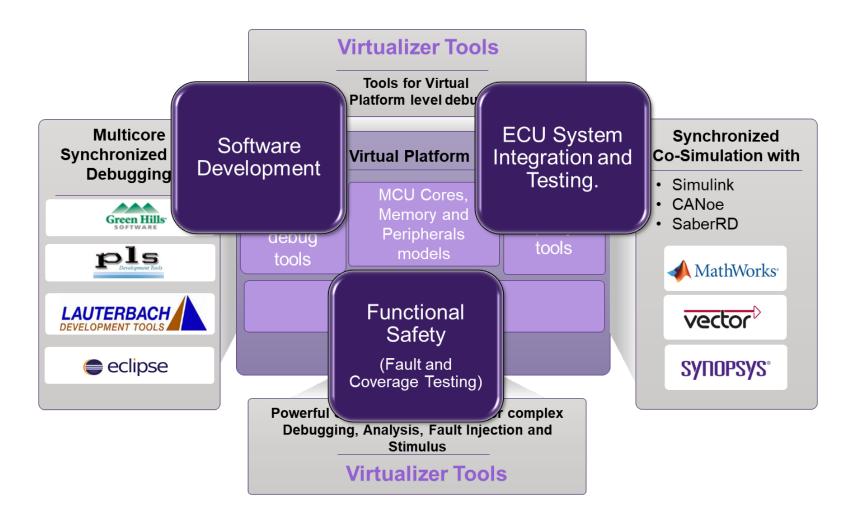


DESIGN AND VERIFICATION"

CONFERENCE AND EXHIBITION

ELIRO

Synopsys Virtualizer[™]





DESIGN AND VERIFICATION

CONFERENCE AND EXHIBITION

EUROPE

Challenges in debugging and current solutions

- Challenges
 - Huge amount of ethernet traffic
 - Missing holistic view of Ethernet transactions
 - Manual mapping of ethernet transactions with other hardware and software events





Challenges in debugging and current solutions

- Challenges
 - Huge amount of ethernet traffic
 - Missing holistic view of Ethernet transactions
 - Manual mapping of ethernet transactions with other hardware and software events

- Current solutions
 - Virtualizer
 - ✓pcap capture with timestamping
 - ✓ analyze ethernet and platform events
 - analyze ethernet traffic
 - Wireshark
 - ✓ analyze single pcap
 - analyze multiple pcap
 - correlate other platform events
 - TraceCompass
 - ✓ analyze multiple pcap
 - correlate other platform events

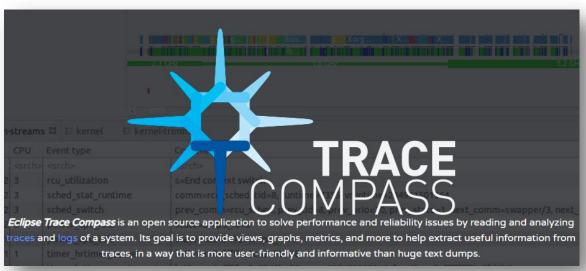




TraceCompass

Network Tracing

- Visualize multiple pcap files
- Time-Synchronous analysis views
 - Pcap Trace Viewer, TimeChart
 - Histogram, State System Explorer
- Other statistical analysis views and options
 - Stream List, Statistics, Filters, Colors, merge pcap traces into single view



Reference Link

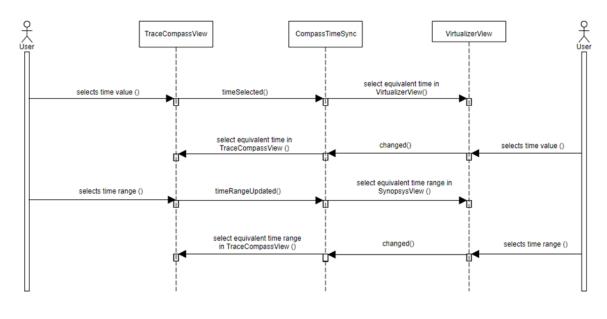






Integrating TraceCompass with Virtualizer

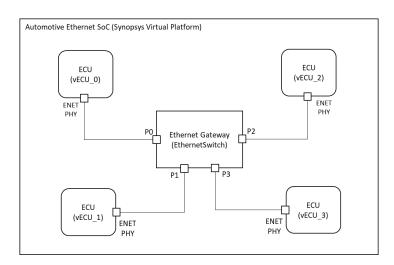
- Combines the TraceCompass multi-node pcap analysis capabilities with Virtualizer's tracing analysis
- User interactions with TraceCompass views synchronized with Virtualizer views







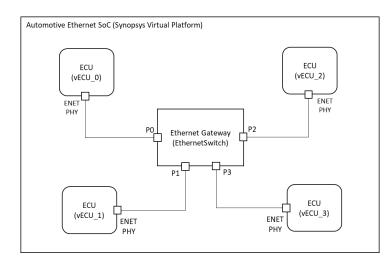
- An Automotive Ethernet scenario
 - Typical Automotive platform with 4x Virtual ECUs connected via Ethernet Switch
 - ping executed across ECUs and analyzed successfully using the integrated solution







- An Automotive Ethernet scenario
 - Typical Automotive platform with 4x Virtual ECUs connected via Ethernet Switch
 - ping executed across ECUs and analyzed successfully using the integrated solution



	(
E VECU	l_0_mac.pcap	ECU_0_mac_tx.p 🛿	Switch_port0_in	E vECU_3_mac_d	rop 🔚 vECU_3_	mac_tx.p "1	19	🎟 Time Chart 📄 5	rsten 'orer &	3 🗉 🏠 🎼	💐 🛼 🎘 🖉 🔹 😵	♦ < < # •	• 8 -
Tir	nestamp	Source	Destination	Reference Pro	otocol Contents			State System / Attr	ibu. 👻	01:31:40	01:32:00 01:32	:20 01:32:40)
🔗 <s< td=""><td>rch></td><td><srch></srch></td><td><srch></srch></td><td><srch> <sr< td=""><td>ch> <srch></srch></td><td></td><td></td><td>▼ vECU_0_mac_tx.pcap</td><td></td><td></td><td></td><td></td><td></td></sr<></srch></td></s<>	rch>	<srch></srch>	<srch></srch>	<srch> <sr< td=""><td>ch> <srch></srch></td><td></td><td></td><td>▼ vECU_0_mac_tx.pcap</td><td></td><td></td><td></td><td></td><td></td></sr<></srch>	ch> <srch></srch>			▼ vECU_0_mac_tx.pcap					
01	31:32.513 178 000	42:55:d6:6a:21:aa	33:33:00:00:00:16	vECU_0_mai ETH	H Source MAC	: 42:55:d6:6a:	2 . , Destinatio					5	
01	31:32.557 183 000	42:55:d6:6a:21:aa	33:33:00:00:00:16	vECU_0_mai ETH	H Source MAC	: 42:55:d6:67	.aa , Destinatic	♥ org.eclipse.linuxtoo total	ls.tmf.statistics.totais	0		12 13	
01	31:33.397 238 000	42:55:d6:6a:21:aa	33:33:ff:6a:21:aa	vECU_0_mai ETH	H Source MAC	.: 42:55:d6;	z1:aa , Destinatio		ls.tmf.statistics.types				
01	31:34.421 275 000	42:55:d6:6a:21:aa	33:33:00:00:00:16	vECU_0_mai ETH	I Source MAC	.: 42:55:d a:	21:aa , Destinatio	▼ event_types		0			
01	31:34.421 335 000	42:55:d6:6a:21:aa	33:33:00:00:00:02	vECU_0_mai ETH	H Source MAC	.: 42:55 o:6a:	21:aa , Destinatio	packet:eth		1 6 7	8 9 10	11 12	13
01	31:35.413 178 000	42:55:d6:6a:21:aa	33:33:00:00:00:16	vECU_0_mai ETH	H Source MAC	.: 42;1_:d6:6a:1	21:aa , Destinatior	packet:ipv4		2		1	2
01	31:38.389 173 000	42:55:d6:6a:21:aa	33:33:00:00:00:02	vECU_0_mai ETH	H Source MAC	.: 41,55:d6:6a:	21:aa , Destinatior						
01	31:46.581 172 000	42:55:d6:6a:21:aa	33:33:00:00:00:02	vECU_0_mai ETH	H Source MAC	: 42:55:d6:6a:	21:aa , Destination						
01	32:02.197 172 000	42:55:d6:6a:21:aa	33:33:00:00:00:02	vECU_0_mai ETH	H Source MA	42:55:d6:6a:	21:aa , Destination	<u> </u>					
01	32:21.662 071 000	42:55:d6:6a:21:aa	ff:ff:ff:ff:ff:ff	vECU_0_mai ETH	H Source MAC	: 42:55:d6:6a:	21:aa , Destination	🐴 Histogram 🛛 Stati	stics 🔲 Properties	nts :	x 🔄 🔍 🔍 📖 🛺 🔌	୍ କ 🕫 🖶 🗟	1 8 -
			42:55:d6:6a:21:bb/1	1 vECU_0_ma IPV			=7454 Len=68						
01	32:26.771 643 000	42:55:d6:6a:21:aa	42:55:d6:6a:21:bb	vECU_0_mai ETH	Source 10	: 42:55:d6:6a:	21:aa , Destination I	Diff = 1941663565	953 ns			8:1941 53565953 ns	
01	32:35.989 173 000	42:55:d6:6a:21:aa	33:33:00:00:00:02	vECU_0_mai ETH	i Sou 2	: 42:55:d6:6a:	21:aa , Destination I	3			1500 s		2000 s
01	32:55.459 687 000	42:55:d6:6a:21:aa	ff:ff:ff:ff:ff:ff	vECU_0_mai ETH	I Source of	: 42:55:d6:6a:	21:aa , Destination N	Select Function	fpsimd_thread_switch hrtimer_active				
01	32:55.461 213 000	42:55:d6:6a:21:aa/	42:55:d6:6a:21:cc/1	vECU_0_mai IPV	4 10.1.1.1 >	10.1.1.3 [DF] Id	i=58054 Len=68	Register Trace	brtimer cancel	MAC 0x0			
01	33:00.571 563 000	42:55:d6:6a:21:aa	42:55:d6:6a:21:cc	vECU_0_ma ETH	H Source MAC	: 42:55:d6:6a:	21:aa , Destination M	EQOS MAC	DWC EQoS.EQOS — MAC Configuration	ion=0x1C 0x2000 DISABLE		0×10 ENAB	02003
	33:27.152 394 000			vECU_0_mai ETH		:: 42:55:d6:6a:	21:aa , Destination M	Configure - Platform I	TE-ENABLE	DISABLE		ENAB	
			42:55:d6:6a:21:dd/	1 vECU_0_mai IPV	4 10.1.1.1 >	10.1.1.4 [DF] Id	i=63589 Len=68	configure + Hadonin	- PRELEN=PRELI - Reserved DC=				- · · ·
01	33:32.309 914 000	42:55:d6:6a:21:aa	42:55:d6:6a:21:dd	vECU_0_mai ETH	H Source MAC	: 42:55:d6:6a:	21:aa , Destination M/		Reserved_BL= MAC_Configura	0x0 0x0			
01	33:39.477 160 000	42:55:d6:6a:21:aa	33:33:00:00:00:02	vECU_0_mai ETH	Source MAC	: 42:55:d6:6a:	21:aa , Destination M/		- Reserved_DR=	0x0			
- Filter	s 🕕 Bookmarks 📒	Stream List 🛛 😻 Result	s 📴 Details 🕱								A	🕹 🔂 🖬	* : -
astTrac	k Messages Trace												
Time (ps)			= Object N	lessage					▼ Category	= Core	Name = PC	
94166	2839458000 Auto	omotiveMultiECU VDK	VECU 0.DWC EOoS D	OUT.DWC EOoS C	hannel 0: [RX] descr	read @0xff040	010 [0xfafff800:0x000	00000:0x00000000:0x8	10000001	Internal Level 0	EQoS_DUT.DWC	EQoS) 0x0	n
194166	3565708000 Auto	omotiveMultiECU_VDK	VECU O.DWC EQOS D	OUT.DWC EQoS C	hannel 0: [TX] descr	read @0xff042	0a0 [0xfb104000:0x00	000000:0x00000062:0>	b00000621	Internal Level 0	EQoS_DUT.DWC		n
194166	3565708000 Auto	omotiveMultiECU_VDK	VECU 0.DWC EQoS D	OUT.DWC EQoS C	hannel 0: [TX] buffer	1 read @0xfb]	04000 , size 0x62			Internal Level 0	EQoS DUT.DWC	EQoS) 0x0	r
194166	3565953000 Auto	omotiveMultiECU_VDK	VECU 0.DWC EQOS E	OUT.DWC EQoS C	hannel 0: [TX] sent p	acket to Queue	0 [Queue Size = 0x1]			Internal Level 0	EQoS DUT.DWC	EQoS) 0x0	n
94166	3565953000 Auto	omotiveMultiECU_VDK	VECU 0.DWC EQOS D	OUT.DWC_EQoS_Q	ueue 0: [TX] sent p	acket to MAC [(Queue Size = 0x0 , 0x6a	d65542:0x5542bb21:0x	aa216ad6:0x00	Internal Level 0	EQoS_DUT.DWC	EQoS) 0x0	n
94166								:0xaa216ad6:0x004500		Internal Level 0	EQoS_DUT.DWC		r
	3566809000 Auto	omotiveMultiECU VDK	VECU 0.DWC EQoS E	OUT.DWC EQoS C	hannel 0: [TX] descr	write @0xff04:	20a0 [0x00000000:0x0	0000000:0x00000000:0	x300000001	Internal Level 0	EQoS DUT.DWC	EQoS) 0x0	r
194166													
		omotiveMultiECU VDK	VECU 0.DWC EOoS D	OUT.DWC EOoS C	hannel 0: [TX] descr	read @0xff042	0b0 [0x00000000:0x00	0:000000:0x00000000:0	x00000000 4	Internal Level 0	EQoS DUT.DWC	EOoS) 0x0	n,

DESIGN AND VERI

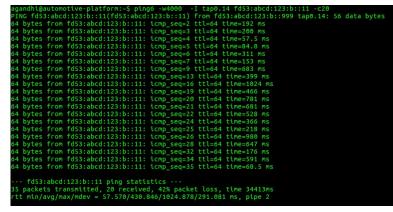


- Ethernet packet loss analysis in an Automotive platform using AUTOSAR
 - 2x ECUs and external interface connected to host ethernet adapter
 - packet losses observed for a VLAN application





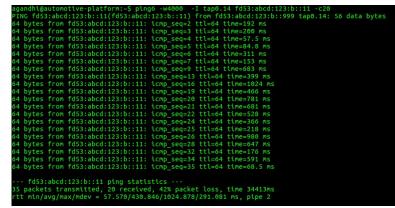
- Ethernet packet loss analysis in an Automotive platform using AUTOSAR
 - 2x ECUs and external interface connected to host ethernet adapter
 - packet losses observed for a VLAN application

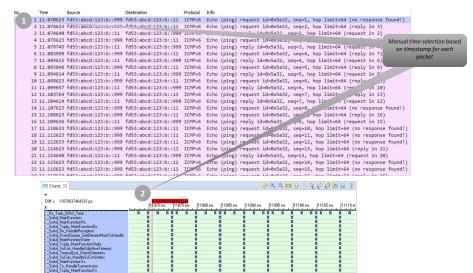






- Ethernet packet loss analysis in an Automotive platform using AUTOSAR
 - 2x ECUs and external interface connected to host ethernet adapter
 - packet losses observed for a VLAN application

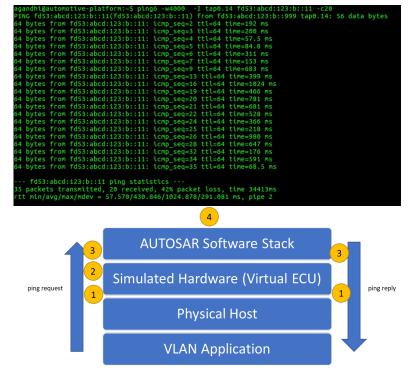


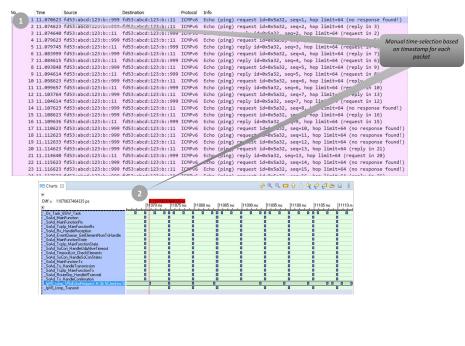






- Ethernet packet loss analysis in an Automotive platform using AUTOSAR
 - 2x ECUs and external interface connected to host ethernet adapter
 - packet losses observed for a VLAN application



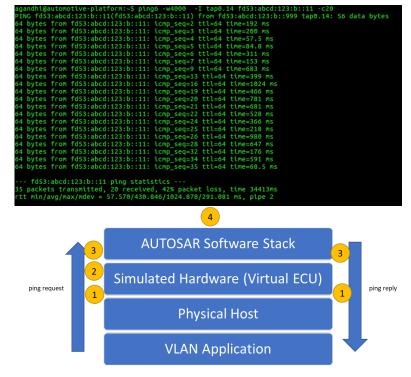


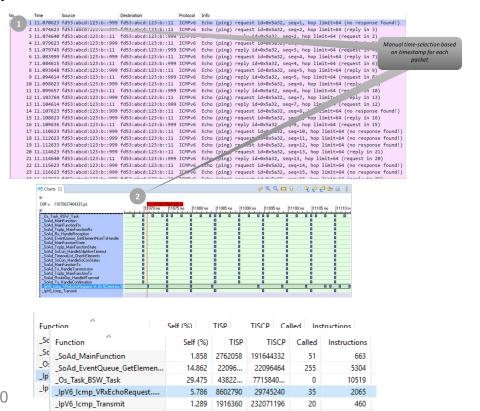
DESIGN AND VERIFICATIO

CONFERENCE AND EXHIBITION



- Ethernet packet loss analysis in an Automotive platform using AUTOSAR
 - 2x ECUs and external interface connected to host ethernet adapter
 - packet losses observed for a VLAN application



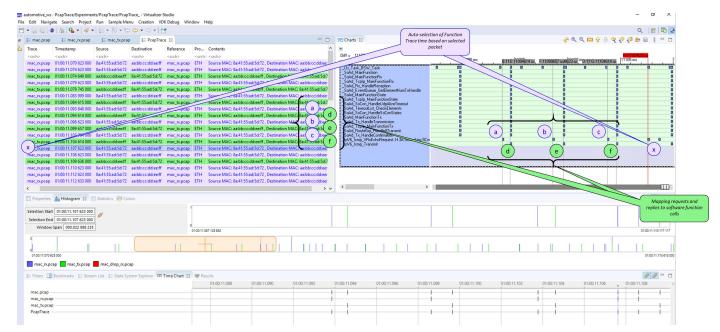


DESIGN AND VERIFICATION

CONFERENCE AND EXHIBITION



• Ethernet packet loss analysis in an Automotive platform using AUTOSAR



- Manual analysis v/s integrated solution analysis compared
- Significant time saved in reaching to the root cause of the issue





- Software Bug analysis for faster debug
 - stmmac bug analysis speedup using integrated solution



 Enhanced Scheduling Traffic (EST) visualization for Time Sensitive Networking





DESIGN AND VERIFICATION

CONFERENCE AND EXHIBITION

Conclusions

- Successful holistic visualization of ethernet transactions in Virtual Platform
- Expedite ethernet software debugging and analysis
- Leveraging TraceCompass with Virtualizer is valuable to accelerate Automotive Ethernet validation
- TraceCompass' network protocol support can be extended for broader usage



Questions



