# DVCon India Awards 2025

- Best Educational Institution
- Outstanding Contribution in Design/Verification - Individual
- Woman Achiever in Semiconductor Industry
- Exemplary Contribution in Design/Verification - Company
- Lifetime Achievement

# Best Educational Institution

- End-to-end **design-to-prototype** capability using industry-standard VLSI tools.
- **Advanced fabrication** ecosystem with cleanrooms and micro/nano-fab enabling device research.
- Facilitated student involvement: hosted Mentor **Chip-athon** groups (CAG), VLSI workshop (250+ participants).
- **Specialized programs:** M.Tech in **System-on-Chip (SoC)** Design; strong industry collaboration via **IPTIF**.
- Empowering the **next generation of engineers**.

Indian Institute of Technology, Palakkad

# Outstanding Contribution – Individual

- **25+ years** leading SoC **design verification** across startups and global tech leaders.
- **First-pass silicon** on multiple complex SoCs; established scalable DV methodologies.
- Trailblazer in making DV errors and mentored many industry professionals.
- **Enduring impact** through technical achievements, setting benchmarks in SoC verification practices and mentorship of future leaders.
- **Industry influencer**: active contributor to DVCon India, driving verification ecosystem growth.

Ashok Kumar Natarajan

# Woman Achiever in Semiconductor Industry

- **35 years in semiconductors**, with 20+ years in leadership roles.
- Delivered **key SoCs for multiple tech giants**.
- Early career in US. Returned to India in 2003, breaking barriers and **driving cultural transformation**.
- Emp... consistently high-performing teams and mentors future leaders.
- **Champion for women in engineering**: drives retention, career growth, and gender equity initiatives.
- Recognized as a **trailblazer and role model** for both junior and senior professionals.

## Namratha Jaisimha

# Exemplary Contribution – Company

- **Consistent, multi-year** contributions to DVCon India through paper submissions and community engagement.
- Driving innovation across **design & verification** domains.

Intel India

# Lifetime Achievement

- **40+ years of experience** in electronics and semiconductor industry.
- **Pivotal contributions to the design & verification community** through sponsorships, leadership roles, technical initiatives, and ecosystem building.
- A pioneer in **Indian electronics**.
- Seasoned operator across regional operations, sales, and cross-functional teams; recognized for **integrity** and **leadership**.
- Recognized for **integrity**, **passion**, and **community engagement**.
- A pioneer in semiconductor test and engineering services.
- **Community champion**: active in Rotary, educational trusts, and grassroots initiatives; supports STEM education.
- **Advocate for collaboration** between industry, academia, and government to strengthen India's semiconductor ecosystem.
- **Chairperson, India Electronics & Semiconductor Association (IESA)**, driving India's ESDM growth.

Dr. V Veerappan

**10th EDITION**

DESIGN AND VERIFICATION
**DVCON**
CONFERENCE AND EXHIBITION
**INDIA**

Architecture to Analytics **A2A**
**DVCON 2025**
September 10-11, 2025 | Radisson Blu, Marathahalli, Bangalore

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