

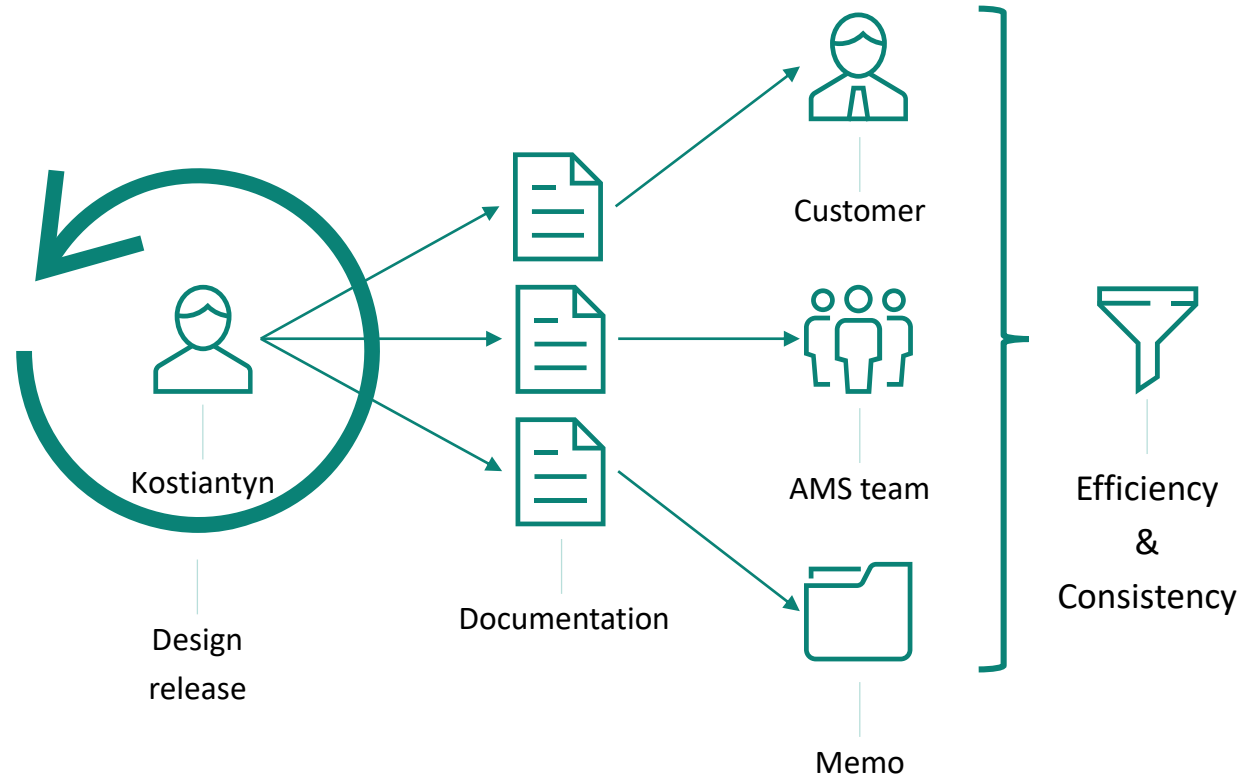
2025
DESIGN AND VERIFICATION™
DVCON
CONFERENCE AND EXHIBITION
EUROPE
MUNICH, GERMANY
OCTOBER 14-15, 2025

Automated PDF Reporting in MSV Workflow

Barabanov Kostiantyn
Suessmuth Clemens



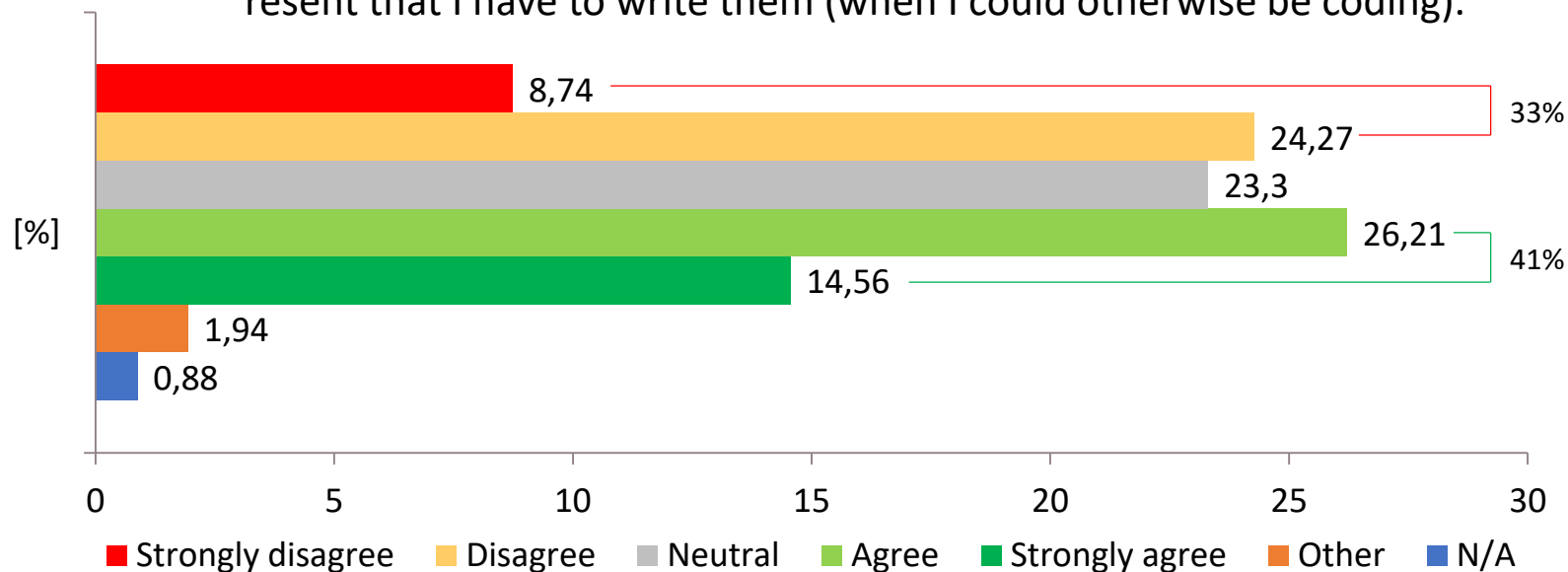
Agenda



Agenda



#9 Although I contribute to documentation, I dislike writing docs and resent that I have to write them (when I could otherwise be coding).



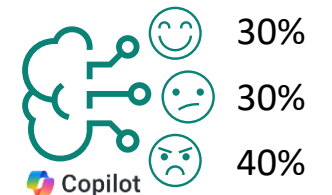
Engineer Sentiment Toward Documentation



by Tom Johnson

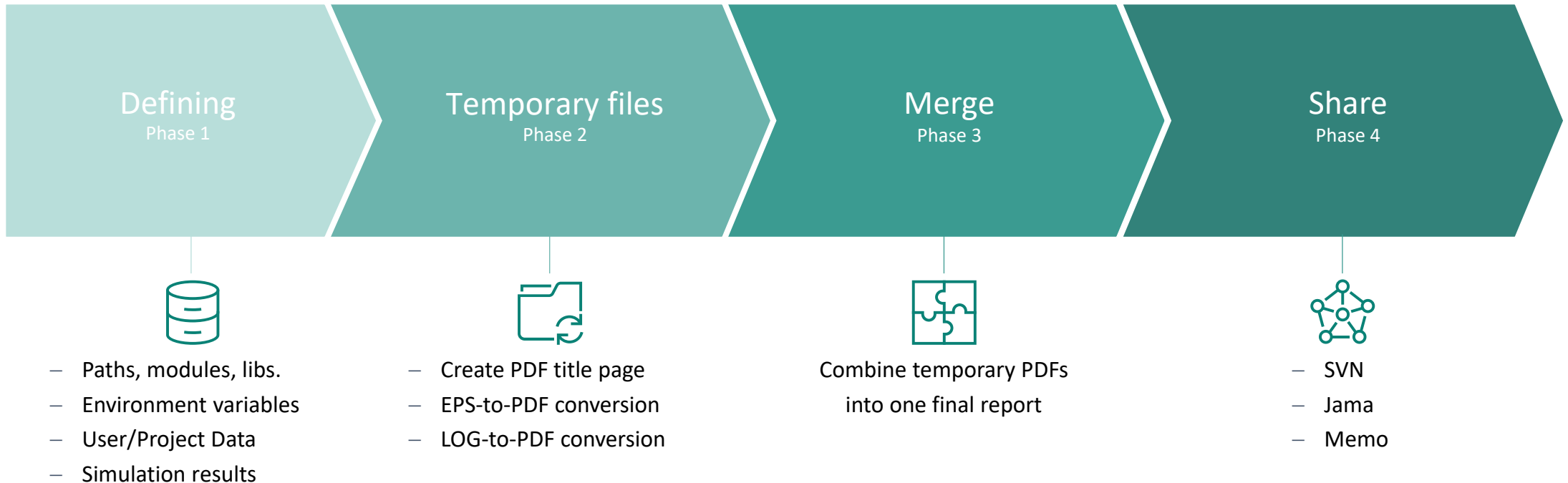
Mar 21, 2019

103 responses



<https://surveys.questionpro.com/a/t/PE5tIZduq7>

Agenda



Agenda

AMS Statistics Overview **Title page** EPS-to-PDF LOG-to-PDF Merge Summary Q&A

```
🐪 PERL (main process)
use PDF::Create;

my $page = $pdf->new_page ( 'MediaBox' =>
    $pdf->get_page_size('A4L') );

my $img1 = $pdf->image('Header.jpg');
my $img2 = $pdf->image('Logo.jpg');

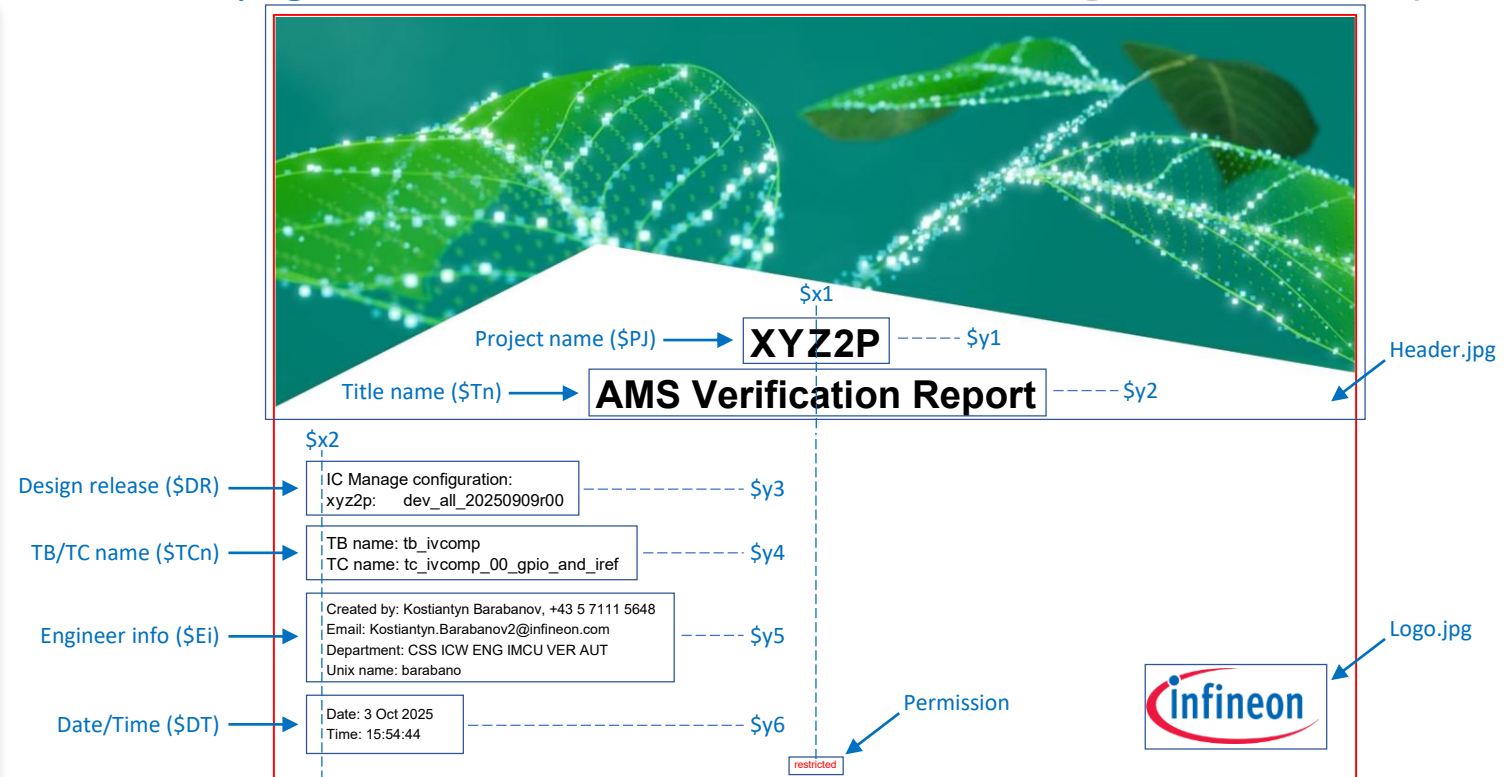
my $f1 = $pdf->font('BaseFont' => 'Helvetica');
my $f2 = $pdf->font('BaseFont' => 'Helvetica-Bold');

$page->stringc($f2, 30, $x1, $y1, $PJ);
$page->stringc($f2, 30, $x1, $y2, $Tn);

$page->stringl($f1, 13, $x2, $y3, $DR);
$page->stringl($f1, 13, $x2, $y4, $TCn);
$page->stringl($f1, 11, $x2, $y5, $Ei);
$page->stringl($f1, 11, $x2, $y6, $DT);
$page->stringc($f1, 8, $x1, $y7, 'restricted');

$pdf->close;
```

<https://metacpan.org/pod/PDF::Create>



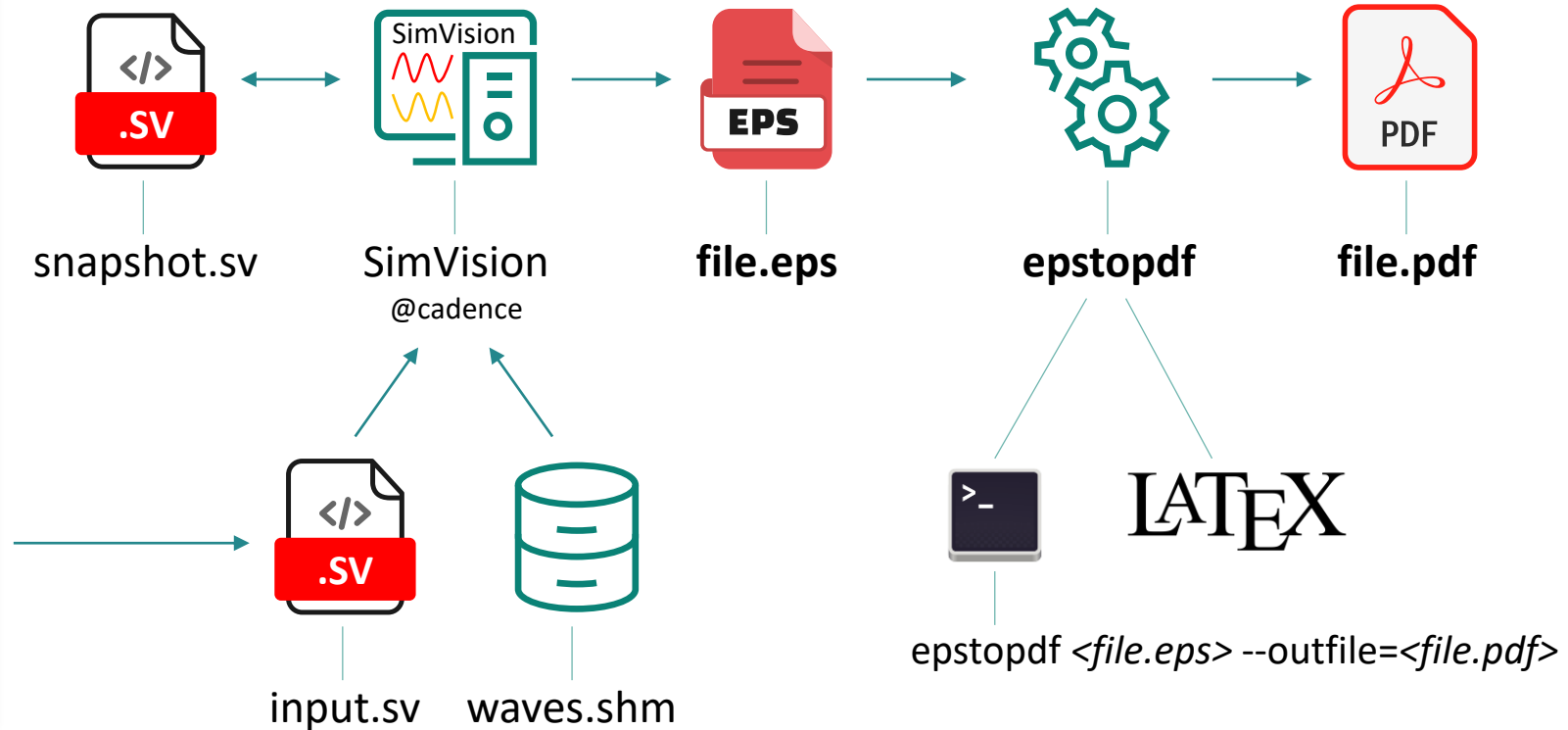
Agenda

AMS Statistics Overview Title page **EPS-to-PDF** LOG-to-PDF Merge Summary Q&A

PERL (main process)

```
# input.sv file
open my $input_sv, '>', 'input.sv';
print $input_sv "waveform print
-using \"Waveform 1\"
-file \"file.eps\"
-variables \"all\"
-time \"visible\"
-designer \"$user_name\"
-company \"Infineon\"
-title \"$stc_name\"
-paper \"A4\"
-orientation \"landscape\"
exit";
close $input_sv;

# SimVision automated run
system("simvision -64
./rundir/waves.shm
-input snapshot.sv
-input input.sv");
```



Agenda

AMS Statistics Overview Title page EPS-to-PDF **LOG-to-PDF** Merge Summary Q&A

 PYTHON (child)

```
from fpdf import FPDF
import textwrap
```

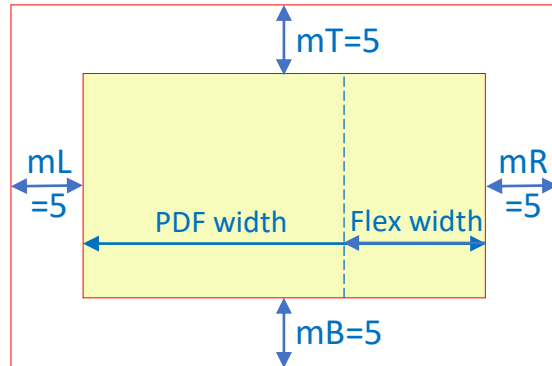
```
pdf = FPDF(orientation='L',
unit='mm', format='A4')
```

```
w = pdf.get_string_width('*')
```

```
pdf.set_font("Courier", size=8)
```

```
pdf.cell(0, h, "Hello World!")
```

```
pdf.output("file.pdf")
```



Customized header
Wrap by last slash before margin right

Font: 8h 9h
w w
Font: Courier, size 8
w=w=w=w=w=w=w=w=w

POST-SIMULATION OCEAN REPORT:

Path: /home/boy2p.ivcs/barabano/barabano_boy2p_1.0-dev_boy2p_soc_tb/msvboy2p/tb/1ab_msv_boy2p/tb_lpcomp/tc_lpcomp_00_gpio_and_iref/rundir/meas_output.log
Name: barabano
Date: 30 Sep 2025
Time: 11:51:51

No	Type	Var. Name	Meas. value	Expected val.	Spec. Min.	Spec. Max.	P/F	Description
0	[---	t1	1.726e-03	AVAILABLE			PASS	cross. time, rise edge of 'p8_0_pad' PAD @ 1.1V
0	[---	t2	2.226e-03	AVAILABLE			PASS	cross. time, rise edge of 'p8_0_pad' PAD @ 2.2V
0	[---	tm	1.000e-04	AVAILABLE			PASS	++ defined time
0	[---	tc1	1.826e-03	AVAILABLE			PASS	measure time tcrl + tm
0	[---	tc2	2.326e-03	AVAILABLE			PASS	measure time tcrl + tm
1	[REG]	r_config_h	80000000	80000000			PASS	(HEX val.) LPCOMP Configuration register, measure @ tml
2	[REG]	r_cmp0_ctrl_h	3	3			PASS	(HEX val.) comparator 0 control register, measure @ tml
3	[REG]	r_cmp1_ctrl_h	3	3			PASS	(HEX val.) comparator 1 control register, measure @ tml
4	[REG]	r_cmp0_sw_h	11	11			PASS	(HEX val.) comparator 0 switch control register, meas. @ tml
5	[REG]	r_cmp1_sw_h	11	11			PASS	(HEX val.) comparator 1 switch control register, meas. @ tml
6	[ANA]	i_iref	2.496e-07		2.475e-07	2.525e-07	PASS	'iref_250n_flow' ref. current of 'u_mxs40lpcomp_top' block, meas. @ tml
7	[ANA]	v1_wk0	2.210e-09		-1.000e-02	1.000e-02	PASS	'out_wk0' output voltage of 'u_mxs40lpcomp_top' block measured @ tml
8	[ANA]	v2_wk0	2.700e+00		2.673e+00	2.727e+00	PASS	'out_wk0' output voltage of 'u_mxs40lpcomp_top' block measured @ tm2
9	[ANA]	v1_wk1	2.700e+00		2.673e+00	2.727e+00	PASS	'out_wk1' output voltage of 'u_mxs40lpcomp_top' block measured @ tml
10	[ANA]	v2_wk1	3.370e-09		-1.000e-02	1.000e-02	PASS	'out_wk1' output voltage of 'u_mxs40lpcomp_top' block measured @ tm2
11	[PWR]	vcc*	1.100e+00		1.089e+00	1.111e+00	PASS	LV 'vccact' voltage value of 'u_mxs40lpcomp_top' block, measured @ tml
12	[PWR]	vcc*	1.100e+00		1.089e+00	1.111e+00	PASS	LV 'vccdpalp' voltage value of 'u_mxs40lpcomp_top' block, measured @ tml
13	[PWR]	vcc*	1.100e+00		1.089e+00	1.111e+00	PASS	LV 'vccret' voltage value of

Defining X value of Description column

Defining line index

Wrap by words

Agenda

AMS

Statistics

Overview

Title page

EPS-to-PDF

LOG-to-PDF

Merge

Summary

Q&A



IC Manage configuration:
 xyZp: dev_all_20250909U00

TB name: tb_lvcomp
 TC name: tc_lvcomp_00_gpio_and_irq

Created by: Kostiantyn Barabanov - +43 5 7111 5648
 Email: Kostiantyn.Barabanov2@infineon.com
 Department: CSS ICW ENG IMCU VER AUT
 User name: barabanov

Date: 3 Oct 2025
 Time: 15:54:44

XYZ2P
 AMS Verification Report

```

SUMMARY OF WARNING/ERRORS PRESENT IN THE LOG FILES:

Batch: /home/vx2p2-1vcn/Workspace/Workspace_vx2p2_1-0-dev_vx2p2_bsc1/naevy2p2/t2/lab_nae_vx2p2/t2_incump/t2_vcomp_03_gpio_and_ioeef
t2_vcomp_03_gpio_and_ioeef.log
Batch: 10 Apr 2015
Time: 11:51:11

LOGFILE /home/vx2p2-1vcn/Workspace/Workspace_vx2p2_1-0-dev_vx2p2_bsc1/naevy2p2/t2/lab_nae_vx2p2/t2_incump/t2_vcomp_03_gpio_and_ioeef/naevy2p2
naevy2p2.log
# 0 Warning/Errors are Found!

LOGFILE /home/vx2p2-1vcn/Workspace/Workspace_vx2p2_1-0-dev_vx2p2_bsc1/naevy2p2/t2/lab_nae_vx2p2/t2_incump/t2_vcomp_03_gpio_and_ioeef/naevy2p2
naevy2p2.log
# 1 No Warnings Found
# 1 No Errors Found

LOGFILE /home/vx2p2-1vcn/Workspace/Workspace_vx2p2_1-0-dev_vx2p2_bsc1/naevy2p2/t2/lab_nae_vx2p2/t2_incump/t2_vcomp_03_gpio_and_ioeef/naevy2p2
naevy2p2_bsc1.log
# 0 Warning/Errors are Found!

LOGFILE /home/vx2p2-1vcn/Workspace/Workspace_vx2p2_1-0-dev_vx2p2_bsc1/naevy2p2/t2/lab_nae_vx2p2/t2_incump/t2_vcomp_03_gpio_and_ioeef/naevy2p2
naevy2p2_bsc1.log
# 0 Warning/Errors are Found!

LOGFILE /home/vx2p2-1vcn/Workspace/Workspace_vx2p2_1-0-dev_vx2p2_bsc1/naevy2p2/t2/lab_nae_vx2p2/t2_incump/t2_vcomp_03_gpio_and_ioeef/naevy2p2
naevy2p2_bsc1.log
# 0 Warning/Errors are Found!

LOGFILE /home/vx2p2-1vcn/Workspace/Workspace_vx2p2_1-0-dev_vx2p2_bsc1/naevy2p2/t2/lab_nae_vx2p2/t2_incump/t2_vcomp_03_gpio_and_ioeef/naevy2p2
naevy2p2_bsc1.log
# 0 Warnings are present, please check the log file.

# 0 Errors are present, please check the log file.

LOGFILE /home/vx2p2-1vcn/Workspace/Workspace_vx2p2_1-0-dev_vx2p2_bsc1/naevy2p2/t2/lab_nae_vx2p2/t2_incump/t2_vcomp_03_gpio_and_ioeef/naevy2p2
naevy2p2_bsc1.log
# 0 Warnings are present, please check the log file.

LOGFILE /home/vx2p2-1vcn/Workspace/Workspace_vx2p2_1-0-dev_vx2p2_bsc1/naevy2p2/t2/lab_nae_vx2p2/t2_incump/t2_vcomp_03_gpio_and_ioeef/naevy2p2
naevy2p2_bsc1.log
# 0 Warnings are present, please check the log file.

*****
00 FINISH (PASS)

```

POST-SIMULATION OCEAN REPORT:							
Path: /home/robert/robert/hardware/hardware_vyrp_1_1-der_vyrp_mw_1/hwvyrp1/00/75ah_mw_vyrp1/00/75ah_mw_vyrp1_00_001_mw_1/robert/							
Name: output_log							
Date: 10 Sep 2025							
Time: 11:51:53							
No	Type	Var. Name	Mean Value	Expected Unit	Spec. Min.	Spec. Max.	P/P Description
0	[---]	0	1.7500E+00	AMPLITUDE			0 [---] 0, rise edge of "tg_0_gpio"
1	[---]	12	2.2324E+02	AMPLITUDE			12 [---] 12, rise edge of "tg_0_gpio"
2	[---]	1	1.0000E+04	AMPLITUDE			2 [---] 1, defined time
3	[---]	1	1.8000E+03	AMPLITUDE			3 [---] 1, measure time ctrl to tx
4	[---]	1	2.5000E+03	AMPLITUDE			4 [---] 1, measure time ctrl to rx
5	[---]	1	0.0000E+00	00000000			5 [---] 1, LPTCM configuration register, measure 0 bit
6	[---]	1					6 [---] 1, register, measure 0 bit
7	[---]	1		3			7 [---] 1, register, measure 0 control
8	[---]	1					8 [---] 1, register, measure 0 bit
9	[---]	1		3			9 [---] 1, register, measure 0 control
10	[---]	1					10 [---] 1, register, measure 0 bit
11	[---]	1		11			11 [---] 1, register, measure 0 control
12	[---]	1					12 [---] 1, register, measure 0 bit
13	[---]	1		11			13 [---] 1, register, measure 0 control
14	[---]	1	2.4746E+07		2.4726E+07		14 [---] 1, "time_250ns" value, constant of "measuring_time_block", measure 0 bit
15	[---]	1	2.2324E+02		-1.0000E+02	1.0000E+02	15 [---] 1, "measuring_time_block", measure 0 bit
16	[---]	1	2.7050E+02		2.4730E+02	2.7270E+02	16 [---] 1, "measuring_time_block", measure 0 bit
17	[---]	1	2.7050E+02		2.4730E+02	2.7270E+02	17 [---] 1, "measuring_time_block", measure 0 bit
18	[---]	1	3.3770E+09		-1.0000E+02	1.0000E+02	18 [---] 1, "measuring_time_block", measure 0 bit
19	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	19 [---] 1, "measuring_time_block", measure 0 bit
20	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	20 [---] 1, "measuring_time_block", measure 0 bit
21	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	21 [---] 1, "measuring_time_block", measure 0 bit
22	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	22 [---] 1, "measuring_time_block", measure 0 bit
23	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	23 [---] 1, "measuring_time_block", measure 0 bit
24	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	24 [---] 1, "measuring_time_block", measure 0 bit
25	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	25 [---] 1, "measuring_time_block", measure 0 bit
26	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	26 [---] 1, "measuring_time_block", measure 0 bit
27	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	27 [---] 1, "measuring_time_block", measure 0 bit
28	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	28 [---] 1, "measuring_time_block", measure 0 bit
29	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	29 [---] 1, "measuring_time_block", measure 0 bit
30	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	30 [---] 1, "measuring_time_block", measure 0 bit
31	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	31 [---] 1, "measuring_time_block", measure 0 bit
32	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	32 [---] 1, "measuring_time_block", measure 0 bit
33	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	33 [---] 1, "measuring_time_block", measure 0 bit
34	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	34 [---] 1, "measuring_time_block", measure 0 bit
35	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	35 [---] 1, "measuring_time_block", measure 0 bit
36	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	36 [---] 1, "measuring_time_block", measure 0 bit
37	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	37 [---] 1, "measuring_time_block", measure 0 bit
38	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	38 [---] 1, "measuring_time_block", measure 0 bit
39	[---]	1	1.1000E+09		1.0000E+09	1.1110E+09	39 [---] 1, "measuring_time_block", measure 0 bit
4							

[illegible]

Agenda



Summary



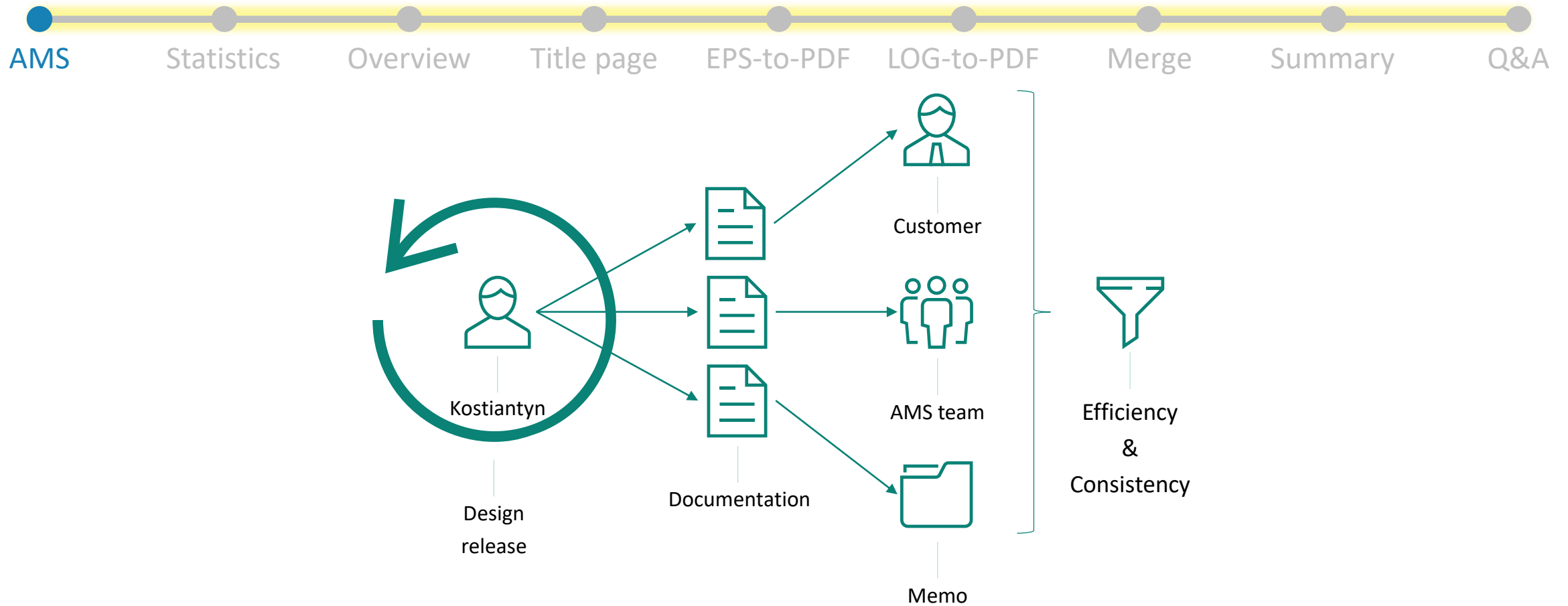
Agenda



Q&A Session



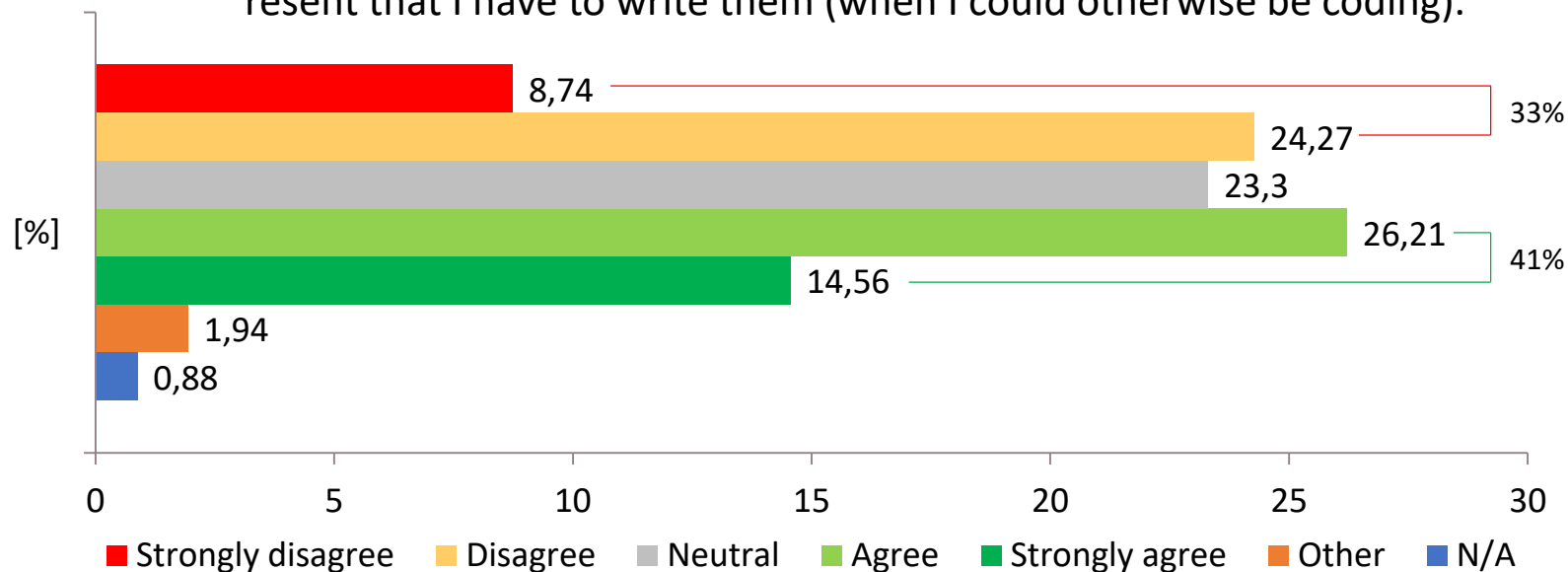
AMS problem



Statistics



#9 Although I contribute to documentation, I dislike writing docs and resent that I have to write them (when I could otherwise be coding).



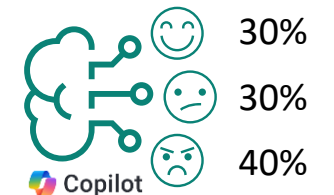
Engineers writing docs



by Tom Johnson

Mar 21, 2019

103 responses



<https://surveys.questionpro.com/a/t/PE5tIZduq7>

Overview



Defining
Phase 1



- Paths, modules, libs.
- Environment variables
- User/Project Data
- Simulation results

Overview



Defining
Phase 1



- Paths, modules, libs.
- Environment variables
- User/Project Data
- Simulation results

Temporary files
Phase 2



- Create PDF title page
- EPS-to-PDF conversion
- LOG-to-PDF conversion

Overview



- Paths, modules, libs.
- Environment variables
- User/Project Data
- Simulation results

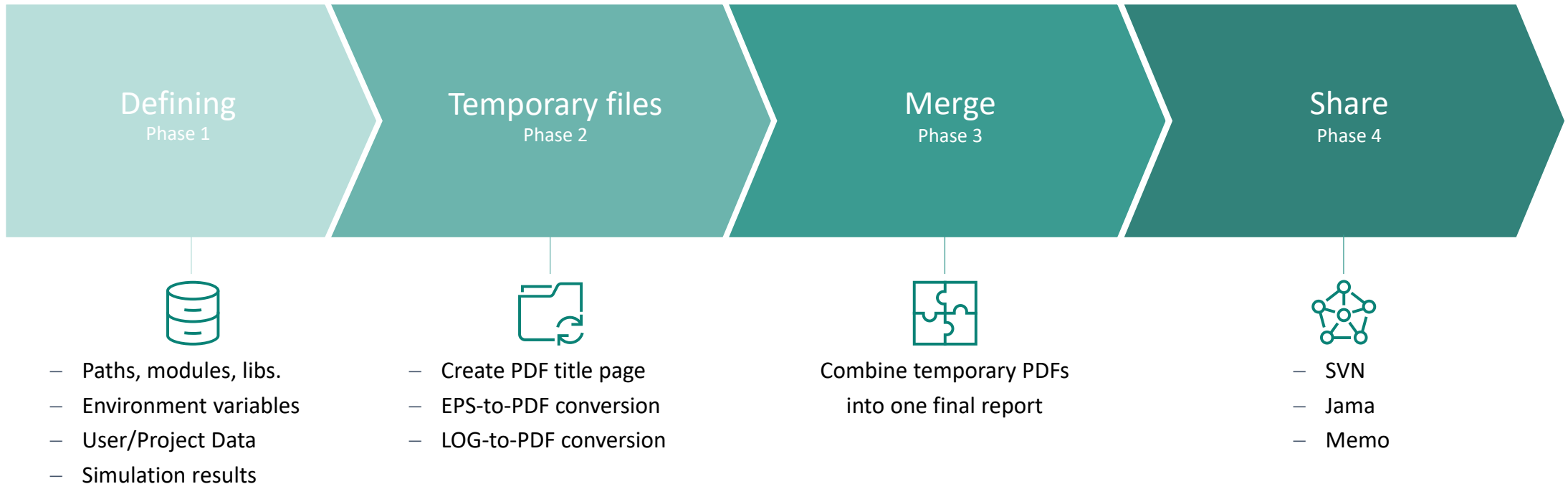


- Create PDF title page
- EPS-to-PDF conversion
- LOG-to-PDF conversion



- Combine temporary PDFs
into one final report

Overview



Title page



 PERL (main process)

```
use PDF::Create;
```

<https://metacpan.org/pod/PDF::Create>

Title page

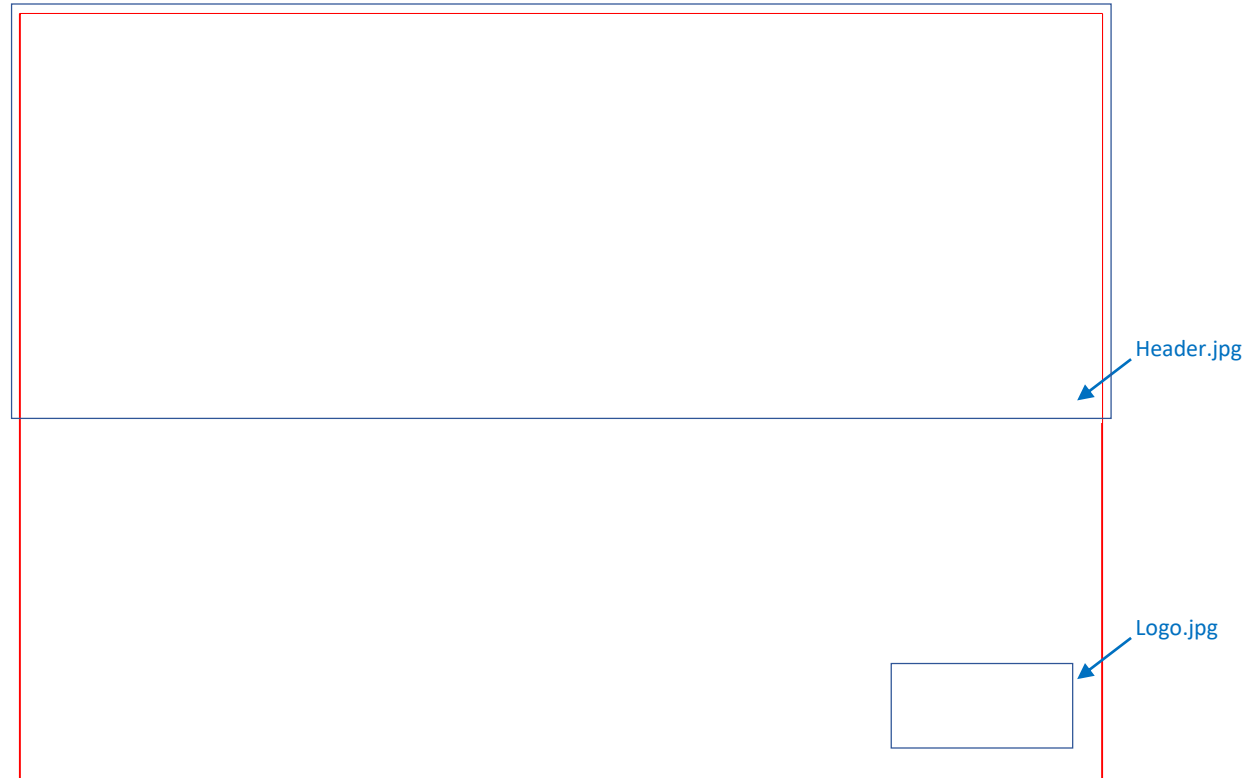


```
🐪 PERL (main process)
use PDF::Create;

my $page = $pdf->new_page ( 'MediaBox' =>
    $pdf->get_page_size('A4L') );

my $img1 = $pdf->image('Header.jpg'); #*
$page->image($img1); #*
xscale => 0.5, # scale image (horizontally)
my $font = $pdf->font('BaseFont' => 'Helvetica');
my $s1 = $pdf->font('BaseFont' => 'Helvetica-Bold');
my $s2 = $pdf->font('BaseFont' => 'Helvetica');
$ypos => 290, # Y position (points)
$xalign => 0, # X align
$yalign => 0, # Y align
);
```

<https://metacpan.org/pod/PDF::Create>



Title page

AMS Statistics Overview **Title page** EPS-to-PDF LOG-to-PDF Merge Summary Q&A

```
🐪 PERL (main process)
use PDF::Create;

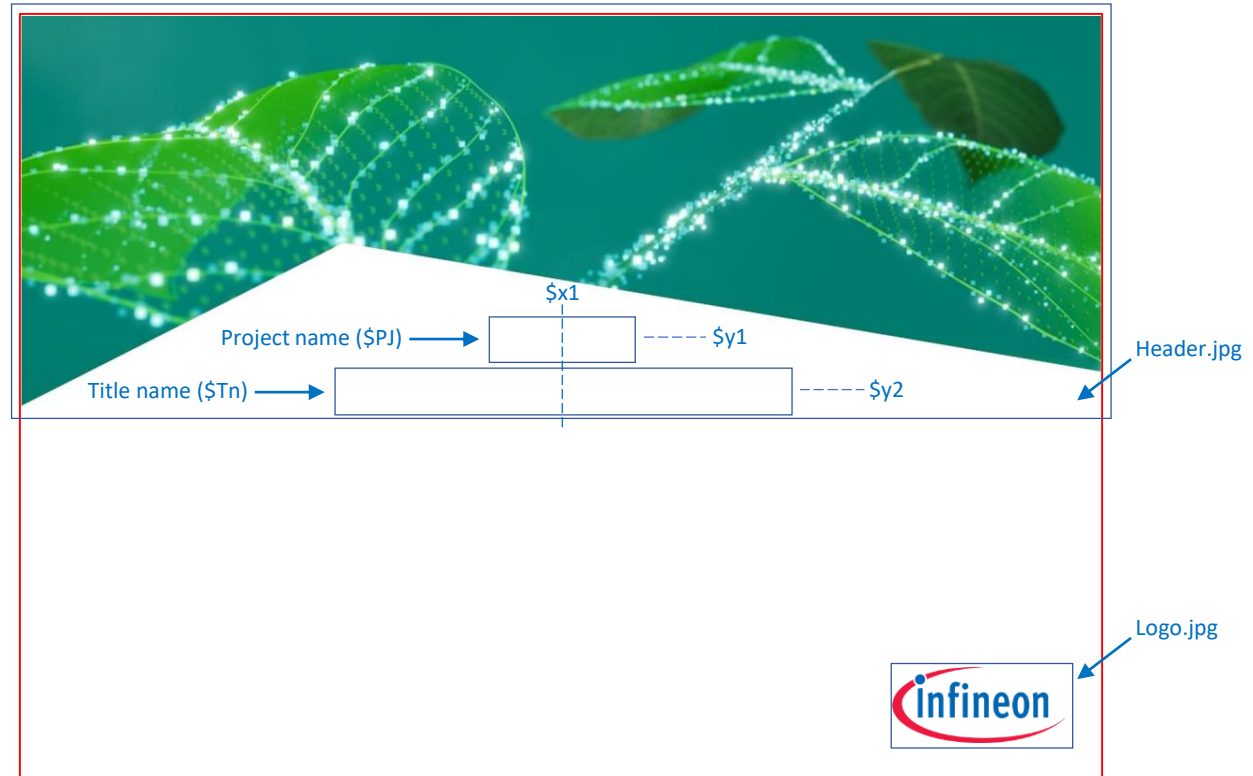
my $page = $pdf->new_page ( 'MediaBox' =>
    $pdf->get_page_size('A4L') );

my $img1 = $pdf->image('Header.jpg'); #*
my $img2 = $pdf->image('Logo.jpg'); #*

my $f1 = $pdf->font('BaseFont' => 'Helvetica');
my $f2 = $pdf->font('BaseFont' => 'Helvetica-Bold');

$page->stringc($f2, 30, $x1, $y1, $PJ);
$page->stringc($f2, 30, $x1, $y2, $Tn);
```

<https://metacpan.org/pod/PDF::Create>



Title page

AMS Statistics Overview **Title page** EPS-to-PDF LOG-to-PDF Merge Summary Q&A

```
🐪 PERL (main process)
use PDF::Create;

my $page = $pdf->new_page ( 'MediaBox' =>
    $pdf->get_page_size('A4L') );

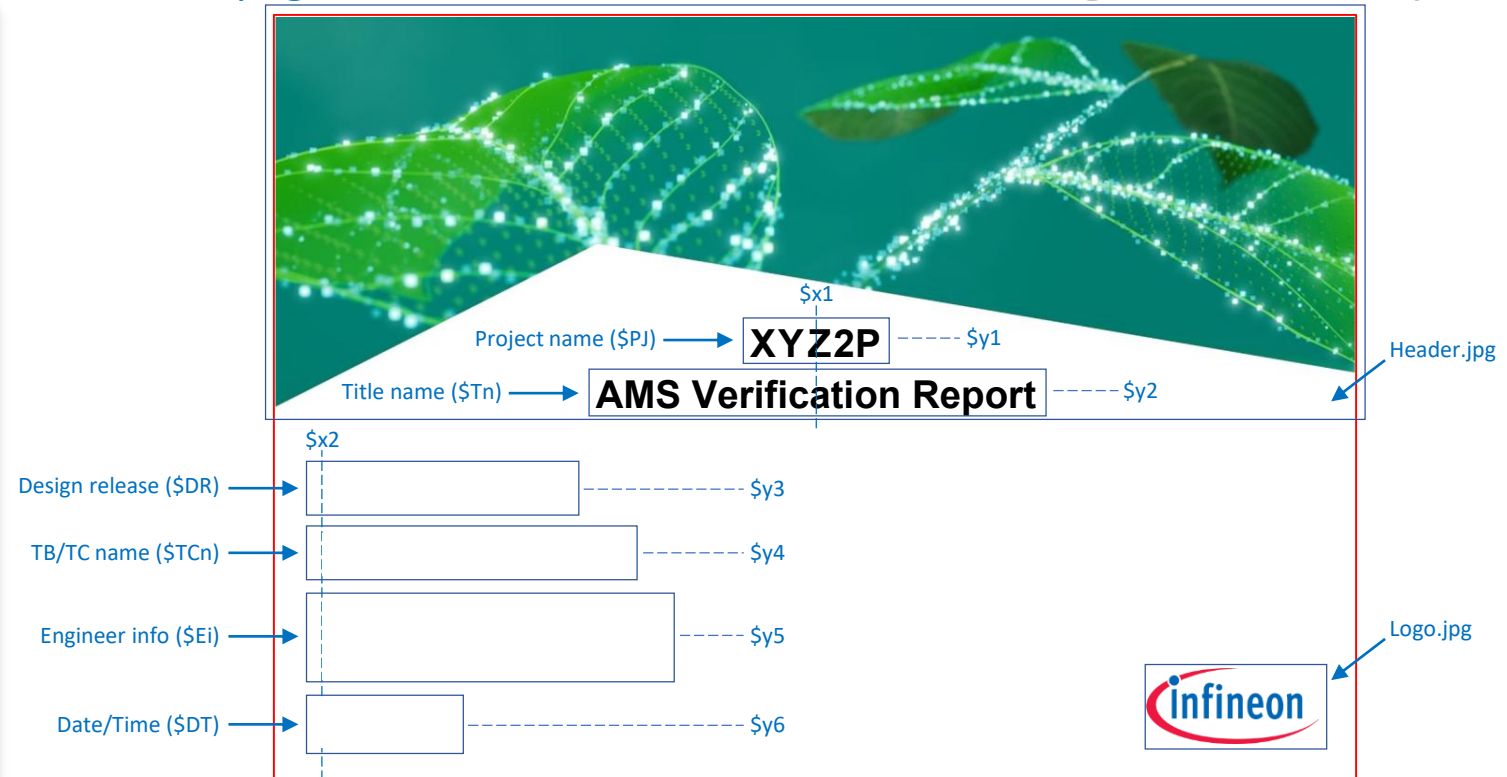
my $img1 = $pdf->image('Header.jpg'); #*
my $img2 = $pdf->image('Logo.jpg'); #*

my $f1 = $pdf->font('BaseFont' => 'Helvetica');
my $f2 = $pdf->font('BaseFont' => 'Helvetica-Bold');

$page->stringc($f2, 30, $x1, $y1, $PJ);
$page->stringc($f2, 30, $x1, $y2, $Tn);

$page->stringl($f1, 13, $x2, $y3, $DR);
$page->stringl($f1, 13, $x2, $y4, $TCn);
$page->stringl($f1, 11, $x2, $y5, $Ei);
$page->stringl($f1, 11, $x2, $y6, $DT);
```

<https://metacpan.org/pod/PDF::Create>



Title page

AMS Statistics Overview Title page EPS-to-PDF LOG-to-PDF Merge Summary Q&A

```
🐪 PERL (main process)
use PDF::Create;

my $page = $pdf->new_page ( 'MediaBox' =>
    $pdf->get_page_size('A4L') );

my $img1 = $pdf->image('Header.jpg'); #*
my $img2 = $pdf->image('Logo.jpg'); #*

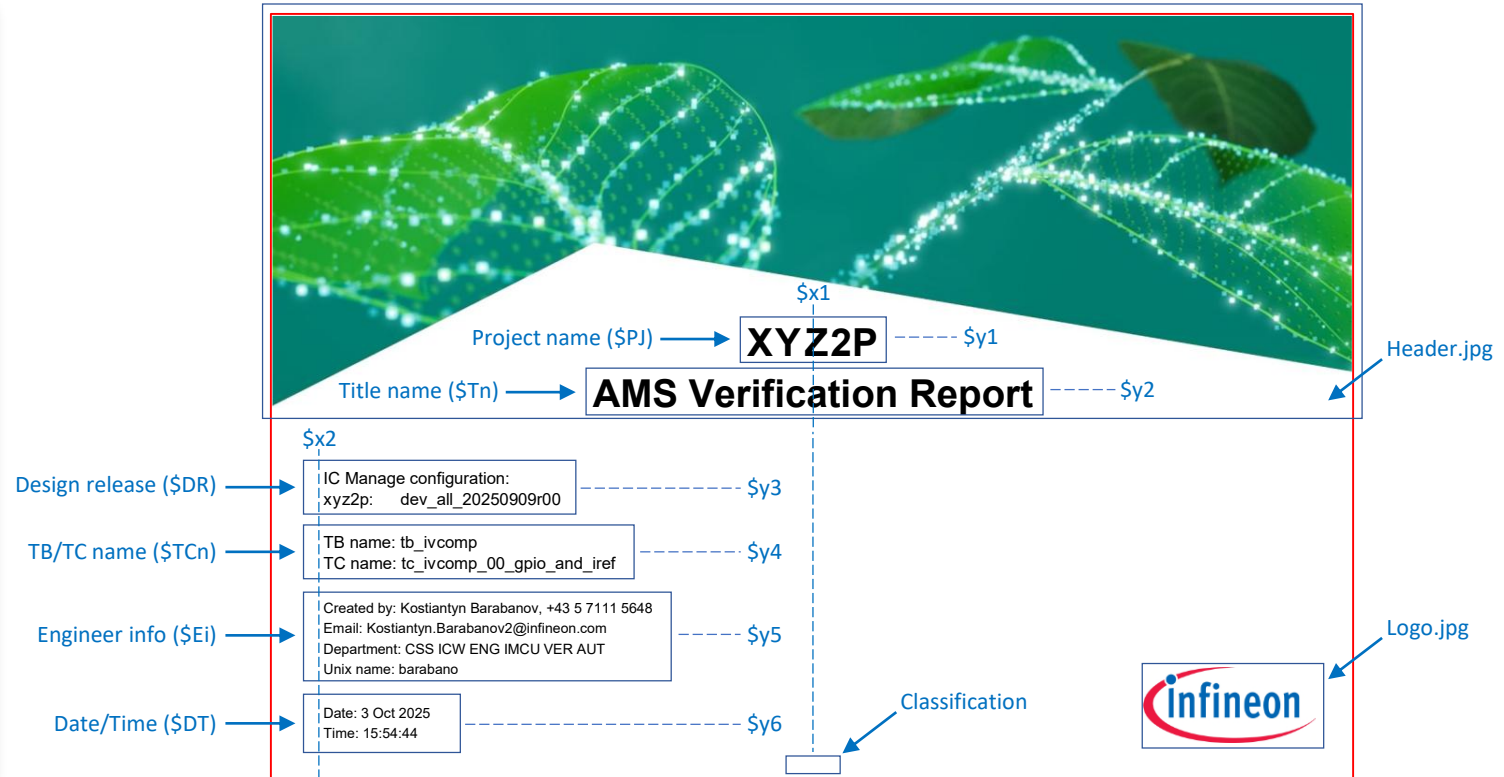
my $f1 = $pdf->font('BaseFont' => 'Helvetica');
my $f2 = $pdf->font('BaseFont' => 'Helvetica-Bold');

$page->stringc($f2, 30, $x1, $y1, $PJ);
$page->stringc($f2, 30, $x1, $y2, $Tn);

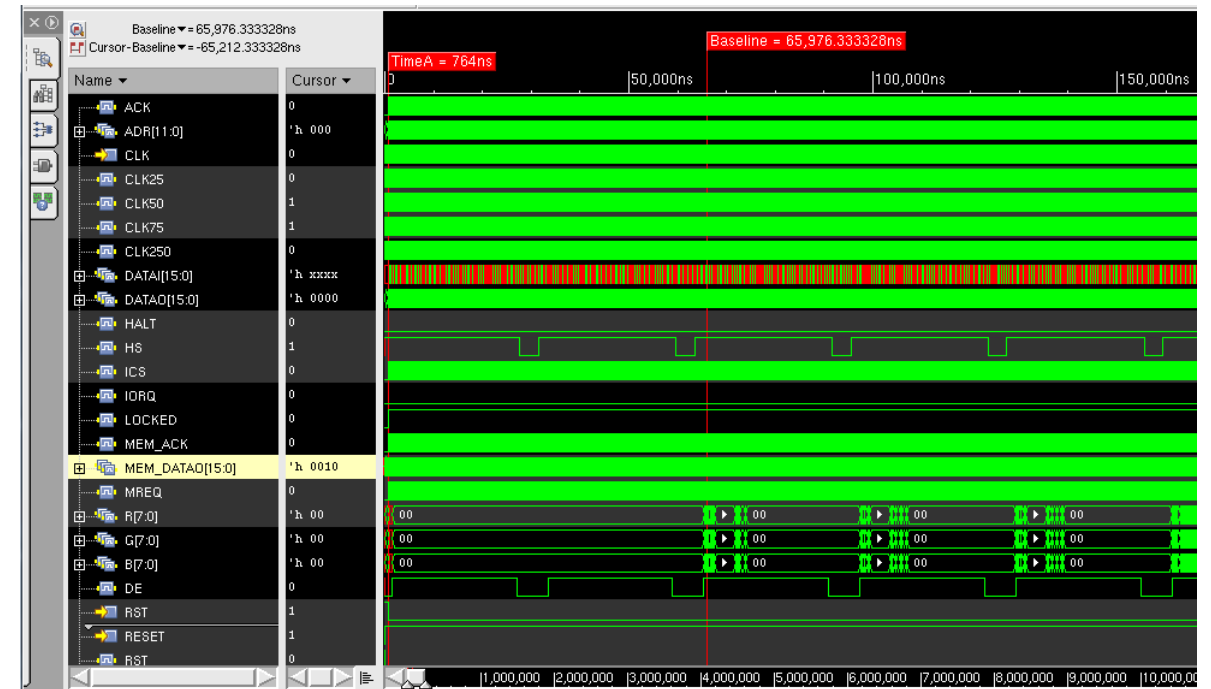
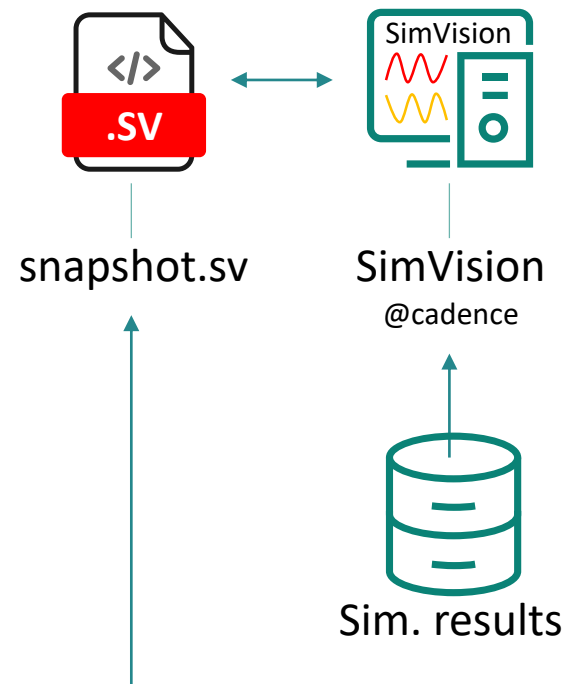
$page->stringl($f1, 13, $x2, $y3, $DR);
$page->stringl($f1, 13, $x2, $y4, $TCn);
$page->stringl($f1, 11, $x2, $y5, $Ei);
$page->stringl($f1, 11, $x2, $y6, $DT);
$page->stringc($f1, 8, $x1, $y7, 'restricted');

$pdf->close;
```

<https://metacpan.org/pod/PDF::Create>

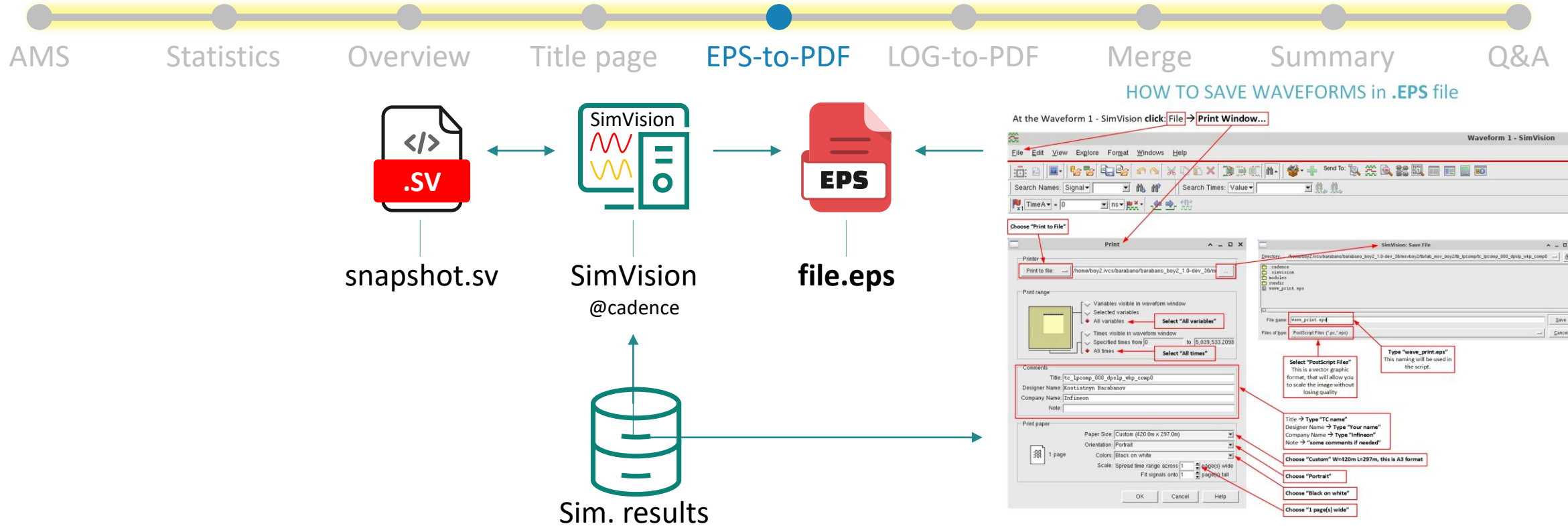


EPS-to-PDF



SimVision waveform window

EPS-to-PDF



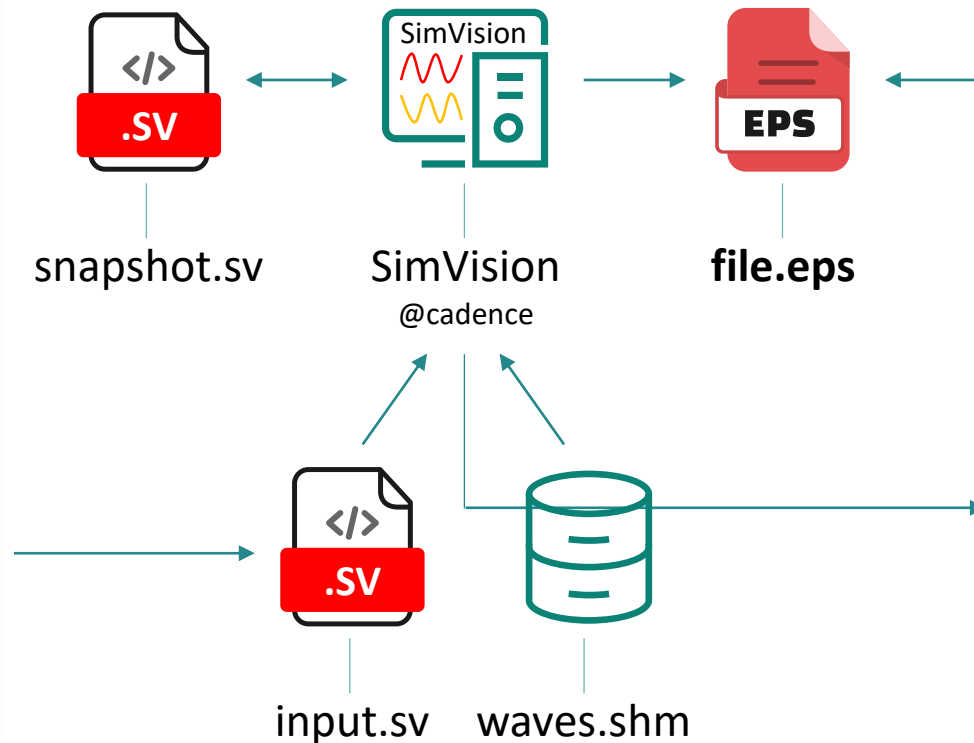
EPS-to-PDF

AMS Statistics Overview Title page **EPS-to-PDF** LOG-to-PDF Merge Summary Q&A

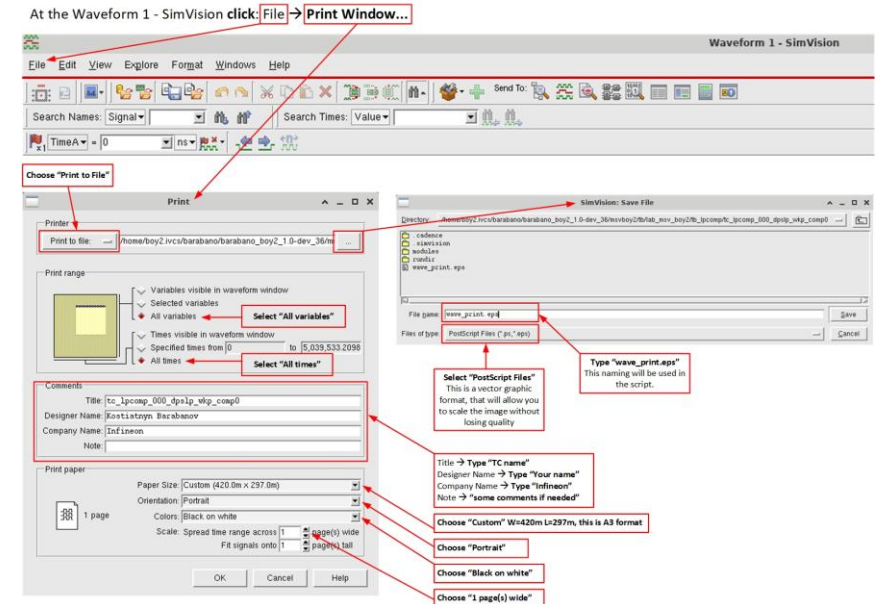
 PERL (main process)

```
# input.sv file
open my $input_sv, '>', 'input.sv';
print $input_sv "waveform print
-using \"Waveform 1\"
-file \"file.eps\"
-variables \"all\"
-time \"visible\"
-designer \"$user_name\"
-company \"Infineon\"
-title \"$tsc_name\"
-paper \"A4\"
-orientation \"landscape\"
exit";
close $input_sv;

# SimVision startup command
system("simvision -64
./rundir/waves.shm
-input snapshot.sv
-input input.sv");
```



HOW TO SAVE WAVEFORMS in .EPS file



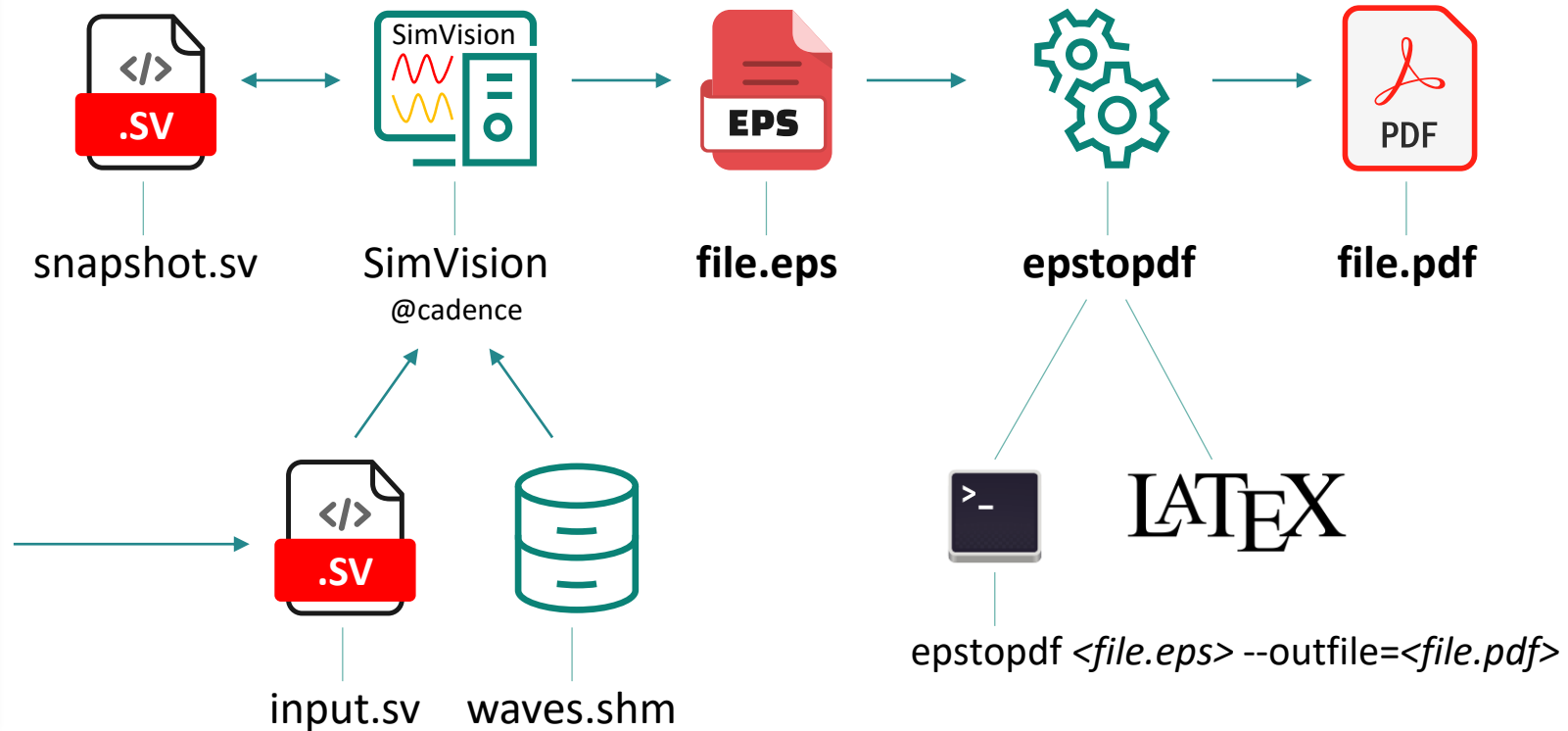
EPS-to-PDF

AMS Statistics Overview Title page **EPS-to-PDF** LOG-to-PDF Merge Summary Q&A

 PERL (main process)

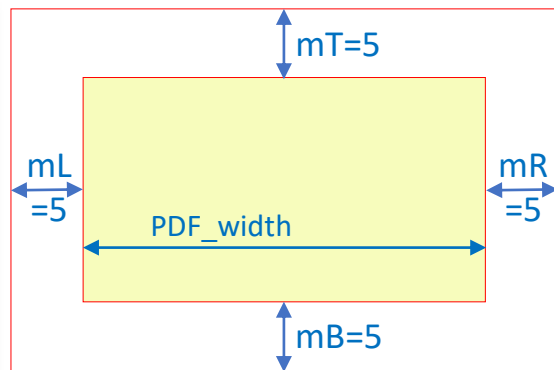
```
# input.sv file
open my $input_sv, '>', 'input.sv';
print $input_sv "waveform print
-using \"Waveform 1\"
-file \"file.eps\"
-variables \"all\"
-time \"visible\"
-designer \"$user_name\"
-company \"Infineon\"
-title \"$stc_name\"
-paper \"A4\"
-orientation \"landscape\"
exit";
close $input_sv;

# SimVision startup command
system("simvision -64
./rundir/waves.shm
-input snapshot.sv
-input input.sv");
```



AMS Statistics Overview Title page EPS-to-PDF **LOG-to-PDF** Merge Summary Q&A

```
pdf.output("file.pdf")
```



Wrap by last slash before margin right

```
if
wrap by "/"
elif
wrap by word
else
wrap by letter
```

[illegible]

Wrap by
words

- Wrap by letters

LOG-to-PDF

AMS Statistics Overview Title page EPS-to-PDF **LOG-to-PDF** Merge Summary Q&A

 PYTHON (child)

```
from fpdf import FPDF
import textwrap
```

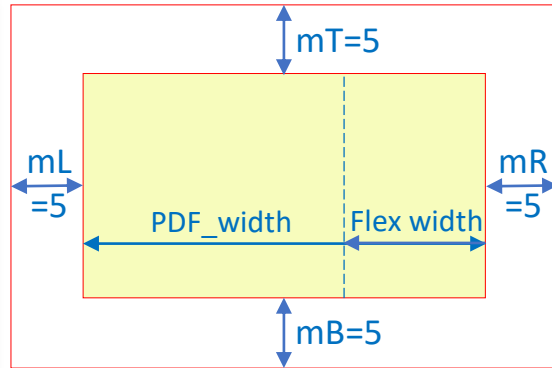
```
pdf = FPDF(orientation='L',
unit='mm', format='A4')
```

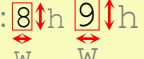
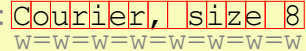
```
pdf.set_font("Courier", size=8)
```

```
w = pdf.get_string_width('*')
h = 4 # line height in mm
```

```
pdf.cell(0, h, "text from .log file")
```

```
pdf.output("file.pdf")
```



Font: 
 Font: 
 W=W=W=W=W=W=W=W=W

Customized header
Wrap by last slash before margin right

POST-SIMULATION OCEAN REPORT:

Path: /home/xyz2p.ivcs/barabano/barabano_xyz2p_1.0-dev_xyz2p_soc_tb/msvxyz2p/tb/lab_msv_xyz2p/tb_ivcomp/tc_ivcomp_00_gpio_and_iref/rundir/meas_output.log
 Name: barabano
 Date: 30 Sep 2025
 Time: 11:51:51

No	Type	Var. Name	Meas. value	Expected val.	Spec. Min.	Spec. Max.	P/F	Description
0	[---	t1	1.726e-03	AVAILABLE			PASS	cross. time, rise edge of 'p8_0_pad' PAD @ 1.1V
0	[---	t2	2.226e-03	AVAILABLE			PASS	cross. time, rise edge of 'p8_0_pad' PAD @ 2.2V
0	[---	tm	1.000e-04	AVAILABLE			PASS	++ defined time
0	[---	tc1	1.826e-03	AVAILABLE			PASS	measure time tcrl + tm
0	[---	tc2	2.326e-03	AVAILABLE			PASS	measure time tcrl + tm
1	[REG]	r_config_h	80000000	80000000			PASS	(HEX val.) LPCOMP Configuration register, measure @ tml
2	[REG]	r_cmp0_ctrl_h	3	3			PASS	(HEX val.) comparator 0 control register, measure @ tml
3	[REG]	r_cmp1_ctrl_h	3	3			PASS	(HEX val.) comparator 1 control register, measure @ tml
4	[REG]	r_cmp0_sw_h	11	11			PASS	(HEX val.) comparator 0 switch control register, meas. @ tml
5	[REG]	r_cmp1_sw_h	11	11			PASS	(HEX val.) comparator 1 switch control register, meas. @ tml
6	[ANA]	i_iref	2.496e-07		2.475e-07	2.525e-07	PASS	'iref_250n_flow' ref. current of 'u_mxs40ivcomp_top' block, meas. @ tml
7	[ANA]	v1_wk0	2.210e-09		-1.000e-02	1.000e-02	PASS	'out_wk0' output voltage of 'u_mxs40ivcomp_top' block measured @ tml
8	[ANA]	v2_wk0	2.700e+00		2.673e+00	2.727e+00	PASS	'out_wk0' output voltage of 'u_mxs40ivcomp_top' block measured @ tm2
9	[ANA]	v1_wk1	2.700e+00		2.673e+00	2.727e+00	PASS	'out_wk1' output voltage of 'u_mxs40ivcomp_top' block measured @ tml
10	[ANA]	v2_wk1	3.370e-09		-1.000e-02	1.000e-02	PASS	'out_wk1' output voltage of 'u_mxs40ivcomp_top' block measured @ tm2
11	[PWR]	vcc*	1.100e+00		1.089e+00	1.111e+00	PASS	LV 'vccact' voltage value of 'u_mxs40ivcomp_top' block, measured @ tml
12	[PWR]	vcc*	1.100e+00		1.089e+00	1.111e+00	PASS	LV 'vccdpalp' voltage value of 'u_mxs40ivcomp_top' block, measured @ tml
13	[PWR]	vcc*	1.100e+00		1.089e+00	1.111e+00	PASS	LV 'vccret' voltage value of

Defining line index

Wrap by words

Merge

AMS

Statistics

Overview

Title page

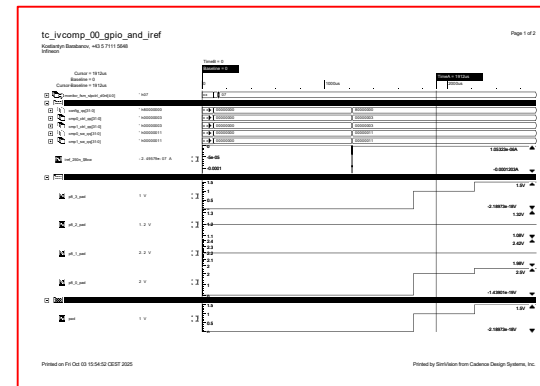
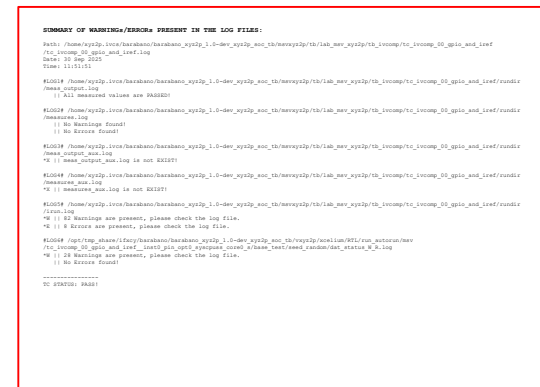
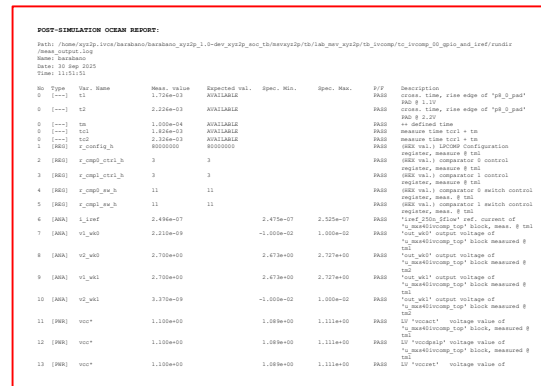
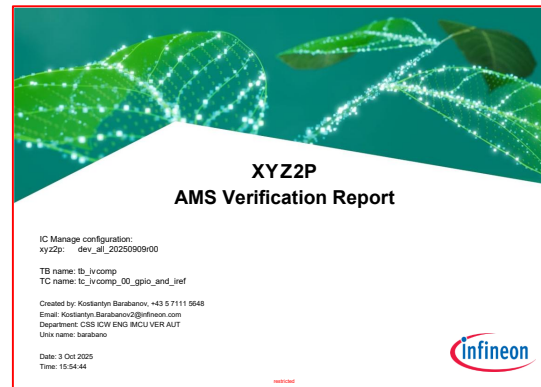
EPS-to-PDF

LOG-to-PDF

Merge

Summary

Q&A



Summary



Problems

- A recurring challenge in AMS SoC verification is the repetitive, time-consuming documentation workload, which slows down efficiency and undermines consistency.
- Most engineers contribute to documentation, but they don't particularly enjoy doing it.



Tips to tool vendors

- Integrate **epstopdf** utility into waveform viewers
- Enable background EPS file creation



Highlights

-  One-Click PDF Generation
Seamless report creation from simulation and log data
-  Saving Time = Saving Money
Reduces manual effort, accelerates documentation, boosts engineering efficiency
-  Unified Document Format
Consistent layout across title, waveform, and log sections
-  Integrated Sharing via SVN & Jama
Easy distribution and traceable project linkage
-  Customization Options
Tailor headers, fonts, and formatting to project or team needs
-  Full Traceability
From simulation inputs to final report—every step is documented

Questions



Q&A Session



Merge (BKP)



XYZ2P AMS Verification Report

IC Manage configuration:
xyz2p: dev_all_20250909r00

TB name: tb_ivcomp
TC name: tc_ivcomp_00_gpio_and_iref

Created by: Kostiantyn Barabanov, +43 5 7111 5648
Email: Kostiantyn.Barabanov2@infineon.com
Department: CSS ICW ENG IMCU VER AUT
Unix name: barabano

Date: 3 Oct 2025
Time: 15:54:44

restricted



Merge (BKP)



SUMMARY OF WARNINGS/ERRORS PRESENT IN THE LOG FILES:

```
Path: /home/xyz2p.ivcs/barabano/barabano_xyz2p_1.0-dev_xyz2p_soc_tb/msvxyz2p/tb/lab_msv_xyz2p/tb_ivcomp/tc_ivcomp_00_gpio_and_iref
/tc_ivcomp_00_gpio_and_iref.log
Date: 30 Sep 2025
Time: 11:51:51

#LOG1# /home/xyz2p.ivcs/barabano/barabano_xyz2p_1.0-dev_xyz2p_soc_tb/msvxyz2p/tb/lab_msv_xyz2p/tb_ivcomp/tc_ivcomp_00_gpio_and_iref/rundir
/meas_output.log
|| All measured values are PASSED!

#LOG2# /home/xyz2p.ivcs/barabano/barabano_xyz2p_1.0-dev_xyz2p_soc_tb/msvxyz2p/tb/lab_msv_xyz2p/tb_ivcomp/tc_ivcomp_00_gpio_and_iref/rundir
/measures.log
|| No Warnings found!
|| No Errors found!

#LOG3# /home/xyz2p.ivcs/barabano/barabano_xyz2p_1.0-dev_xyz2p_soc_tb/msvxyz2p/tb/lab_msv_xyz2p/tb_ivcomp/tc_ivcomp_00_gpio_and_iref/rundir
/meas_output_aux.log
*X || meas_output_aux.log is not EXIST!

#LOG4# /home/xyz2p.ivcs/barabano/barabano_xyz2p_1.0-dev_xyz2p_soc_tb/msvxyz2p/tb/lab_msv_xyz2p/tb_ivcomp/tc_ivcomp_00_gpio_and_iref/rundir
/measures_aux.log
*X || measures_aux.log is not EXIST!

#LOG5# /home/xyz2p.ivcs/barabano/barabano_xyz2p_1.0-dev_xyz2p_soc_tb/msvxyz2p/tb/lab_msv_xyz2p/tb_ivcomp/tc_ivcomp_00_gpio_and_iref/rundir
/irun.log
*W || 82 Warnings are present, please check the log file.
*E || 8 Errors are present, please check the log file.

#LOG6# /opt/tmp_share/ifxcy/barabano/barabano_xyz2p_1.0-dev_xyz2p_soc_tb/vxyz2p/xcelium/RTL/run_autorun/msv
/tc_ivcomp_00_gpio_and_iref_inst0_pin_opt0_syscpuss_core0_s/base_test/seed_random/dat_status_W_R.log
*W || 28 Warnings are present, please check the log file.
|| No Errors found!

-----
TC STATUS: PASS!
```

Merge (BKP)

AMS Statistics Overview Title page EPS-to-PDF LOG-to-PDF Merge Summary Q&A

POST-SIMULATION OCEAN REPORT:

Path: /home/xyz2p.ivcs/barabano/barabano_xyz2p_1.0-dev_xyz2p_soc_tb/msvxyz2p/tb/lab_msv_xyz2p/tb_ivcomp/tc_ivcomp_00_gpio_and_iref/rundir
/meas_output.log
Name: barabano
Date: 30 Sep 2025
Time: 11:51:51

No	Type	Var. Name	Meas. value	Expected val.	Spec. Min.	Spec. Max.	P/F	Description
0	[---]	t1	1.726e-03	AVAILABLE			PASS	cross. time, rise edge of 'p8_0_pad' PAD @ 1.1V
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5	[REG]	r_cmp1_sw_h	11	11			PASS	(HEX val.) comparator 1 switch control register, meas. @ tml
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8	[ANA]	v2_wk0	2.700e+00		2.673e+00	2.727e+00	PASS	'out_wk0' output voltage of 'u_mxs40ivcomp_top' block measured @ tm2
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10	[ANA]	v2_wk1	3.370e-09		-1.000e-02	1.000e-02	PASS	'out_wk1' output voltage of 'u_mxs40ivcomp_top' block measured @ tm2
11	[PWR]	vcc*	1.100e+00		1.089e+00	1.111e+00	PASS	LV 'vccact' voltage value of 'u_mxs40ivcomp_top' block, measured @ tml
12	[PWR]	vcc*	1.100e+00		1.089e+00	1.111e+00	PASS	LV 'vccdspalp' voltage value of 'u_mxs40ivcomp_top' block, measured @ tml
13	[PWR]	vcc*	1.100e+00		1.089e+00	1.111e+00	PASS	LV 'vccret' voltage value of

Merge (BKP)

AMS Statistics Overview Title page EPS-to-PDF LOG-to-PDF **Merge** Summary Q&A

