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SV_LUT: A SystemVerilog Look Up Table package for developing complex AMS Real Number Modeling

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Agenda

- Need for Look Up Table (LUT) in SV-RNM modeling
- Challenges associated with LUT based AMS modeling and Our solution
- LUT Structure
- Overview of SV_LUT_PKG
 - Methods for defining and Population of LUT
 - Method for fetching data from LUT
- Application of LUT package in AMS modeling
 - RNM modeling demonstration of simple PTAT core
- Advanced use cases of SV_LUT_PKG
- Conclusion and Future Works

Need for Look Up Table (LUT) in SV-RNM modeling

- SV-RNM modeling of analog system is getting popular for Mixed Signal Verification
- Modeling process of Complex behavior of Analog Systems like Solar Cell, Ptat core, etc. involves,
 - Utilization of Look Up Tables (LUT)
 - Data points are exported from waveform dump on external simulators,
 - Most used file format for dumping and exporting data: Comma Separated Value (CSV)
- VerilogAMS supports .tbl format for LUT using \$table_model()
- SystemVerilog do not have any built-in support for LUT

Challenges associated with LUT based AMS modeling

- Mechanism for addressing unknown data points between two known data points
- Real number-based index value matching mechanism for fetching data
- Performance of LUT should have minimum impact on AMS model performance
- Integration process of LUT in SV-RNM flow should be simple and require minimum code

Our Solution

- A SystemVerilog package named “sv_lut_pkg” which,
 - Defines a data type for representing the LUT
 - Parse the CSV file and map the data points into the LUT.
 - Search the populated LUT and fetch value based on index and data column definition and index column value
 - Apply reasonable tolerance while fetching values based on real number indexes
 - Support interpolation (Linear and cubic spline) and extrapolation
- Simple to integrate in traditional SV-RNM flow with minimum lines of codes

Overview of SV_LUT_PKG

LUT Structure

- LUT is defined as a SV struct datatype that contains,
 - Two SV dynamic array/queue for,
 - Storing CSV column headers i.e. col_name[\$]
 - Storing CSV Column data i.e. val[\$][\$]
- For every row x and column y,
 - val[x][0] represents index column value of CSV file
 - val[x][y], where y>0 holds the data points of the CSV column

```
typedef struct {
    string col_name[$];
    real val[$][$];
} lut_struct_t;
```

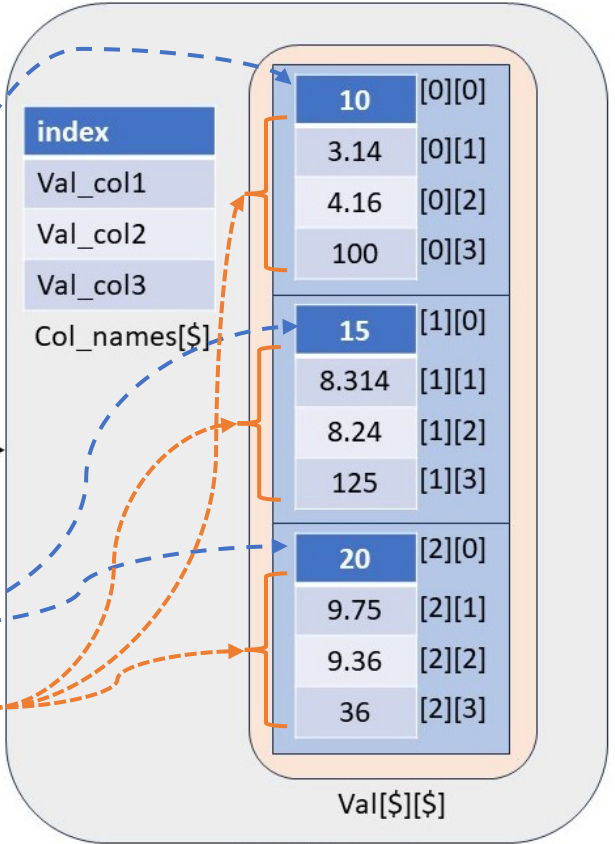
	index	Val_col1	Val_col2	Val_col3
1	10	3.14	4.16	100
2	15	8.314	8.24	125
3	20	9.75	9.36	36

CSV File

CSV Parsing

Index

Data points



SV_LUT struct

Overview of SV_LUT_PKG

- Defined as SV struct data type containing two dynamic arrays/queue members
- Pre-processor macros are used for defining, populating and fetching value from LUT.
 - `POP_LUT() macro defines and populates the LUT
 - `SV_LUT() macro fetches value from LUT
- These pre-processor macros implement all the logic and functions required to define, populate and fetch value from LUT

Methods for Defining and Population of LUT

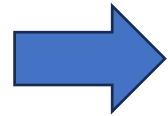
```
testbench.sv  sv_lut_pkg.sv
```

```

1 package sv_lut_pkg;
2   typedef struct {
3     string col_name[$];
4     real val[$][$];
5   } lut_struct_t;
6
7   // create lut and populate it from csv file
8   // parameters: lut_name = name of lut variable
9   //               csv_file = full path to csv file
10  `define POP_LUT(lut_name, csv_file) \
11    lut_struct_t lut_name; \
12    int `lut_name`_x_indx_col_num = 0; \
13    real `lut_name`_x_indx_val_tol = 0.001; \
14    initial begin \
15      assert(read_csv(`"csv_file`", lut_name)); \
16    end

```

Name of the LUT

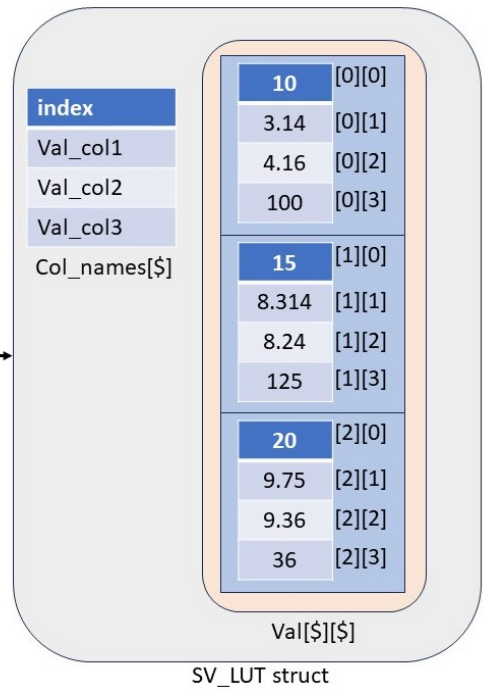


index	Val_col1	Val_col2	Val_col3
1	10	3.14	4.16
2	15	8.314	8.24
3	20	9.75	9.36

CSV File

CSV Parsing

Path to CSV File



SV_LUT struct

Method for fetching data from LUT

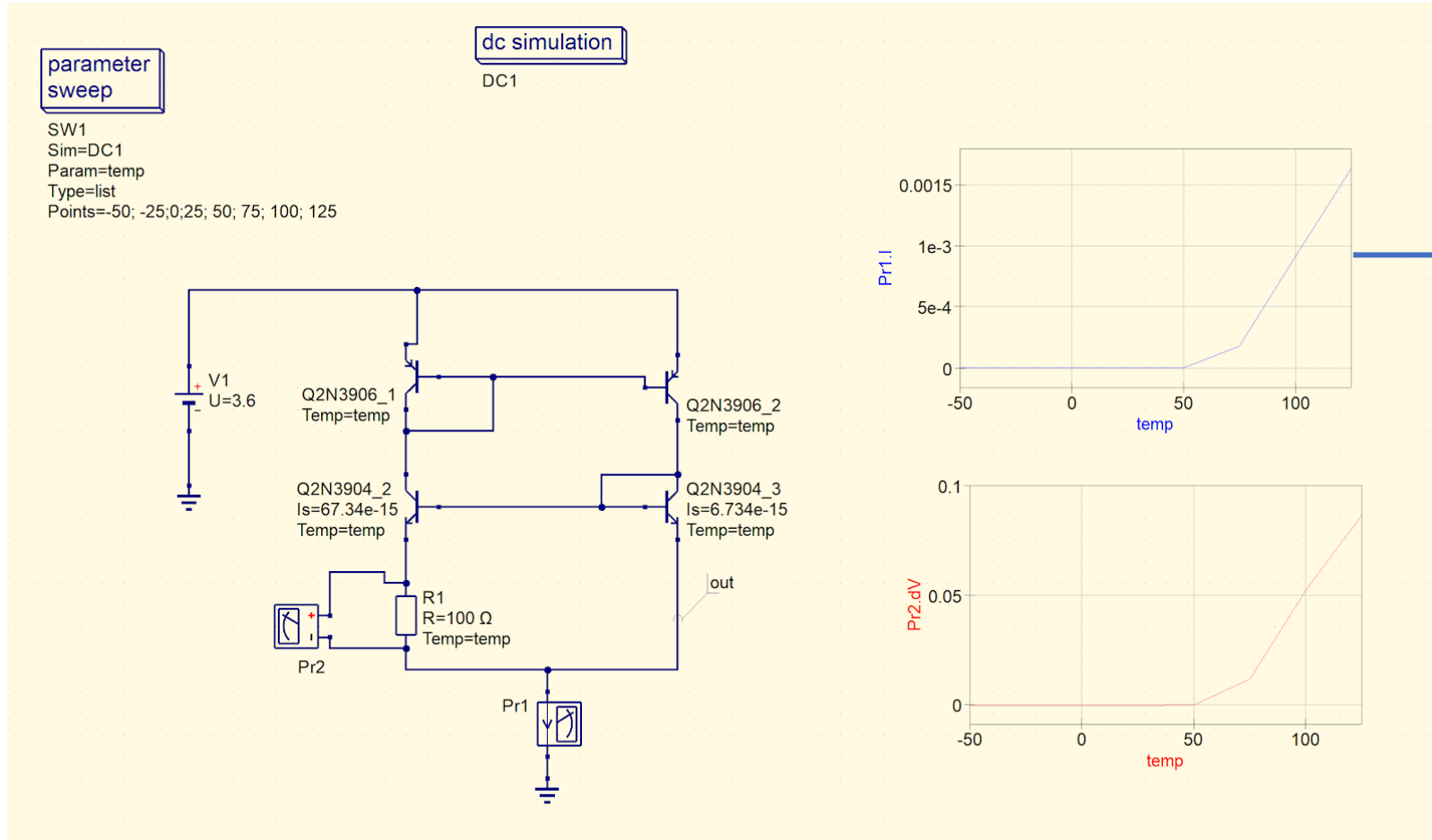
```
testbench.sv  sv_lut_pkg.sv x +
79
80 // main lut function
81 // parameters:
82 //   x_indx: (string) lookup index of look up table
83 //   lut_name: ()
84 //   y_indx: (string) return index selector for LUT
85 //   interpolation_type: (string) L: linear interpolatrion, C: cuble spline interpolation
86 //   x_indx_col_num: (int) column number for x_index
87 `define SV_LUT(x_indx_val, lut_name, y_indx, interpolation_type) \
88 lut_fetch_val(x_indx_val, lut_name, y_indx, interpolation_type, ``lut_name``_x_indx_col_num,
89 ``lut_name``_x_indx_val_tol);
90
91 function real lut_fetch_val(real x_indx, lut_struct_t lut, int y_indx_col, string intrpol_type,
92 int x_indx_col_num=0, real x_indx_val_reltol=0.01);
```

Integration of SV_LUT_PKG in SV-RNM flow

Application of LUT package in AMS modeling

- Demonstrated with SV-RNM modeling flow for Simple PTAT core design
 - Opensource qucsstudio is used for designing the schematic
 - Spice simulation waveform is dumped as CSV file
- SV-RNM model of ptat core is developed with sv_lut_pkg to demonstrate the modeling flow and capabilities

Schematic View of the PTAT core and CSV



parameter sweep

SW1
 Sim=DC1
 Param=temp
 Type=list
 Points=-50; -25; 0; 25; 50; 75; 100; 125

dc simulation

DC1

	A	B
1	temperature (°C)	r Pr1.I (A)
2	-50	4.85E-13
3	-25	1.10E-11
4	0	2.10E-10
5	25	4.42E-09
6	50	5.85E-07
7	75	0.000176578
8	100	0.000911202
9	125	0.00163012
10		

CSV dump

RNM modeling flow with sv_lut_pkg

- LUT define and population
 - lut_name: ptat_lut
 - csv_file_path: ptat_sim.csv

- Value fetching from LUT
 - x_indx_val: temp
 - lut_name: ptat_lut
 - y_indx: 1 i.e. col 1 (rPr1.l)
 - Interpolation_type: "1L" i.e. linear

```
design sv | ptat_sim.csv x | EE_pkg.sv x | multi_lut_design.sv x | sample_csv.csv x +
1 // RNM model of ptat core
2 `timescale 1s/1ps
3 `include "sv_lut_pkg.sv"
4
5 module ptat(
6     input real vdd,
7     input real gnd,
8     output real ptat_out
9 );
10 import sv_lut_pkg:: *;
11
12 // real vdd;
13 // real gnd;
14 // real ptat_out;
15
16 real temp; // temperature in degree c
17
18 // create lut and populate data from csv file
19 `POP_LUT(ptat_lut, ptat_sim.csv)
20
21 always @(temp) begin
22     // `SV_LUT("input_index_name"/Int(input_index_number), "csv_file_name",
23     "output_index_name"/Int(output_index_number), "interpolation type = linear")
24     ptat_out = `SV_LUT(temp, ptat_lut, 1, "1L");
25 end
26 endmodule
```

SV testbench

- Design is simulated from -50°C to 125°C
- Incremental Step: 5°C
- Simulated using Cadence Xcelium Suite

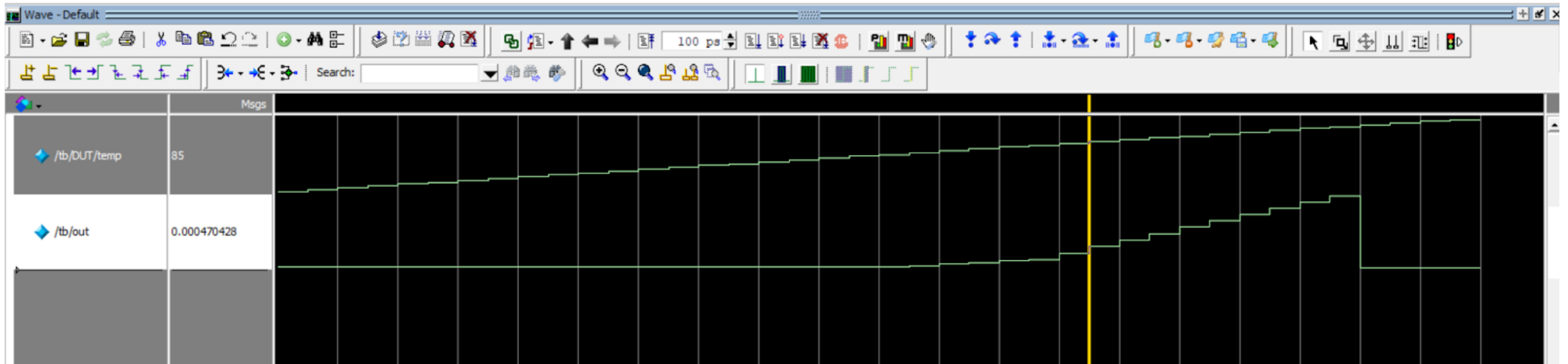
```
testbench.sv  sv_lut_pkg.sv  *  +
5  module tb;
6
7  import sv_lut_pkg::*;
8
9  real vdd, gnd, out;
10
11  ptat DUT(
12    .vdd(vdd),
13    .gnd(gnd),
14    .ptat_out(out)
15  );
16
17  initial begin
18    for(int temp = -50; temp<150; temp=temp+5) begin
19      $display("setting temp: %0d", temp);
20      DUT.temp = temp;
21      # 100us;
22      $display("v(out):%0e", out);
23    end
24  end
25
26  initial begin
27    vdd = 3;
28    gnd = 0;
29  end
30
31  initial begin
32    $dumpvars(0, DUT);
33    $dumpfile("ptat_sim.vcd");
34  end
35 endmodule
```

Simulation Results

- Data points in CSV File
 - -50°C,
 - -25°C,
 - 0°C,
 - 25°C,
 - 50°C,
 - 75°C,
 - 100°C,
 - 125°C
- Rest of the data points are linearly interpolated

# V(out):4.850000e-13	# Setting temp: 10	# Setting temp: 65
# Setting temp: -45	# V(out):1.894000e-09	# V(out):1.061808e-04
# V(out):2.588000e-12	# Setting temp: 15	# Setting temp: 70
# Setting temp: -40	# V(out):2.736000e-09	# V(out):1.413794e-04
# V(out):4.691000e-12	# Setting temp: 20	# Setting temp: 75
# Setting temp: -35	# V(out):3.578000e-09	# V(out):1.765780e-04
# V(out):6.794000e-12	# Setting temp: 25	# Setting temp: 80
# Setting temp: -30	# V(out):4.420000e-09	# V(out):3.235028e-04
# V(out):8.897000e-12	# Setting temp: 30	# Setting temp: 85
# Setting temp: -25	# V(out):1.205360e-07	# V(out):4.704276e-04
# V(out):1.100000e-11	# Setting temp: 35	# Setting temp: 90
# Setting temp: -20	# V(out):2.366520e-07	# V(out):6.173524e-04
# V(out):5.080000e-11	# Setting temp: 40	# Setting temp: 95
# Setting temp: -15	# V(out):3.527680e-07	# V(out):7.642772e-04
# V(out):9.060000e-11	# Setting temp: 45	# Setting temp: 100
# Setting temp: -10	# V(out):4.688840e-07	# V(out):9.112020e-04
# V(out):1.304000e-10	# Setting temp: 50	# Setting temp: 105
# Setting temp: -5	# V(out):5.850000e-07	# V(out):1.054986e-03
# V(out):1.702000e-10	# Setting temp: 55	# Setting temp: 110
# Setting temp: 0	# V(out):3.578360e-05	# V(out):1.198769e-03
# V(out):2.100000e-10	# Setting temp: 60	# Setting temp: 115
# Setting temp: 5	# V(out):7.098220e-05	# V(out):1.342553e-03
# V(out):1.052000e-09		# Setting temp: 120
		# V(out):1.486336e-03

Simulation Waveform Results



Advanced Use cases of SV_LUT_PKG

Advanced Use cases of SV_LUT_PKG

- Use of Multiple LUTs in a single model
 - Lut_name parameter should be different in POP_LUT

```
design.sv  ptat_sim.csv x  EE_pkg.sv x  multi_lut_design.sv x  sample_csv.csv x +
1 `timescale 1s/1ps
2 `include "sv_lut_pkg.sv"
3
4 module multi_lut(
5     input real vdd,
6     input real gnd,
7     output real out_V_1,
8     output real out_V_2,
9     output real out_V_3
10    );
11 import sv_lut_pkg::*;
12 real temp;
13
14 // create lut and populate data from csv file
15 `POP_LUT(ptat_lut1, ptat_sim.csv)
16 `POP_LUT(ptat_lut2, ptat_sim2.csv)
17 `POP_LUT(ptat_lut3, ptat_sim3.csv)
18
19 always @(temp) begin
20     // `SV_LUT("input_index_name"/Int(input_index_number), "csv_file_name",
21     "output_index_name"/Int(output_index_number), "interpolation type = linear")
22     out_V_1 = `SV_LUT(temp, ptat_lut1, 1, "1L");
23 end
24 always @(temp) begin
25     // `SV_LUT("input_index_name"/Int(input_index_number), "csv_file_name",
26     "output_index_name"/Int(output_index_number), "interpolation type = linear")
27     out_V_2 = `SV_LUT(temp, ptat_lut2, 1, "1L");
28 end
29 always @(temp) begin
30     // `SV_LUT("input_index_name"/Int(input_index_number), "csv_file_name",
31     "output_index_name"/Int(output_index_number), "interpolation type = linear")
32     out_V_3 = `SV_LUT(temp, ptat_lut3, 1, "1L");
33 end
34 endmodule
```

Different lut_name parameter

Advanced Use cases of SV_LUT_PKG Cont..

- Different y_idx value to fetch data from a single CSV against a single index column
 - In the y_idx value in the SV_LUT() macro should be unique

```
design.sv  ptat_sim.csv x  EE_pkg.sv x  multi_lut_design.sv x  sample_csv.csv x  single_lut_multi_op x +
1  `timescale 1s/1ps
2  `include "sv_lut_pkg.sv"
3
4  module single_lut_multi_op(
5      input real vdd,
6      input real gnd,
7      output real out_V_1,
8      output real out_V_2,
9      output real out_V_3
10     );
11  import sv_lut_pkg::*;
12  real temp;
13
14  // create lut and populate data from csv file
15  `POP_LUT(ptat_lut, ptat_sim.csv)
16
17  always @(temp) begin
18      // `SV_LUT("input_index_name"/Int(input_index_number), "csv_file_name",
19      // "output_index_name"/Int(output_index_number), "interpolation type = linear")
20      out_V_1 = `SV_LUT(temp, ptat_lut, 1, "1L");
21      out_V_2 = `SV_LUT(temp, ptat_lut, 2, "1L");
22      out_V_3 = `SV_LUT(temp, ptat_lut, 3, "1L");
23  end
24  endmodule
```

Different y_idx value

Conclusion and Future Works

Conclusion and Future Works

- Conclusion
 - Modeling is effortless
 - Most of the LUT design complexity is transparent to verification engineers
 - Easy to integrate with traditional RNM modeling flow
- Limitations
 - Maps single index to single data column
 - Mapping of multiple inputs for fetching corresponding output not supported
 - No data validation in CSV parser
 - Slow data processing for huge CSV files

Any Questions



Thank You

