

Achieving Full Liveness Proofs via a Systematic Assume-Guarantee Approach and Iterative Helper Generation

Stella Simic and Karthik Baddam

Qualcomm Technologies





Acknowledgements

Thanks to our colleagues Nitish Sharma, Nishanth Narisetty for feedback, and Abhishek Datta for guidance and encouraging us to publish this work . Thanks to DVCON organizers, volunteers , anonymous reviewers and our paper's shepherd for feedback.

Qualcom