#### 2025 DESIGN AND VERIFICATION™ DVCCONFERENCE AND EXHIBITION

#### UNITED STATES

SAN JOSE, CA, USA FEBRUARY 24-27, 2025

Time-Travel Debugging for High-Level Synthesis Greg Law, Undo

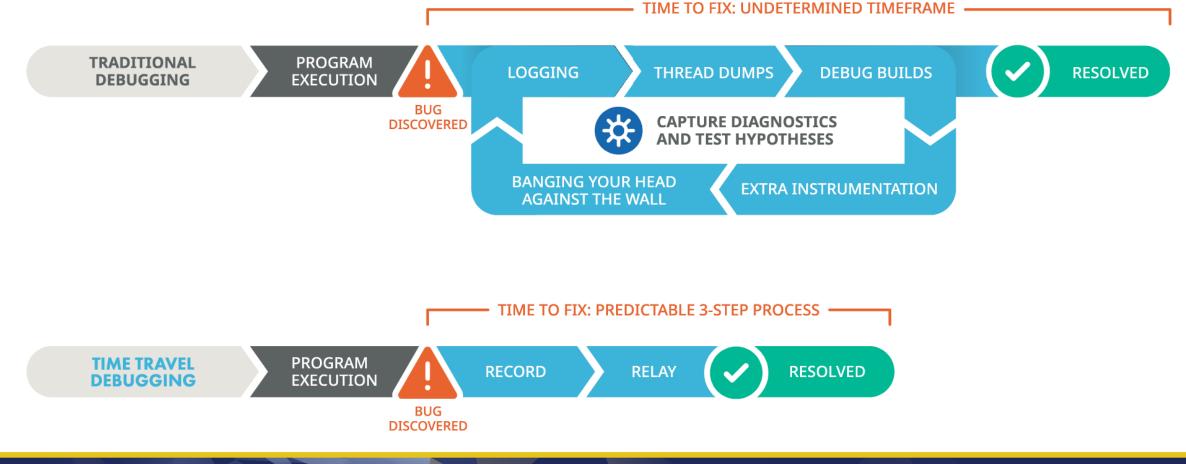
Jonathan Bonsor-Matthews, LightBlue Logic

LIGHTBLUE LOGIC





## Why Time-Travel Debugging?





## Time-Travel Debugging Commands

Command	Forward Function	<b>Reverse Function</b>
Step	Step into next function	Step into previous function
Next	Execute next line	Execute previous line
Finish	Return from function	Execute until just before function called
Break (condition)	Stop execution at location in code (optional condition)	Same
Watch	Stop execution when certain variable/memory changes	Same
Continue	Execute forwards	Execute backwards
Last	-	Jump to last time data changes





### **Best Practice**

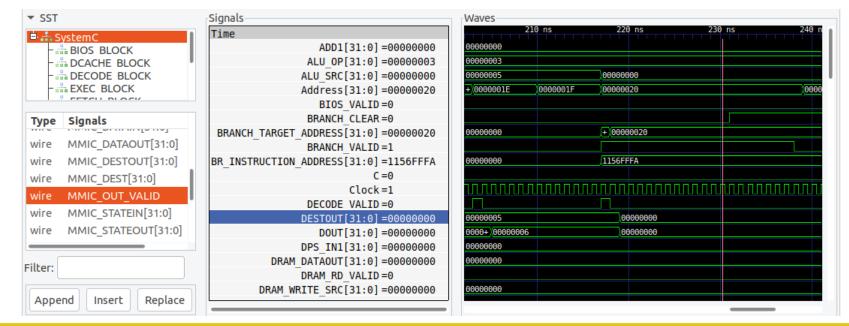
- Build time-travel debugging into regression flow
  - Record the failing test, simply replay the recording
- Design for debug
  - Identify failures in the executable to use last command
  - Use assertions widely
  - Add intermediate variables, for conditional breakpoints
- Thread Fuzzing
  - Challenge mode to provoke concurrency issues (race conditions, deadlocks)





### Waveforms -> Debugger

- Extract waveforms from recording *without re-running*
- Click on transition to load same point in debugger







### Results

- Four times faster to find a bug
- Easy to follow data through complex designs
- Helps to understand large codebases
- Frees engineers' time to **shift left**:
  - Bring-up more application layers before tape-out
  - Explore alternative **PPA optimizations**
  - Improve coverage closure
  - Improve **stability** (by not ignoring the challenging bugs)
  - Reduce time-to-market







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# Questions?

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