



Day 2 TPC Updates

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TPC Chair

Architecture to Analytics **A2A**

DVCON 2025

September 10-11, 2025 | Radisson Blu, Marathahalli, Bangalore

Architecture to Analytics (A2A)

- Design architecture for AI computation !
- Verification in AI Era !!
- Formal and Future
- Addressing Verification challenges with increasing Design Complexity
- Opportunities to shrink Pre-Post silicon validation cycles!!

Design & Architecture Processor / RISC-V Mixed Signal/Low Power Design & Verification	4 Sessions
Static & Formal Verification Design Verification <ul style="list-style-type: none">● AI & ML● SV & UVM● 3D IC/Chiplet	9 Sessions
Emulation & Prototyping Post Silicon Validation	2 Sessions

Technical Program Committee - Updates

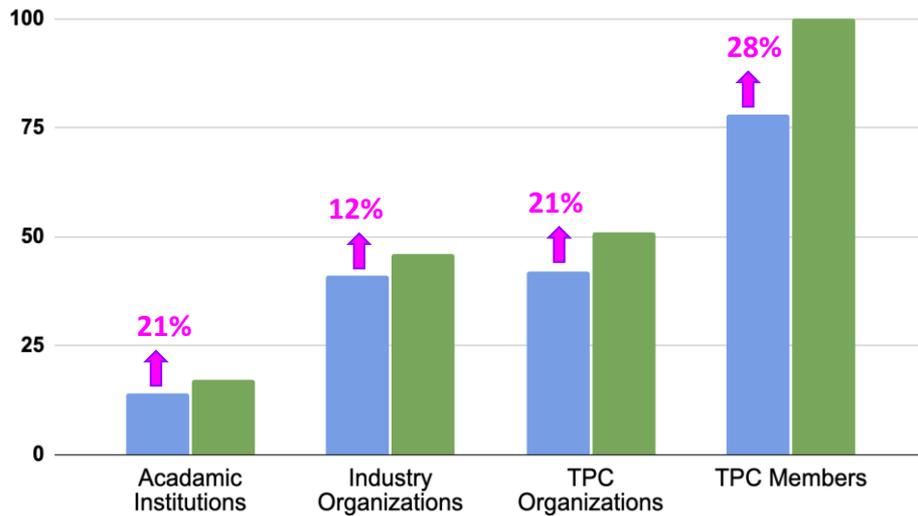


- DVCon India 2025 - 9 Categories
 - Architecture to Analytics (A2A), AI/ML as separate Track
- **234** Submissions from **716** authors taken for TPC review
 - 46 Organizations & 17 Academic Institutions participated
- **100** TPC members from **51** Organizations participated in TPC reviews
- Total 45 papers with Rich Technical contents
 - 5 Parallel Tracks and 15 Sessions
- Total **24** posters
- Best Papers & Posters Awards
 - Thanks to Awards Sponsors

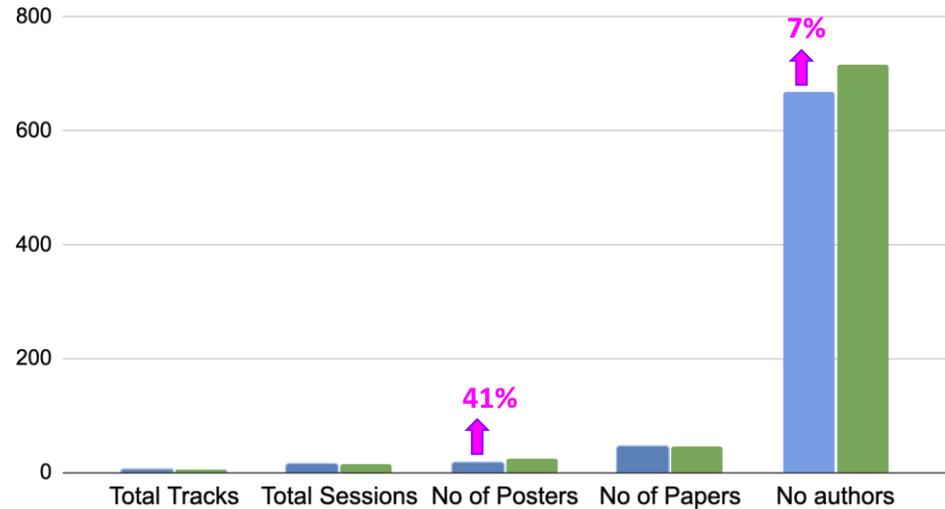
DVCon India 2025 vs 2024 Comparison



■ Y'2024 ■ Y'2025



■ Y'2024 ■ Y'2025



Industry/Academia Participation



PES University	Agnisys	NVIDIA
Rajiv Gandhi University of knowledge technologies,RK Valley	Alpha-Numero	NXP Semiconductors
VIT, Vellore	AMD	Open Source Developer, mBits Github Community
Sarvajani College of Engineering and Technology, Surat	Analog Devices	Paradigm Works
Vellore Institute of Technology,chennai	ARM	Praakrit Energy and Technolgies
EASWARI ENGINEERING COLLEGE	AsFigo Technologies	Qualcomm India Pvt Ltd
Sri Eshwar College of Engineering	ASHLING	Renesas Electronics
Vellore Institute of Technology	Astera labs	Samsung Semiconductor India Research
IIT Goa	Baya Systems	SanDisk India Design Center Pvt Ltd
MNNIT Allahabad	Cadence Design Systems India	Scaledge India Private Limited
KLE Technological University	Cadence US	Siemens EDA
SR University	Circuitsutra	SmartSoC Solutions Pvt Ltd.
PSG College of Technology	Google	ST Microelectronics
GLA UNIVERSITY	Global Foundaries	Synopsys Inc
Galgotia university	Infineon Technologies GmbH Dresden, Germany	TCS
S V National Institute of Technology, Surat	Infineon Technologies India pvt LTD	Tenstorrent
Sahrdaya College Of Engineering And Technology	INTEL	Thales
	Marvell India Pvt. Ltd.	Truechip
	Marvell Technology India Ltd	Vayavya Labs Pvt. Ltd.
	META	Vayavya Labs Pvt. Ltd.
	Micron Technology	VerifWorks Private Limited
	Microsoft	Verikwest
	MiraFra Software Technologies Pvt Ltd	Western Digital

46 Organizations & 17 Academic Institutions participated in paper submissions
12% & 21% increase in Industry & Academic participation compared with 2024

TPC Chairs



TPC Chair
Karthikeyan Subramanian
Google



TPC Co-Chair
Girish Marudwar
Synopsys



TPC Vice-Chair
Shreya Dasgupta
Analog Devices

Track Chairs



**Sandeep
Sreenivasa**
Qualcomm



Sriram KS
IBM



Sundar PH
SanDisk



Badri Narayanan
Intel

TPC Members

Big Thanks for Meticulous Planning & Organizing



TPC Members	Company	TPC Members	Company	TPC Members	Company	TPC Members	Company
Karthkeyan S	Google	Srobona Mitra	Qualcomm	Aravinda Thimmapuram	Circuitsutra	Anupam Bakshi	Agnisys, Inc.
Shreya Dasgupta	Analog Devices	Subhra Kanti Das	Thales	Kavish Seth	Synopsys	Deepak Gupta	Cappgemini
Narasimha Babu	Synopsys	Saurabh Agarwal	Truechip	Prayline Rajabai C	VIT	Nitin Kishore	Truechip
Nitin Neralkar	Qualcomm	Priya Ananthkrishnan	Miraфра	Debabrata Chatterjee	Intel	Dineshkumar Selvaraj	Infineon
Prasanna Venkatesan	einfochips	Garima Srivastava	Samsung	Sudhakar Bansal	Samsung	Pranav Joshi	INTEL
Girish	Synopsys	Manu Chopra	Cadence	Ashwani Aggarwal	Aarohi	Chakrapani Rayadurgam	IBM
Pradeep Vemuri	AMD	Sitaram Banda	Renesas	Ajeetha Kumari V	Asfigo	Vishwanath Sundararaman	AMD
Bhushan Safi	Microsoft	Jagannadha Naidu	VIT	Sindu Thomas	IBM	Yogish Sekhar	Independent
Ram Babu Penugonda	Google	Abhijit Naik	Vayavya Labs	Suraj Kamat	ARM	Sundar Ph	Western Digital
Suresh Vasu	Intel India Pvt Ltd	Srinivasan V	Verifworks	Sourabh Jain	ISRO	Ashish Mathur	NXP
Shekhar Jha	Analog Devices	Suman Krishna	Micron	Harsh Setia	Samsung	Gerardo Nahum	Siemens EDA
Sandeep Sreenivasa	Qualcomm	Dr Shylashree N	R.N.S Institute	Ipsita Biswas Mahapatra	Miraфра	Pradeep Cr	Ausdia
Aparna Mohan	Groq	Raghavendra J N	Microsoft	Atul Sharma	Siemens EDA	Mustufa Kanchwala	Siemens EDA
Pardeep Kumar	Micron	Saurabh Kasat	Microsoft	Manali Mondal	Analog Devices	Ray Varghese	Synopsys
Nitin Ahuja	Google	Bathri Narayanan S	Intel	Sushma Sambatur	Global Foundries	Pankaj Chhajjer	Alpha-Numero
Vichal Verma	Intel	Ashok Chandran	Analog Devices	Pankaj Singh	Synopsys	Amit Kumar Jain	Qualcomm
Sandeep Korrapati	IBM	Vineet Tandon	Microchip	Mark Burton	Qualcomm	Sharath Kashyap	Intel Corporation
Sudeep Mondal	Synopsys	Anoop Oruvingal	Miraфра	Harshavardhan D	Synopsys	Manish Makkar	SanDisk
Karthik Mohan	Marvell	Pradeep Marikundam	Infineon	Loganath Ramachandran	Verikwest	Sreehari Paladoddi	Onsemi
Gaurav Jain	Renesas	Subrahmanyam Mula	IIT, Palakkad	Naveen Desai	Tessolve	Baskar Rudhra Murthy	Marvell
Anil Deshpande	Samsung	Seru Srinivas	Marvell	Raj Mitra	Verikwest	Kasi Chunduri	Google
Dinesh Malviya	AMD	Robin Garg	Intel	Ipsita Tripathi	Qualcomm	Sunil Rao Kashide	Samsung
Pandithurai Sangaiyah	Google	Prateek Sikka	STMicroelectronics	Sriram KS	IBM	Venkatesh Kudumula	Synopsys
Muralikrishna Kukkupuni	Nvidia	Keshav Joshi	Siemens	Devender Khari	Agnisys	Shalini	Intel
Disha Puri	Synopsys	Vikas Sachdeva	Real Intent	Girish Baliga	Keysight	Pradeep Salla	Siemens EDA



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